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MOS FIELD EFFECT TRANSISTOR **2SK3386**

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3386 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Low On-state Resistance $R_{DS(on)1} = 21 \text{ m}\Omega \text{ MAX.}$ (VGs = 10 V, ID = 17 A) $R_{DS(on)2} = 36 \text{ m}\Omega \text{ MAX.}$ (VGs = 4.0 V, ID = 17 A)
- Low Ciss: Ciss = 2100 pF TYP.
- Built-in Gate Protection Diode
- TO-251/TO-252 package

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

| Drain to Source Voltage | Vdss | 60 | V |
|---|----------|-------------|----|
| Gate to Source Voltage | Vgss | ±20 | V |
| Drain Current (DC) | D(DC) | ±34 | А |
| Drain Current (Pulse) Note1 | D(pulse) | ±120 | А |
| Total Power Dissipation (Tc = 25° C) | Рт | 40 | W |
| Total Power Dissipation ($T_A = 25^{\circ}C$) | Рт | 1.0 | W |
| Channel Temperature | Tch | 150 | °C |
| Storage Temperature | Tstg | –55 to +150 | °C |
| Single Avalanche Current Note2 | las | 28 | А |
| Single Avalanche Energy Note2 | Eas | 78 | mJ |

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting $T_{ch} = 25^{\circ}C$, $V_{DD} = 30$ V, $R_G = 25 \Omega$, $V_{GS} = 20 \rightarrow 0$ V

ORDERING INFORMATION

| PART NUMBER | PACKAGE | |
|-------------|----------------|--|
| 2SK3386 | TO-251 (MP-3) | |
| 2SK3386-Z | TO-252 (MP-3Z) | |

(TO-251)



(TO-252)



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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

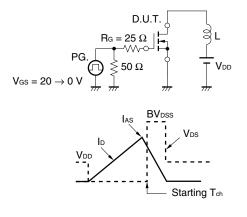
Downloaded from Arrow.com.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

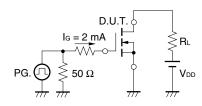
| CHARACTERISTICS | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--|-----------------|---|------|------|------|------|
| Zero Gate Voltage Drain Current | IDSS | Vds = 60 V, Vgs = 0 V | | | 10 | μA |
| Gate Leakage Current | lgss | $V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$ | | | ±10 | μA |
| Gate Cut-off Voltage | VGS(off) | V _{DS} = 10 V, I _D = 1 mA | 1.5 | 2.0 | 2.5 | V |
| Forward Transfer Admittance Note | y fs | Vds = 10 V, Id = 17 A | 10 | 19 | | S |
| Drain to Source On-state Resistance Note | RDS(on)1 | Vgs = 10 V, Id = 17 A | | 17 | 21 | mΩ |
| | RDS(on)2 | V _{GS} = 4.0 V, I _D = 17 A | | 25 | 36 | mΩ |
| Input Capacitance | Ciss | V _{DS} = 10 V | | 2100 | | pF |
| Output Capacitance | Coss | V _{GS} = 0 V | | 340 | | pF |
| Reverse Transfer Capacitance | Crss | f = 1 MHz | | 170 | | pF |
| Turn-on Delay Time | td(on) | Vdd = 30 V, Id = 17 A | | 32 | | ns |
| Rise Time | tr | Vgs = 10 V | | 310 | | ns |
| Turn-off Delay Time | td(off) | R _G = 10 Ω | | 98 | | ns |
| Fall Time | tr | | | 100 | | ns |
| Total Gate Charge | QG | V _{DD} = 48 V | | 39 | | nC |
| Gate to Source Charge | Q _{GS} | Vgs = 10 V | | 7.0 | | nC |
| Gate to Drain Charge | Qgd | ID = 34 A | | 12 | | nC |
| Body Diode Forward Voltage Note | VF(S-D) | IF = 34 A, VGS = 0 V | | 0.87 | | V |
| Reverse Recovery Time | trr | IF = 34 A, VGS = 0 V | | 46 | | ns |
| Reverse Recovery Charge | Qrr | di/dt = 100 A/ <i>µ</i> s | | 84 | | nC |

Note Pulsed

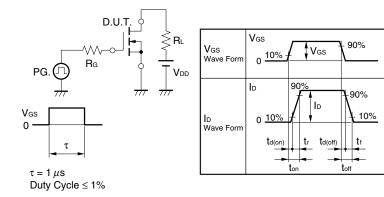
TEST CIRCUIT 1 AVALANCHE CAPABILITY



TEST CIRCUIT 3 GATE CHARGE

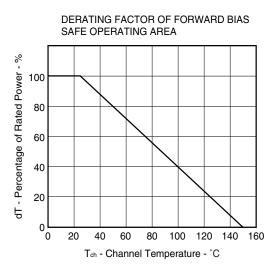


TEST CIRCUIT 2 SWITCHING TIME

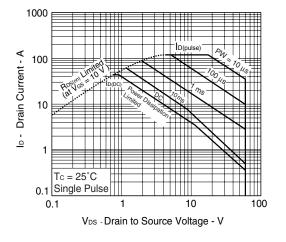


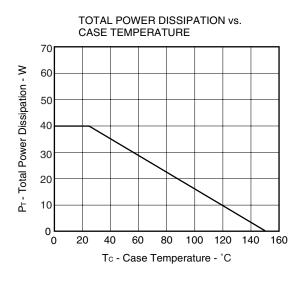
2

TYPICAL CHARACTERISTICS (TA = 25°C)

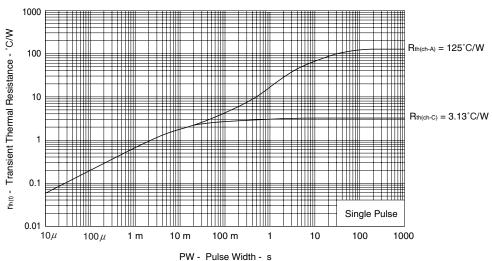


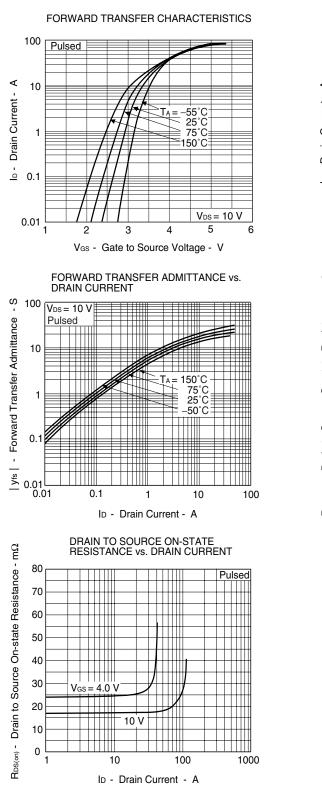


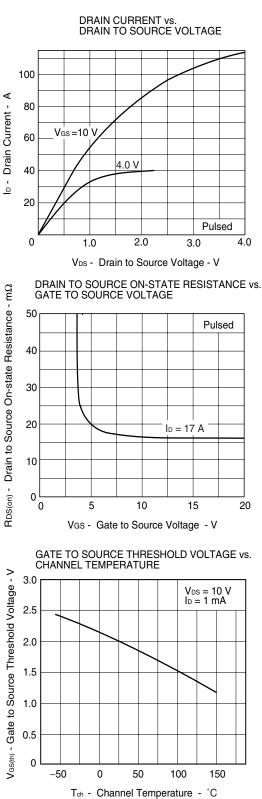




TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

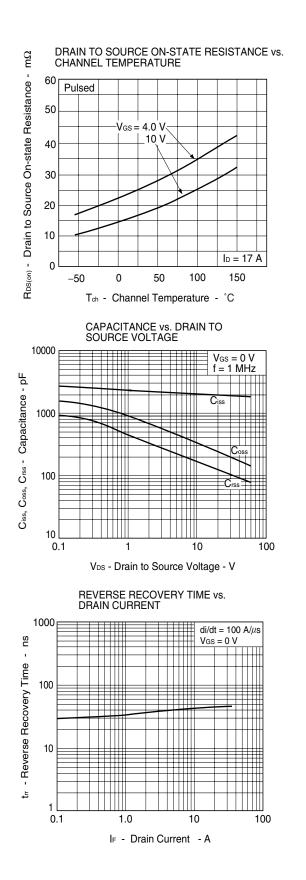


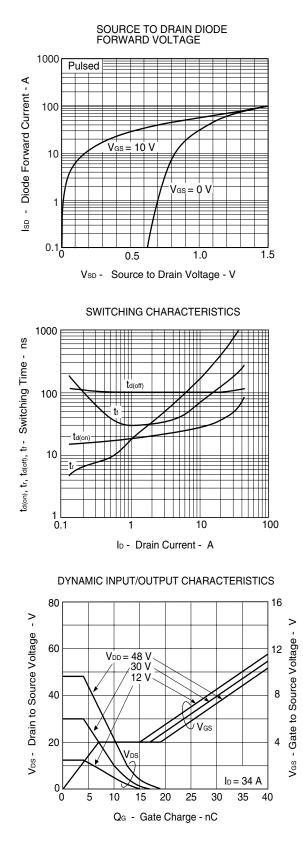




4







5

Vdd = 30 V

 $\mathsf{Ias} \le 28~\mathsf{A}$

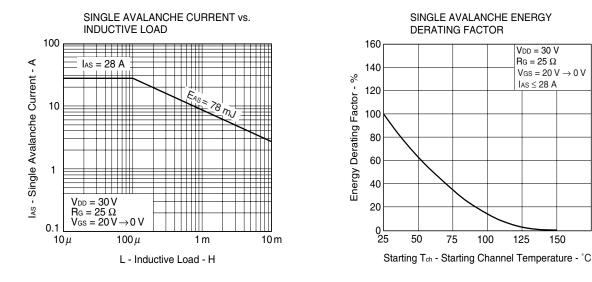
100

125

150

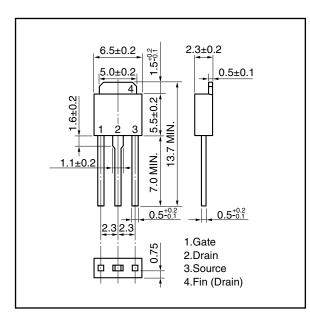
 $\begin{array}{l} \mathsf{R}\mathsf{G} = 25 \ \Omega \\ \mathsf{V}\mathsf{G}\mathsf{S} = 20 \ \mathsf{V} \rightarrow 0 \ \mathsf{V} \end{array}$

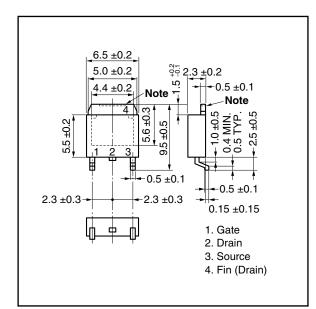




PACKAGE DRAWINGS (Unit: mm)

1) TO-251 (MP-3)





Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

Gate Protection Diode

EQUIVALENT CIRCUIT

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

<R> 2) TO-252 (MP-3Z)

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(Note)