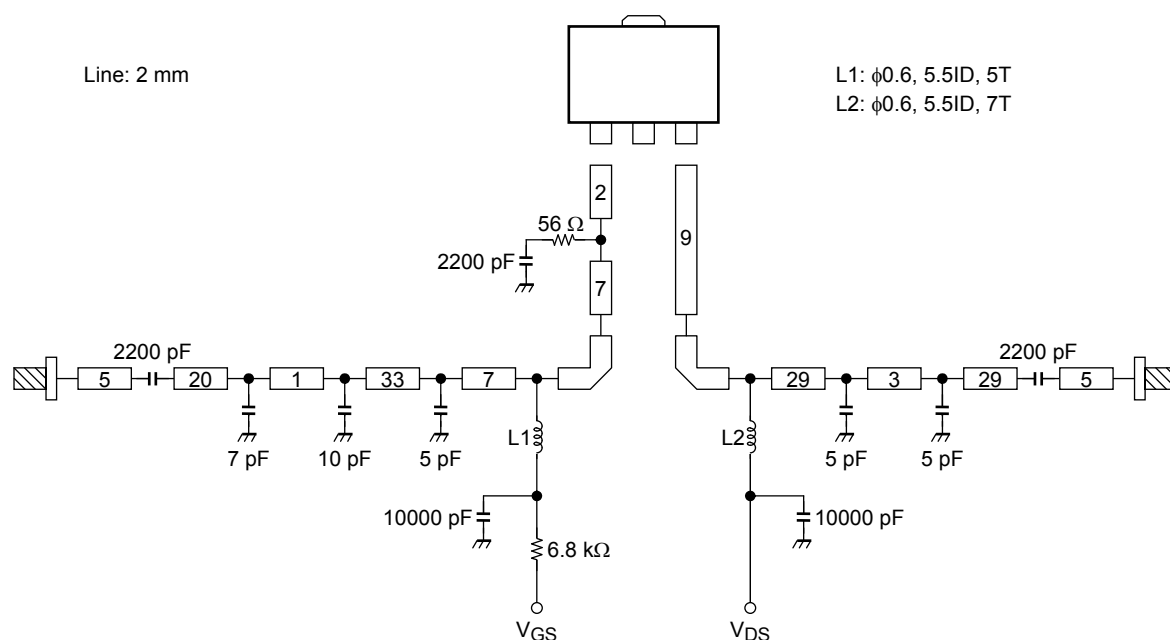
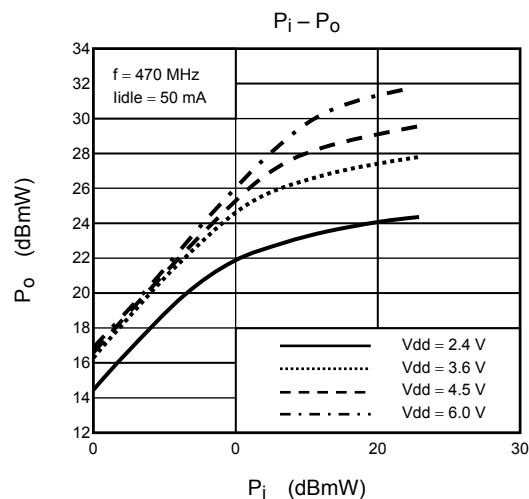
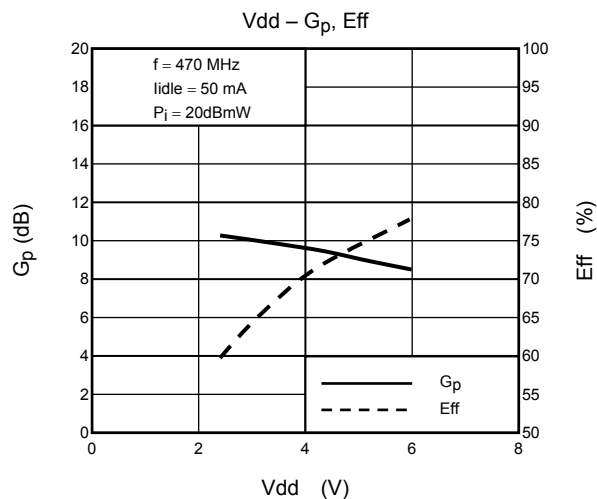
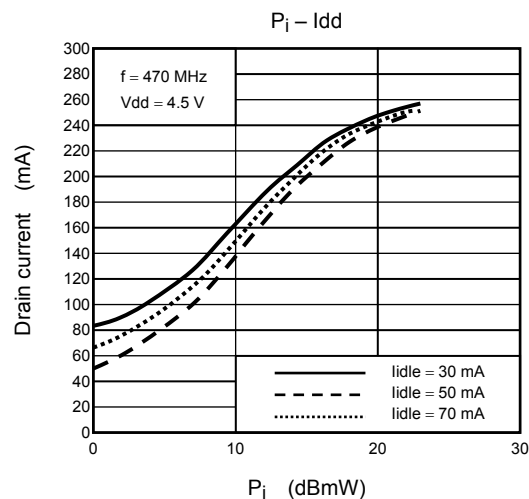
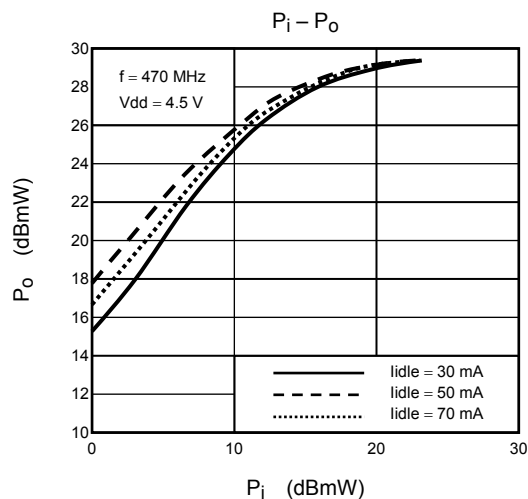
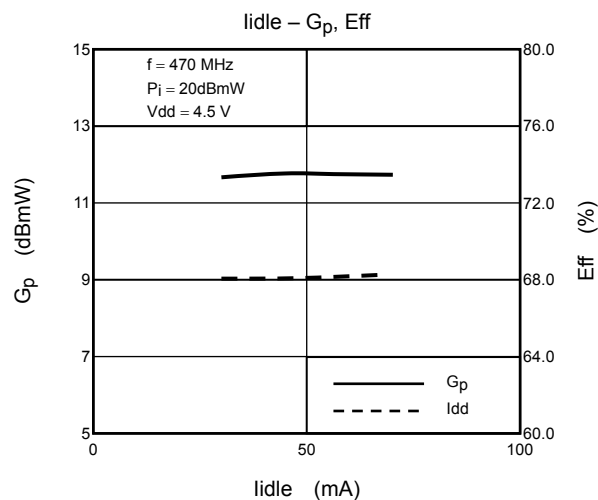
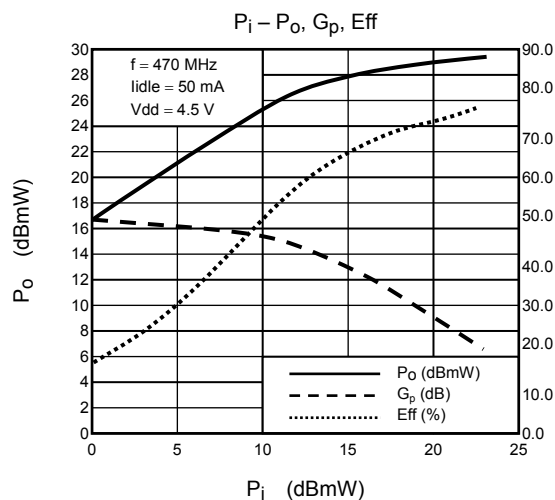
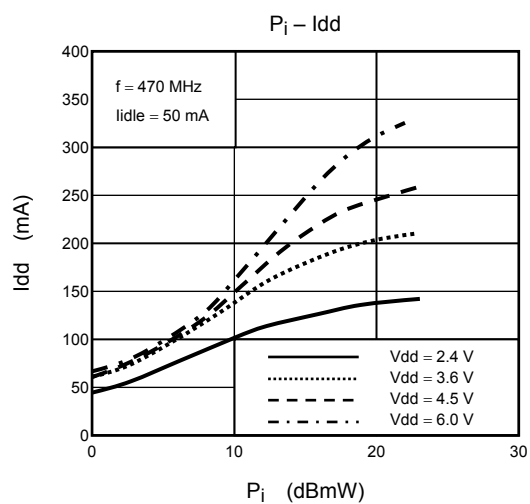


Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output power	P _O	V _{DS} = 4.5 V, I _{idle} = 50 mA (V _{GS} = adjust) f = 470 MHz, P _i = 20dBmW Z _G = Z _L = 50 Ω	28.0	—	—	dBmW
Drain efficiency	η _D		50	—	—	%
Power gain	G _p		8.0	—	—	dB
Threshold voltage	V _{th}	V _{DS} = 4.8 V, I _D = 0.5 mA	0.20	—	1.20	V
Drain cut-off current	I _{DSS}	V _{DS} = 10 V, V _{GS} = 0 V	—	—	10	μA
Gate-source leakage current	I _{GSS}	V _{GS} = 5 V, V _{DS} = 0 V	—	—	5	μA
Load mismatch (Note 3)	—	V _{DS} = 6.5 V, f = 470 MHz, P _i = 20dBmW, P _o = 28.0dBmW (V _{GS} = adjust) VSWR LOAD 10:1 all phase	No degradation			—

PF Output Power Test Fixture







Note3: These are typical curves and devices are not necessarily guaranteed at these curves.

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