

# VS-30ETH06PbF, VS-30ETH06-N3

**Vishay Semiconductors** 

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}$	-	28	35					
		$I_F = 1 \text{ A}, \ dI_F/dt = 100$	-	23	30					
		T <sub>J</sub> = 25 °C		-	31	-	- ns - A			
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 30 A dI <sub>F</sub> /dt = 200 A/µs	-	77	-				
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.5	-				
		T <sub>J</sub> = 125 °C	$V_{\rm R} = 200  \text{V}$	-	7.7	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	VR - 200 V	-	65	-	nC			
		T <sub>J</sub> = 125 °C		-	345	-	nc			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C			
Thermal resistance, junction-to-case per leg	R <sub>thJC</sub>		-	0.7	1.1				
Thermal resistance, junction-to-ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W			
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-				
Weight			-	2.0	-	g			
weight			-	0.07	-	oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-220AC	30ETH06						

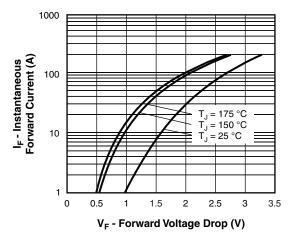
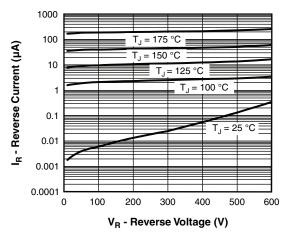
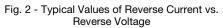


Fig. 1 - Typical Forward Voltage Drop Characteristics





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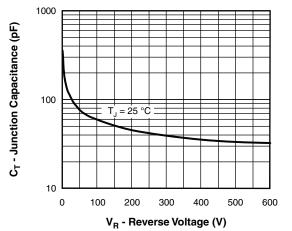


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

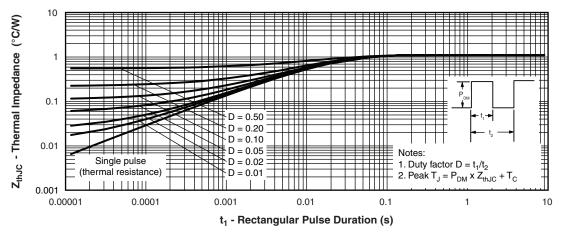
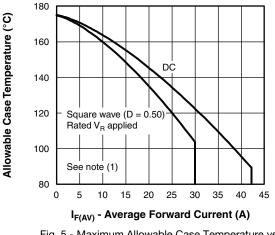
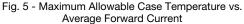
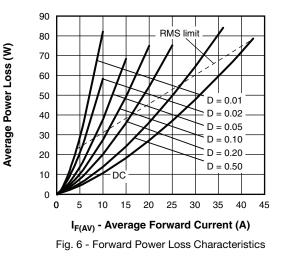


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics







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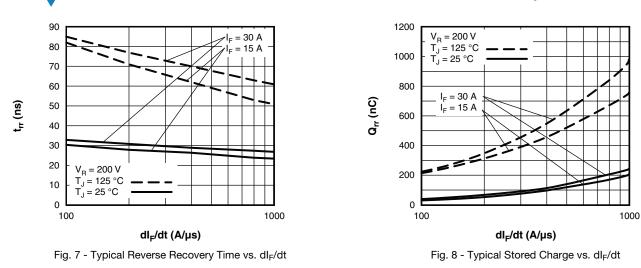
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#### Note

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<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

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 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

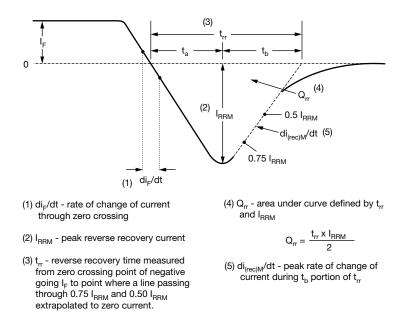


Fig. 9 - Reverse Recovery Waveform and Definitions



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### **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**

Device code	VS-	30	Е	т	н	06	PbF
		2	3	4	5	6	7
	1	- Visl	hay Sem	niconduc	ctors pro	oduct	
	2	- Cur	rent rati	ng (30 =	: 30 A)		
	3	- E=	single				
	4	- Pac	kage:				
		T =	TO-220	AC			
	5	- H=	hyperfa	ist recov	very		
	6	- Vol	tage rati	ng (06 =	= 600 V)		
	7	- Env	vironmer	ntal digit	:		
		PbF	= lead	(Pb)-fre	e and R	oHS-cc	ompliant

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	RRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIP								
VS-30ETH06PbF	50	1000	Antistatic plastic tube						
VS-30ETH06-N3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95221					
Part marking information	TO-220ACPbF	www.vishay.com/doc?95224					
Fait marking information	TO-220AC-N3	www.vishay.com/doc?95068					
SPICE model		www.vishay.com/doc?96439					

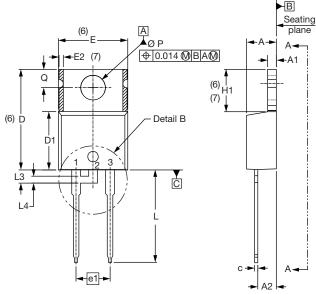


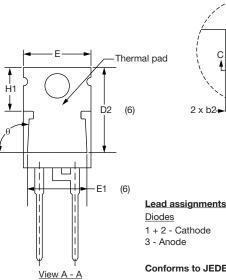
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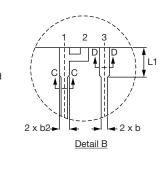
**TO-220AC** 

plane

#### **DIMENSIONS** in millimeters and inches









**Diodes** 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimension: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2 and E1
- <sup>(7)</sup> Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- <sup>(8)</sup> Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

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