

Figure 2: SE5516A Pin Out (Top View Through Package)

## **Pin Out Description**

Pin No.	Name	Description
1	ENg	2.4 GHz Power Amplifier Enable
2	GND	Ground
3	GND	Ground
4	Ant	Antenna
5	GND	Ground
6	C1	Switch Control
7	C0	Switch Control
8	GND	Ground
9	VCC	Supply Voltage
10	VCC	Supply Voltage
11	GND	Ground
12	GND	Ground

Pin No.	Name	Description
13	Tg	2GHz Transmit RF Input
14	GND	Ground
15	Rg	2GHz Receive RF Output
16	Ra	5GHz Receive RF Output
17	GND	Ground
18	Та	5GHz Transmit RF Input
19	ENa	5GHz Power Amplifier Enable
20	VCC	Supply Voltage
21	VCC	Supply Voltage
22	DET	2/5GHz Power Detector Output
23	GND	Ground
24	GND	Ground



SE5516A: Dual Band 802.11a/g/n/ac Wireless LAN Front End

**Preliminary** 

#### **Absolute Maximum Ratings**

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
Vcc	Supply Voltage	-0.3	3.6	V
PU	PA Enable pins: ENa, ENg	-0.3	3.6	V
TXRF	Max power input to Ta, Tg, with ANT terminated in 6:1 load or better	-	12.0	dBm
TA	Operating Temperature Range	-40	85	°C
Tstg	Storage Temperature Range	-40	150	°C
ESD <sub>ANT</sub>	JEDEC JESD22-A114 ANT pin zap to ground		1.5	KV
ESD <sub>HBM</sub>	JEDEC JESD22-A114 all pins	-	250	V

**Recommended Operating Conditions** 

Symbol	Parameter	Min.	Тур.	Max.	Unit
Vcc	Supply Voltage	3.0	3.3	3.6	V
TA	Ambient Temperature	-40	25	85	°C

#### **DC Electrical Characteristics**

Conditions: Vcc = 3.3 V, TA = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Icq-a	Total 802.11a Transmit Supply Current	No RF	-	170	-	mA
Icq-g	Total 802.11g Transmit Supply Current	No RF	-	140	-	mA
lcc-g	Total 802.11g Transmit Supply Current	P <sub>OUT</sub> = 18 dBm, 54 Mbps OFDM signal, 64 QAM	-	185	200	mA
Ісс-в	Total 802.11b Transmit Supply Current	P <sub>OUT</sub> = 21 dBm, 11 Mbps CCK signal, BT = 0.45,	-	205	220	mA
Icc-A	Total 802.11a Transmit Supply Current	P <sub>OUT</sub> = 16 dBm, 54 Mbps OFDM signal, 64 QAM,	-	220	250	mA
ICC-AC2G	Total 802.11ac Transmit Supply Current 2G	P <sub>OUT</sub> = 16 dBm, 256 QAM,MCS9, 40Mhz	-	155	165	mA
ICC-AC5G	Total 802.11ac Transmit Supply Current 5G	P <sub>OUT</sub> = 13 dBm, 256 QAM,MCS9, 80Mhz	-	190	210	mA
Icc-rx2	Total Receive Supply Current 2G	Eng=Ena=0V, LNA on Switch condition 1	-	-	12	mA
Icc-rx5	Total Receive Supply Current 5G	Eng=Ena=0V, LNA on Switch condition 4	-	-	12	mA
Icc_off	Total Supply Current	No RF, ENg = ENa = 0 V Switch condition 3 or 6	-	65	200	μΑ



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## **Logic Characteristics**

Conditions: Vcc = 3.3 V, T<sub>A</sub> = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board (de-embedded to

device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
VENH	Logic High Voltage for ENg, ENa (Module On)	-	1.8	-	Vcc	V
VENL	Logic Low Voltage ENg, ENa (Module Off)	-	0	-	0.5	V
lenh	Input Current Logic High Voltage (ENg, ENa)	-	-	350	400	μΑ
lenl	Input Current Logic Low Voltage (ENg, ENa)	-	1	0.2	1	μΑ

#### **Switch Characteristics**

Conditions: Vcc = Ven = 3.3 V, Ta = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board (dembedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Vctl_on	Control Voltage (On State)	-	3.0	-	3.6	٧
Vctl_off	Control Voltage (OFF State)	-	0.0	-	0.2	V
SWon	Low Loss Switch Control Voltage	High State = Vctl_on - Vctl_off	2.8	-	Vcc	V
SWoff	High Loss Switch Control Voltage	Low State = Vctl_off - Vctl_off	0	-	0.3	V
ICTL_ON	Switch Control Bias Current (RF Applied)	On pin (C0, C1) being driven high. RF Applied	-	-	100	μΑ
ICTL_ON	Switch Control Bias Current (No RF)	On pin (C0, C1) being driven high. No RF	-	-	30	μΑ
Ссть	Control Input Capacitance	-	-	-	100	pF



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# **Switch Control Logic Table**

Condition		Logic				State		
Condition	C0	C1	ENa	Eng	SP3T	LNA	Bypass	
1	0	1	0	0	RXg	RXg_EN	Open	
2	0	0	0	1	TX	RXg_OFF	RXg_Bypass	
3	0	0	0	0	RXg	RXg_OFF	RXg_Bypass	
4	1	1	0	0	RXa	RXa_EN	Open	
5	1	0	1	0	TX	RXa_OFF	RXa_Bypass	
6	1	0	0	0	RXa	RXa_OFF	RXa_Bypass	



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#### 2.4 GHz AC Electrical Characteristics

#### 2.4 GHz Transmit Characteristics

Conditions: Vcc = 3.3 V, ENg = C0 = 3.3 V, ENa = C1 = 0 V, TA = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit	
Fin	Frequency Range	-	2400	-	2500	MHz	
P802.11g	Output power	54 Mbps OFDM signal, 64QAM, DEVM = 3.0 %, input signal EVM < 1% 802.11g mask compliant	-	18	-	dBm	
P802.11n	Output power	OFDM signal, MCS7, HT40, DEVM = 3% 802.11n mask compliant <u>+</u> 11MHz offset, RBW=100KHz, VBW=30KHz	-	18	-	dBm	
		DEVM = 2%	-	17	-		
P802.11ac	Output power	40 MHz 256 QAM, MCS9 DEVM= 1.8% ac mask compliant	-	16	-	dBm	
		DEVM = 1.2%	-	15	-		
P802.11b	Output power	11 Mbps CCK signal, BT = 0.45 ACPR(± 11MHz offset) < -35 ACPR(± 22MHz offset) < -56	21	22	-	dBm	
P <sub>1dB</sub>	P1dB	-	24	24.5	-	dBm	
S <sub>21</sub>	Small Signal Gain	-	25	-	30	dB	
ΔS21	Small Signal Gain Variation Over Band	-	-	1.0	2.0	dB	
S <sub>21</sub> 1.6	Gain at ½Ref-VCO	1640.00 to 1942.00 MHz	-	12	20	dB	
S <sub>21</sub> 3.2	Gain at Ref-VCO	3216.00 to 3312.00 MHz	-	-	0	dB	
		Pout ≤ 21 dBm, 1Mbps, CCK	-	-	-45.2		
2f,3f	Harmonics	Pout ≤ 18 dBm, 802.11gn, all data rates	-	-	-50.0	dBm/MHz	
tar, taf	Delay and rise/fall Time	50 % of VEN edge and 90/10 % of final output power level	-	0.2	0.4	μs	
S <sub>11</sub>	Input Return Loss	-	9	10	-	dB	
STAB	Stability	CW, Pout = 21 dBm 0.1 GHz – 21 GHz Load VSWR = 6:1	All non-harmonically related outputs less than -42 dBm/MHz				
Ru	Ruggedness	Tg = 12dBm, ANT load varies over 6:1 VSWR	No Irreversible damage				



#### 2.4 GHz Receive Characteristics

Conditions: Vcc = 3.3 V, Switch Condition 1, T<sub>A</sub> = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fоuт	Frequency Range	-	2400	ī	2500	MHz
	Receive Gain, LNA Enabled.	2400 – 2485 MHz Switch Condition 1	10	14	16	
S <sub>21</sub>	Receive Gain, Bypass mode	LNA bypassed Switch Condition 3	-12	-	-10	dB
	High Band Gain @5150-5850MHz	Switch Condition 1	-	-10	-	
Δ\$21	Gain Variation	2400 – 2485 MHz, Over any 40MHz band	-	0.25	0.5	dB
NF	Noise Figure	De-embedded to device	-	2.5	2.8	dB
S <sub>11</sub>	Input Return Loss	-	5	10	-	dB
S22	Output Return Loss	-	8	10	-	dB
IP1dB	Input P1dB	LNA enabled Switch Condition 1	-	-7	-	dBm
IP1dB	Input P1dB	LNA bypassed Switch Condition 3	-	8	-	dBm
LB	Loop Back Isolation (ANT – RXg)	Switch Condition 2, Pout = Psat	Psat+10	40	-	dB
т.	Enable Time	RXg LNA_ON    ∠ LNA_Bypass	-	-	0.2	
$T_{EN}$	From C0 = 50% to RX RF power to 90% of transition	TX ₹ RXg, RXa ₹ RXg	-	0.6	1.0	usec



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#### **5 GHz AC Electrical Characteristics**

#### **5 GHz Transmit Characteristics**

Conditions: Vcc = 3.3 V, ENa = C0 = 3.3 V, ENg = C1 = 0 V,  $T_A = 25 ^{\circ}C$ , as measured on Skyworks' SE5516A-EK1

evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fin	Frequency Range	-	4900	-	5900	MHz
P802.11n	Output Power	OFDM signal, MCS7, HT40, DEVM = 3% 802.11n mask compliant ± 11MHz offset, RBW=100KHz, VBW=30KHz	-	16	-	dBm
		DEVM = 2.0%	-	15	-	1
P802.11ac	Output Power	80Mhz , 256 QAM, MCS9 DEVM = 1.8 % Input signal EVM < 0.6% 802.1111ac mask compliant	-	13	-	dBm
		DEVM = 1.2%	-	12	-	]
P <sub>1dB</sub>	P1dB	-	21	22.5	-	dBm
	Small Signal Gain	In Band	23	-	30	dB
S <sub>21</sub>		1.9GHz	-	-	-20	
		3.9GHz	-	-	4	
	Small Signal Gain Variation	n Over 80 MHz Channel	-1	-	1	dB
ΔS21	Small Signal Gain Variation Over sub-bands	4.9 – 5.18 GHz 5.18 – 5.50 GHz 5.50 – 5.90 GHz	-	2	3	dB
S <sub>12</sub>	-	Switch State 5, 4.9 – 5.9 GHz	-	-	-45	dB
2f,3f	Harmonics @16dBm, 54Mbps, 802.11a		-	-	-50	dBm/MHz
tar, taf	Delay and rise/fall Time	50 % of V <sub>EN</sub> edge and 90/10 % of final output power level	-	0.2	0.4	μs
S <sub>11</sub>	Input Return Loss	-	-	10	-	dB
S22	Output Return Loss		-	10		dB
STAB	Stability	64 QAM, POUT = 16 dBm 0.1 GHz – 21 GHz Load VSWR = 6:1	All non-harmonically related outputs less than -42 dBm/MHz			
Ru	Ruggedness	TXa = 12dBm, ANT load varies over 6:1 VSWR	No Irreversible damage			



## **5 GHz Receive Characteristics**

Conditions: Vcc = 3.3 V, Switch Condition 4, TA = 25 °C, as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fouт	Frequency Range	-	4900	-	5850	MHz
	Receive Gain	4900 – 5850 MHz Switch Condition 4	10	12	14	
S <sub>21</sub>	Receive Gain, Bypass mode	LNA bypassed Switch Condition 6	-14	1	-10	dB
	Low Band Gain @2400-2500MHz	Switch Condition 4	ı	-10	ı	
Δ\$21	Gain Variation	4900 – 5850 MHz, Over any 80MHz band	-0.5	ı	0.5	dB
		Over Entire band	-TBD	-	TBD	
NF	Noise Figure	De-embedded to device	ı	2.8	3.0	dB
S <sub>11</sub>	Input Return Loss	-	8	10	-	dB
S22	Output Return Loss	-	6	10	-	dB
IP1dB	Input P1dB	LNA enabled Switch Condition 4	-6	-	-	dBm
IP1dB	Input P1dB	LNA bypassed Switch Condition 6	6	-	-	dBm
LB	Loop Back Isolation (TXa – RXa)	Switch Condition 5, Pout = Psat	-	-40	-	dB
T <sub>EN</sub>	Enable Time	10% to 90% of RX RF power, from time that C0 is at 50%	1	-	0.2	usec



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#### 2.4 GHz Power Detector Characteristic

Conditions:  $V_{CC} = 3.3 \text{ V}$ , Switch Condition 2,  $T_A = 25 \,^{\circ}\text{C}$ , as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50  $\Omega$ , unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fouт	Frequency Range	-	2400	-	2500	MHz
PDR	Power detect range, peak power	Measured at ANT	0	-	22	dBm
PDZout	DC Output impedance	-	-	2.3	-	ΚΩ
PDV <sub>P21</sub>	Output Voltage, Pout = 21dBm	Measured into 26.5KΩ	-	0.80	-	٧
PDV <sub>p18</sub>	Output Voltage, Pout = 18dBm	Measured into 26.5KΩ	-	0.60	-	٧
PDVpnoRF	Output Voltage, Pout = No RF	Measured into 26.5KΩ	-	0.20	-	<b>V</b>
LPF-3dB	Power detect low pass filter -3dB corner frequency	Measured into 26.5KΩ	-	10	-	MHz
PDet <sub>acc</sub>	Power Detector Accuracy	Measured into 3:1 load at ANT port	-1	-	1	dB

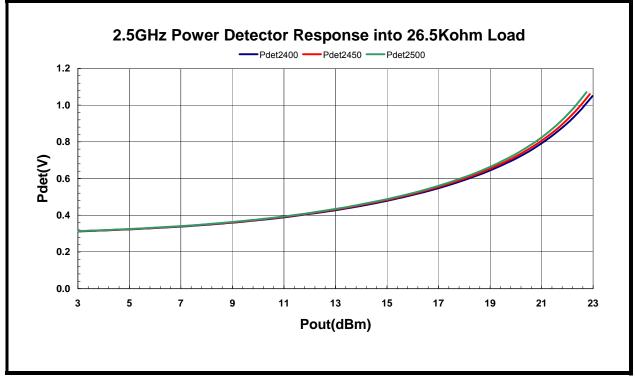


Figure 3: SE5516A Power Detector vs. Output Power over Frequency (CW Signal)



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#### **5 GHz Power Detector Characteristic**

Conditions:  $V_{CC} = 3.3 \text{ V}$ , Switch Condition 5,  $T_A = 25 \,^{\circ}\text{C}$ , as measured on Skyworks' SE5516A-EK1 evaluation board, all unused ports terminated with 50  $\Omega$ , unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fouт	Frequency Range	-	4900	-	5900	MHz
PDR	Power detect range, peak power	Measured at ANT	0	-	21	dBm
PDZout	DC Output impedance	-	-	26.5	-	ΚΩ
PDV <sub>p18</sub>	Output Voltage, Pout = 18dBm	Measured into 26.5KΩ	-	0.70	-	V
PDV <sub>p16</sub>	Output Voltage, Pout = 16dBm	Measured into 26.5KΩ	-	0.6	-	V
PDV <sub>NoRF</sub>	Output Voltage, Pout = No RF	Measured into 26.5KΩ	-	0.20	-	V
LPF-3dB	Power detect low pass filter -3dB corner frequency	Measured into 26.5KΩ	-	10	-	MHz
PDet <sub>acc</sub>	Power Detector Accuracy	Measured into 3:1 load at ANT port	-1.5	-	1.5	dB

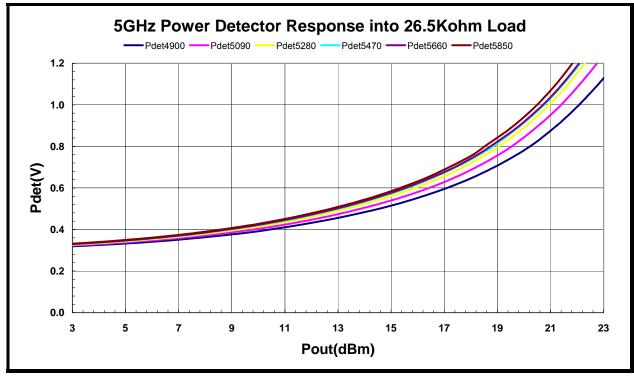


Figure 4: Preliminary SE5516A Power Detector vs. Output Power over Frequency (CW Signal)



## **Package Drawing**

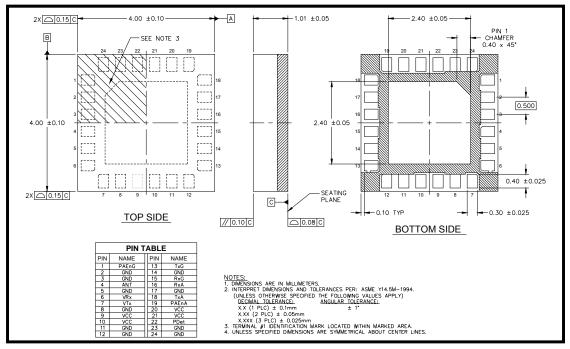


Figure 5: Package Drawing: Topside

## **Recommended Land and Solder Patterns**

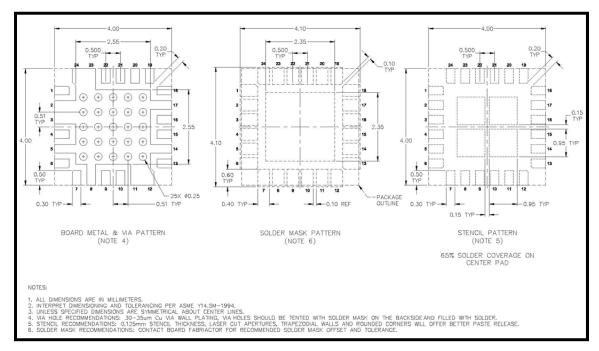


Figure 6: Recommended Land and Solder Patterns



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#### **Package Handling Information**

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE5516A is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended by Skyworks, please refer to:

- Skyworks' Application Note: "Land Grid Array Module Solder Reflow & Rework Information", Document Number QAD-00046.
- Skyworks' Application Note: "Handling, Packing, Shipping and Use of Moisture Sensitive LGA", Document Number QAD-00047.



## **Product Branding**

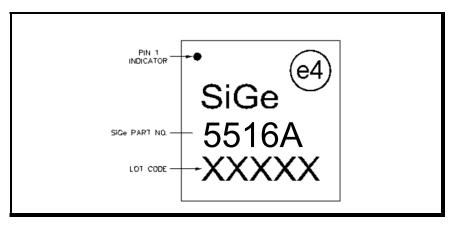


Figure 7: SE5516A Branding Information



SE5516A: Dual Band 802.11a/g/n/ac Wireless LAN Front End

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## **Tape and Reel Information**

Production quantities of this product are shipped in a standard tape-and-reel format. Specific tape and reel dimensions and sizing is shown in Table 1 and Figure .

Parameter	Value
Devices Per Reel	3000
Reel Diameter	13 inches

**Table 1: Tape and Reel Dimensions** 

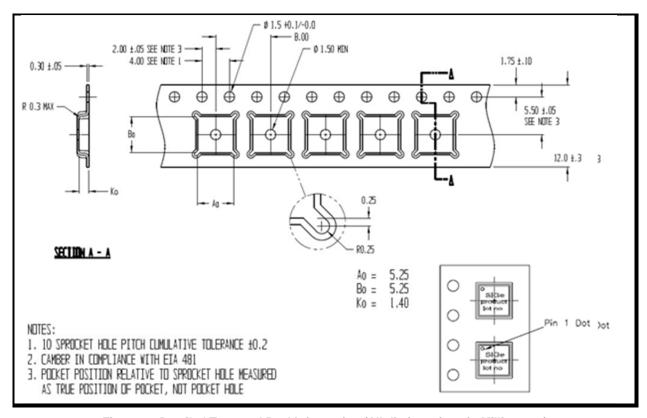


Figure 8: Detailed Tape and Reel Information (All diminensions in Millimeters)



## **Document Change History**

Revision	Date	Notes	
1.0	Mar-29-2011	Created	
1.1	Apr-20-2011	Numerous updates to performance limits	
1.2	Apr-26-2011	Update RX loop-back limits Update detector bandwidth	
1.3	Apr-30-2011	Updated RX Gain in Active and Bypass modes, both bands	
1.4	Nov-22-2011	Update to DEVM Update LNA gain at the opposite band (both bands) Update Harmonics Update loopback mode isolation (both bands)	
1.5	May 15, 2012	Update to change header and footer to Skyworks.  Update current consumption, 2GHz TX gain, 2GHx RX EN time, detector characteristics	



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