## **Highlights**

- · Hot-plug controllers, end-to-end data integrity protection, high-quality and low-power fifth generation SERDES
- Comprehensive diagnostics and debugging: PCle generator and analyzer, per-port performance and error counters, multiple loopback modes and real-time eye capture
- Up to 24 ports, two NTBs, and six virtual switch partitions
- Flexible x4, x8, and x16 port bifurcation with no restrictions on configuring ports as either upstream or downstream, or on mapping ports to NTBs

### High-Speed I/O

- PCle Gen3 8 GT/s
- Supports PCle-compliant link training and manual PHY configuration

#### **Power Management**

- Active State Power Management (ASPM)
- Software controlled power management

### **Chiplink Diagnostic Tools**

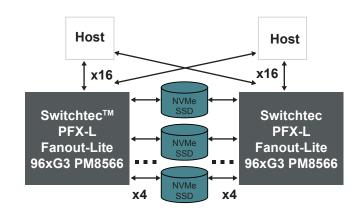
- Extensive debug, diagnostics, configuration and analysis tools with an intuitive GUI
- Access to configuration data, management capabilities and signal integrity analysis tools (such as real-time eye capture)
- Connects to device over in-band PCle or sideband signals (UART, TWI and EJTAG)

#### **Evaluation Kit**

The evaluation kit is a device evaluation environment supporting multiple host and SSD interfaces. The following kit is available for evaluating PSX, PFX, PFX-I and PFX-L features using the populated PSX 96xG3 device on the PM5461-KIT:

 PM5461-KIT - PSX/PFX/PFX-I/PFX-L 96/80/64×G3 PCIe Switch HD Evaluation Kit (PMC-2151996)

# **Example Application**



# **Ordering Information**

Product	Lanes	Ports/NTBs	Virtual Switch Partitions	Hot-Plug Controllers	Package	Ordering Number
PFX-L 96xG3	96	24/2	6	6	37.5 mm × 37.5 mm	PM8566B-FEI
PFX-L 80xG3	80	20/2	6		37.5 mm × 37.5 mm	PM8565B-FEI
PFX-L 64xG3	64	16/2	6		37.5 mm × 37.5 mm	PM8564B-FEI
PFX-L 48xG3	48	12/2	6		27 mm × 27 mm	PM8563B-F3EI
PFX-L 32xG3	32	8/2	4		27 mm × 27 mm	PM8562B-F3EI
PFX-L 24xG3	24	6/2	3		27 mm × 27 mm	PM8561B-F3EI

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