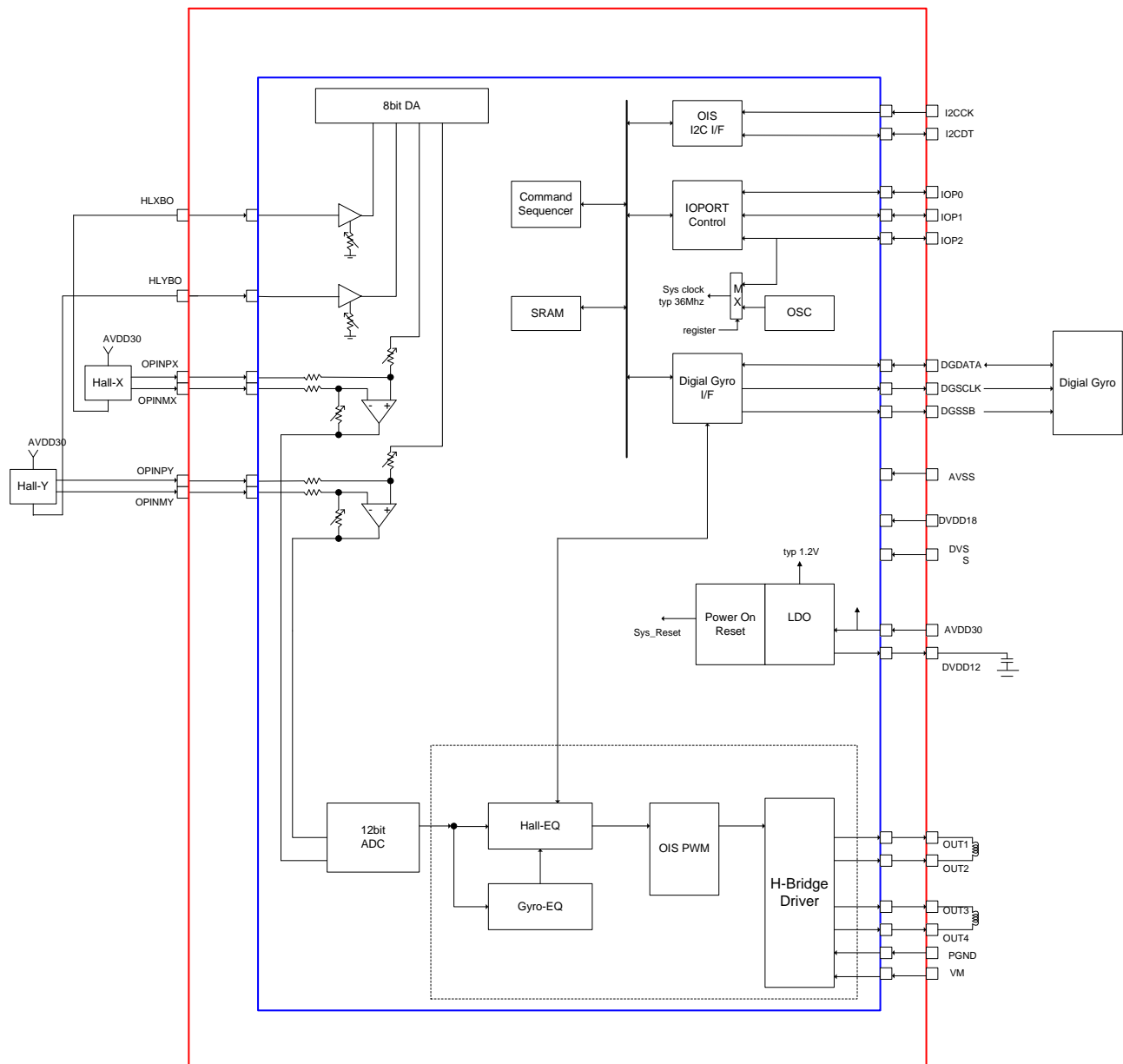


Block Diagram



Example of wiring diagram (Hall) in LC898119XC

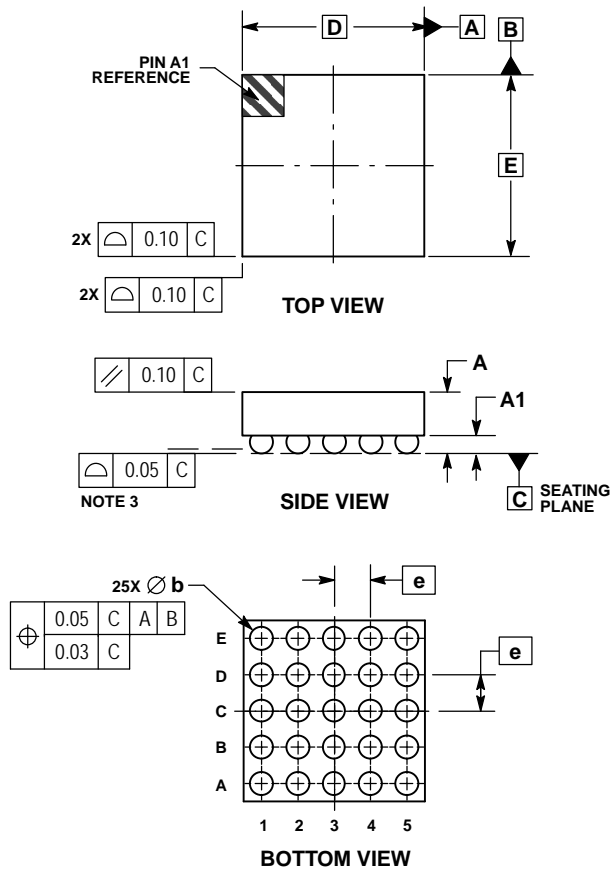
Package Dimensions

unit : mm

WLCSP25, 2.0x2.0

CASE 567HK

ISSUE O

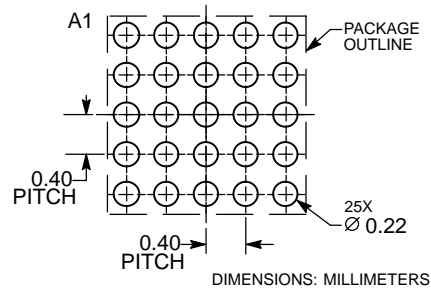


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

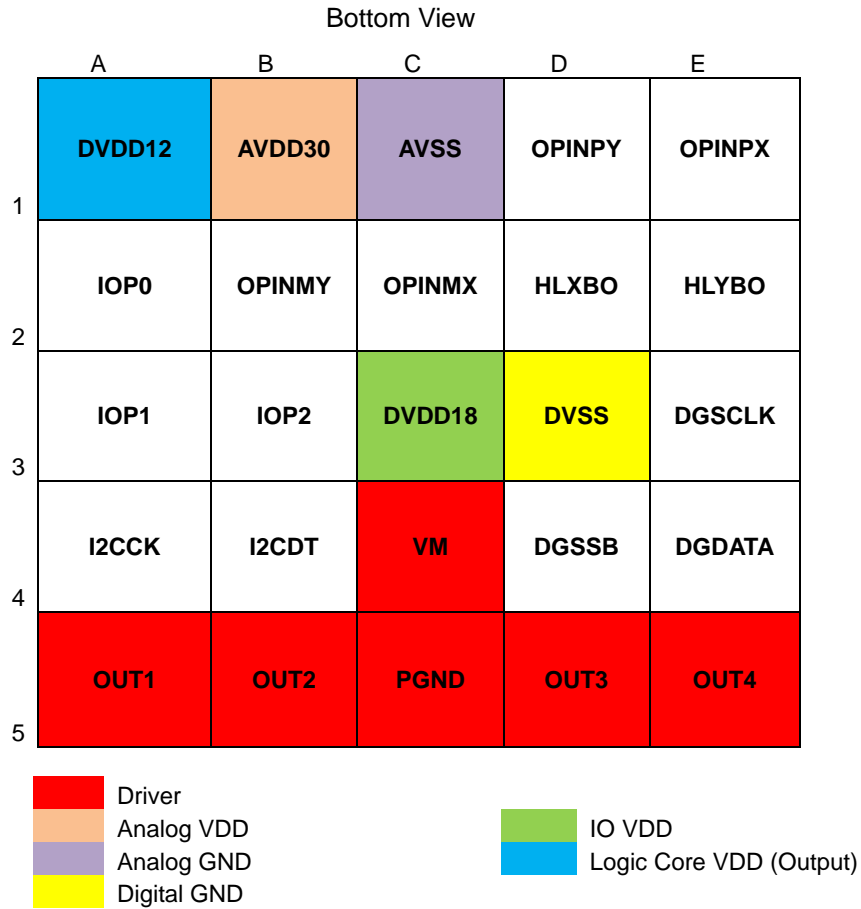
DIM	MILLIMETERS	
	MIN	MAX
A	---	0.675
A1	0.15	0.25
b	0.21	0.31
D	2.00 BSC	
E	2.00 BSC	
e	0.40 BSC	

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Pin Assignment



<typ> I : INPUT, O : OUTPUT, B : BIDIRECTION, P : Power, GND

Ball No	Pin Name	type	Description
A1	DVDD12	P	LDO Power supply out (Logic Core VDD (typ 1.2V))
A2	IOP0	B	General-purpose IOPORT
A3	IOP1	B	General-purpose IOPORT
A4	I2CCK	I	I2C_IF clock
A5	OUT1	O	Driver Output
B1	AVDD30	P	Analog Power(2.6V to 3.6V)
B2	OPINMY	I	Hall-Y OpAmp input-
B3	IOP2	B	General-purpose IOPORT / External Clock input (change at Register)
B4	I2CDT	B	I2C_IF Data
B5	OUT2	O	Driver Output
C1	AVSS	P	Analog GND
C2	OPINMX	I	Hall-X OpAmp input-
C3	DVDD18	P	IO Power (1.62V to 1.98V)
C4	VM	P	Driver Power
C5	PGND	P	Driver GND
D1	OPINPY	I	Hall-Y OpAmp input+
D2	HLXBO	O	Hall-X Bias (Current Drive)
D3	DVSS	P	Logic GND
D4	DGSSB	B	Digital Gyro IF Chip Select (O)
D5	OUT3	O	Driver Output
E1	OPINPX	I	Hall-X OpAmp input+
E2	HLYBO	O	Hall-X Bias (Current Driver)
E3	DGSCCLK	O	Digital Gyro IF clock
E4	DGDATA	B	Digital Gyro IF data
E5	OUT4	O	Driver Output

ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LC898119XC-MH	WLCSP25 2.0x2.0 (Pb-Free / Halogen Free)	4000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

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