TEMPERATURE CHARACTERIS	TICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
Specification	All output types, see safety approval section for UL temperature specification	-40		85	
Storage		-50		125	
Case Temperature above ambient	5V output types		33		°C
	All other output types		28		
	1kVDC short circuit types		18		
	3kVDC short circuit types		19		
Cooling	Free air convection				

ABSOLUTE MAXIMUM RATINGS	
Lead temperature 1.5mm from case for 10 seconds	260°C
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to <u>application notes</u> for further information.
5V input types	7V
12V input types	15V
24V input types	28V



Isolated 0.75W Single and Dual Output Isolated DC-DC Converters

#### **TECHNICAL NOTES**

#### **ISOLATION VOLTAGE**

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions CMR series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 1kVDC for 1 second for C versions and 3kVDC for 1 minute for 3C versions.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The CMR is recognised by Underwriters Laboratory for functional insulation, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

#### REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The CMR series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

## SAFETY APPROVAL

#### **UL60950**

The CMR is recognised by Underwriters Laboratory (UL) to UL 60950 for functional insulation. The CMR100C and CMR118C in a maximum still air ambient temperature of 100°C as measured at any point on the case of the unit (hotspot).

#### **FUSING**

The CMR Series of converters are not internally fused so to meet the requirements of UL an anti-surge input line fuse should always be used with ratings as defined below.

CMR100xC: 0.5A CMR118C: 0.12A CMR05xxxx3C: 0.315A CMR12xxxx3C: 1A

All fuses should be UL recognised and rated to 125V. File number E151252 applies.

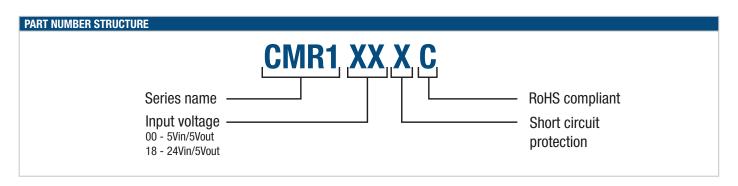
#### **ROHS COMPLIANCE INFORMATION**

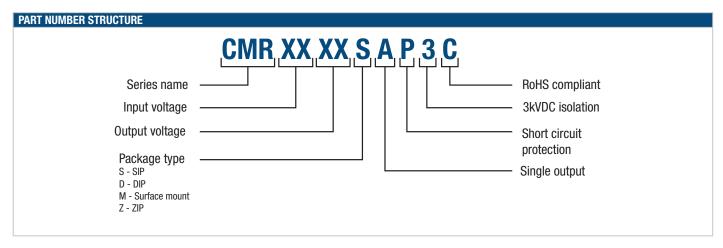


This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to <u>application</u> <u>notes</u> for further information. The pin termination finish on this product series is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The series is backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata-ps.com/rohs









### **CHARACTERISATION TEST METHODS** Ripple & Noise Characterisation Method Ripple and noise measurements are performed with the following test configuration. 1µF X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC-DC converter 10µF tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC-DC converter with an ESR of less C2 than 100mΩ at 100 kHz C3 100nF multilayer ceramic capacitor, general purpose R1 450Ω resistor, carbon film, $\pm 1\%$ tolerance 50Ω BNC termination R2 T1 3T of the coax cable through a ferrite toroid RLOAD Resistive load to the maximum power rating of the DC-DC converter. Connections should be made via twisted wires Measured values are multiplied by 10 to obtain the specified values. Differential Mode Noise Test Schematic OSCILLOSCOPE C1 C2 C3 R2

## **APPLICATION NOTES**

### Minimum load

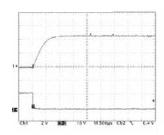
The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

#### Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2 $\mu$ s and output capacitance of 10 $\mu$ F, are shown in the table below. The product series will start into a capacitance of 47 $\mu$ F with an increased start time, however, the maximum recommended output capacitance is 10 $\mu$ F.

	Start-up time
	μs
CMR100C	2300
CMR118C	670
CMR0505SA3C	1970
CMR0512S3C	11200
CMR0515S3C	16300
CMR1215S3C	11200
CMR100PC	360
CMR0505SAP3C	370





## **APPLICATION NOTES (Continued)**

#### **Output Ripple Reduction**

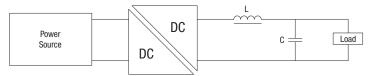
By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

#### Component selection

Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended.

The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC-DC converter.

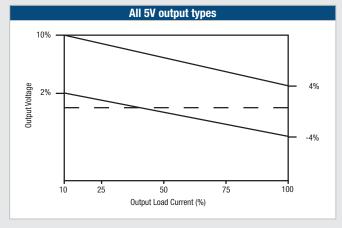
Inductor: The rated current of the inductor should not be less than that of the output of the DC-DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC-DC converter. The SRF (Self Resonant Frequency) should be >20MHz.

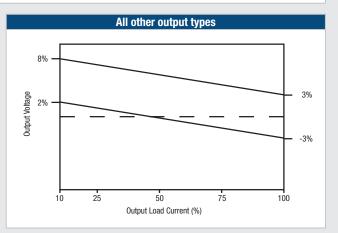


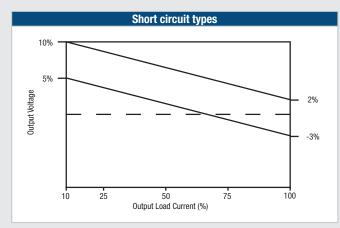
		Inductor		Capacitor
	L, μH	SMD	Through Hole	C, µF
CMR100C	10	82103C	11R103C	4.7
CMR118C	10	82103C	11R103C	4.7
CMR0505SA3C	22	82223C	11R223C	1
CMR0512S3C	150	82154C	11R154C	0.33
CMR0515S3C	220	82224C	11R224C	0.33
CMR1215S3C	220	82224C	11R224C	0.22
CMR100PC	22	82223C	11R223C	1
CMR0505SAP3C	22	82223C	11R223C	1

## **TOLERANCE ENVELOPES**

The voltage tolerance envelopes show typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading and set point accuracy.

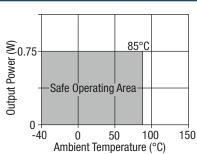




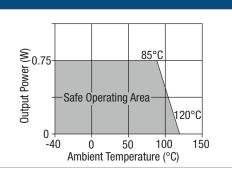




Short Circuit types only.



All other types.





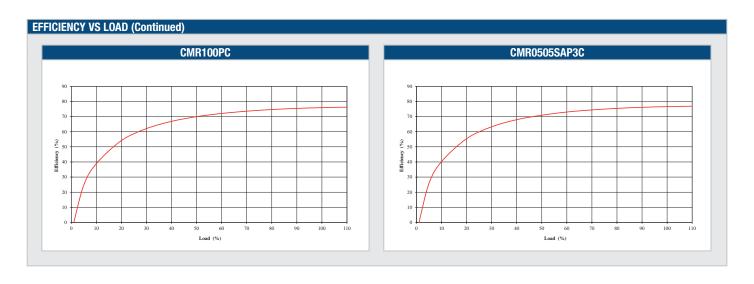
EFFICIENCY VS LOAD	
ELITOLETO PO LOND	
CMR100C	CMR118C
CMR0505SA3C	CMR0512S3C
CMR0515S3C	CMR1215S3C
OWINGS 1353C	GWII11213530

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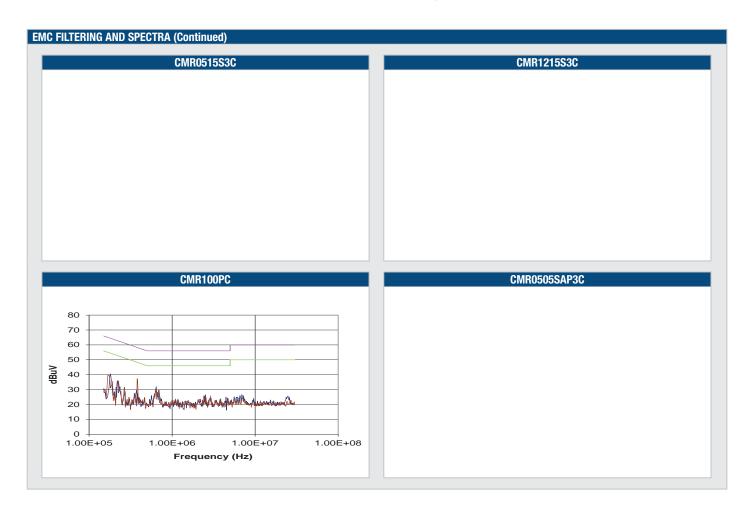




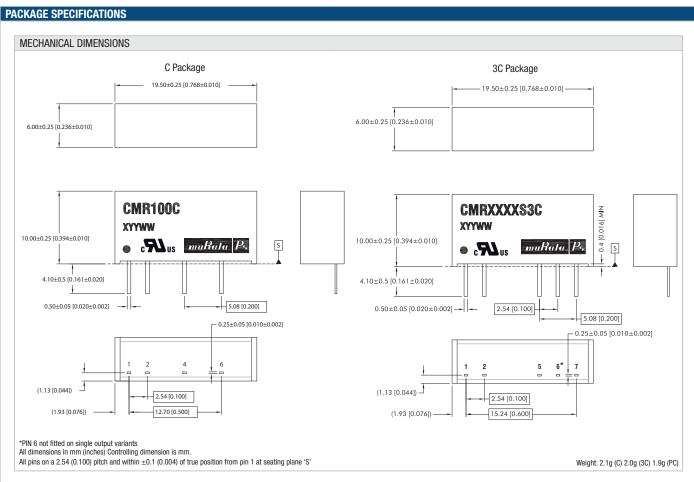


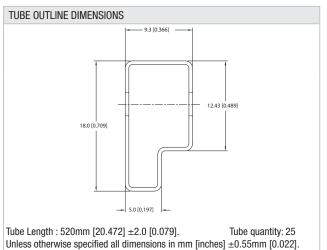
# EMC FILTERING AND SPECTRA FILTERING The following filter circuit and filter table shows the input filters typically required to meet EN 55022 Curve B, Quasi-Peak EMC limit, as shown in the following plots. The following plots show positive and negative quasi peak and CISPR22 Average Limit B (pink line) and Quasi Peak Limit B (green line) adherence limits. DC C = DC C Ceramic capacitor Inductor Capacitor SMD Part Number L, µH Through Hole C, µF CMR100C CMR118C CMR0505SA3C CMR0512S3C CMR0515S3C CMR1215S3C 22 82223C 11R223C CMR100PC CMR0505SAP3C CMR100C **CMR118C** CMR0505SA3C CMR0512S3C





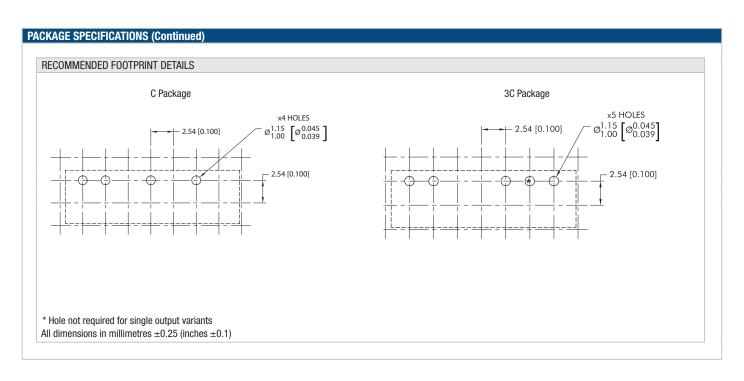






NNECTION	S		
PIN	C C	3C	
FIIN		Single	Dual
1	+VIN	+VIN	+V <sub>IN</sub>
2	-V <sub>IN</sub>	-VIN	-V <sub>IN</sub>
4	-Vout		
5		-Vout	-Vоит
6	+Vоит		OV
7		+Vоит	<b>+V</b> 0UT







Isolated 0.75W Single and Dual Output Isolated DC-DC Converters

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- Data Processing equipment

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