

X02xxxN

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R _{th(j-a)}	Junction to ambient *	60	°C/W
R _{th(j-t)}	Junction to tab for DC	25	°C/W

* : With 5cm² copper ($e=35\mu m$) surface under tab.

GATE CHARACTERISTICS (maximum values)

$$P_G(AV) = 0.2 \text{ W} \quad P_{GM} = 3 \text{ W} \quad (t_p = 20 \mu\text{s}) \quad I_{GM} = 1.2 \text{ A} \quad (t_p = 20 \mu\text{s})$$

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions	Sensitivity			Unit		
		02	03	05			
I _{GT}	V _D =12V (DC) R _L =140Ω	T _j = 25°C	MIN		20	20	μA
			MAX	200	200	50	
V _{GT}	V _D =12V (DC) R _L =140Ω	T _j = 25°C	MAX	0.8		V	
V _{GD}	V _D =V _{DRM} R _L =3.3kΩ R _{GK} = 1 KΩ	T _j = 125°C	MIN	0.1		V	
V _{RGM}	I _{RG} =10μA	T _j = 25°C	MIN	8		V	
t _{gd}	V _D =V _{DRM} I _{TM} = 3 × I _{T(AV)} dI _G /dt = 0.1A/μs I _G = 10mA	T _j = 25°C	TYP	0.5		μs	
I _H	I _T = 50mA R _{GK} = 1 KΩ	T _j = 25°C	MAX	5		mA	
I _L	I _G =1mA R _{GK} = 1 KΩ	T _j = 25°C	MAX	6		mA	
V _{TM}	I _{TM} = 2.8A t _p = 380μs	T _j = 25°C	MAX	1.5		V	
I _{DRM} I _{RRM}	V _D = V _{DRM} R _{GK} = 1 KΩ V _R = V _{RRM}	T _j = 25°C	MAX	5		μA	
		T _j = 110°C	MAX	200		μA	
dV/dt	V _D =67%V _{DRM} R _{GK} = 1 KΩ	T _j = 110°C	TYP	15	20	15	V/μs
t _q	I _{TM} = 3 × I _{T(AV)} V _R =35V dI/dt=10A/μs t _p =100μs dV/dt=2V/μs V _D = 67%V _{DRM} R _{GK} = 1 KΩ	T _j = 110°C	MAX	100		μs	

ORDERING INFORMATION

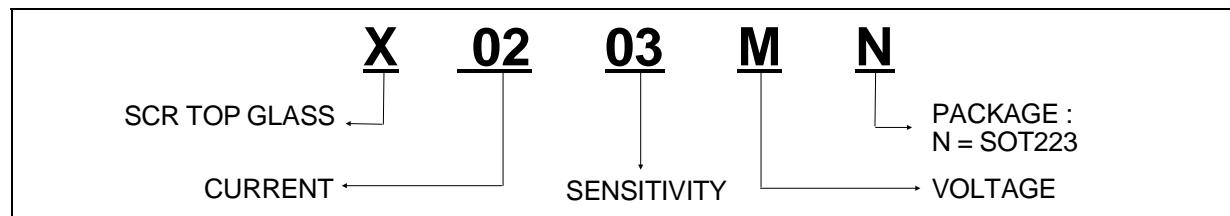


Fig.1 : Maximum average power dissipation versus average on-state current.

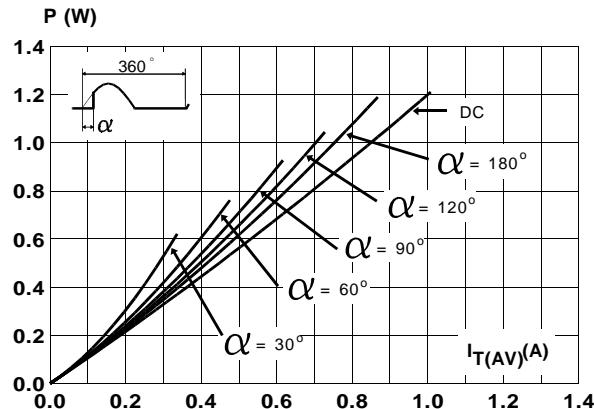


Fig.2 : Correlation between maximum average power dissipation and maximum allowable temperature (Tamb and Ttab).

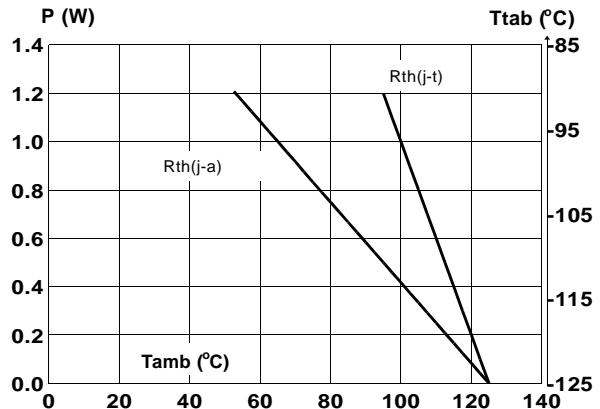


Fig.3 : Average on-state current versus tab temperature.

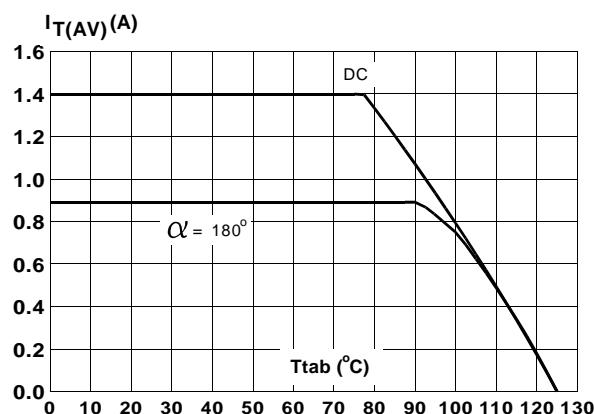


Fig.4 : Relative variation of thermal impedance junction to ambient versus pulse duration.

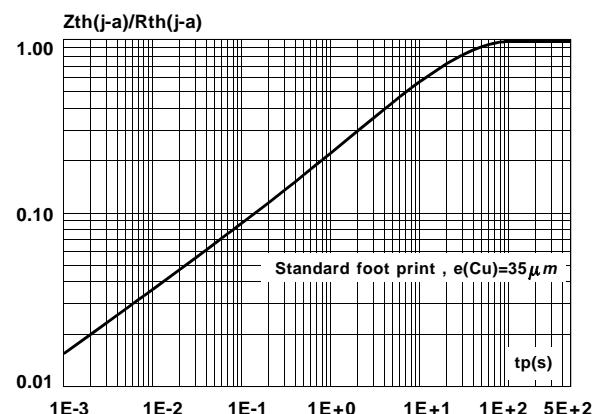


Fig.5 : Relative variation of gate trigger current and holding current versus junction temperature.

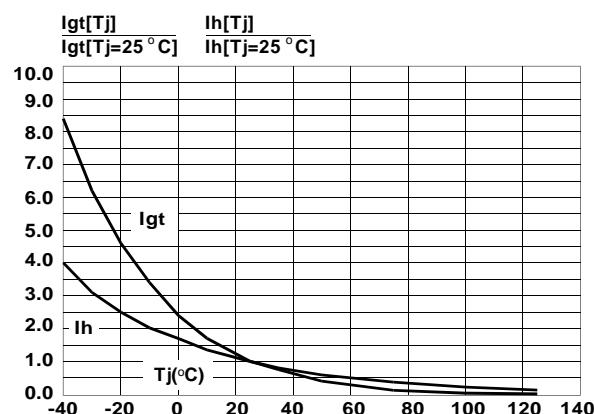


Fig.6 : Non repetitive surge peak on-state current versus number of cycles.

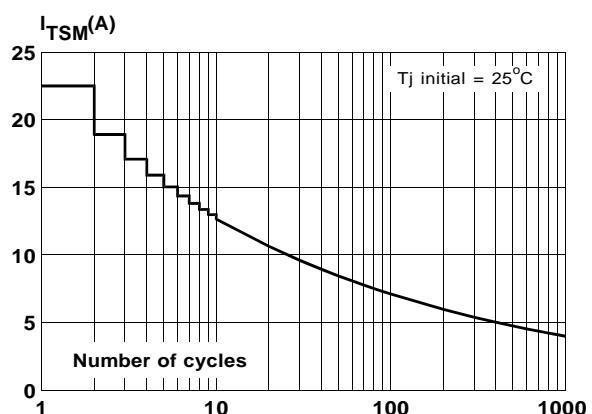


Fig.7 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $tp \leq 10\text{ms}$, and corresponding value of I^2t .

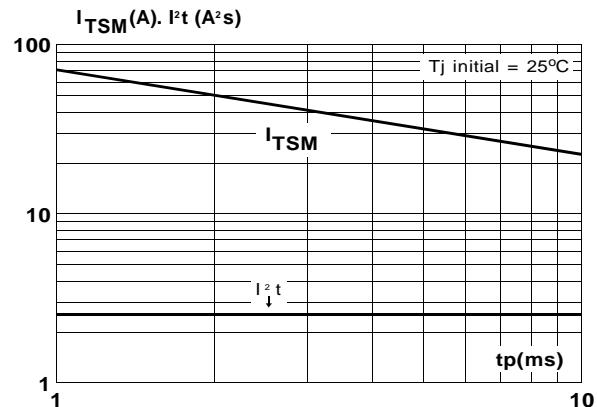
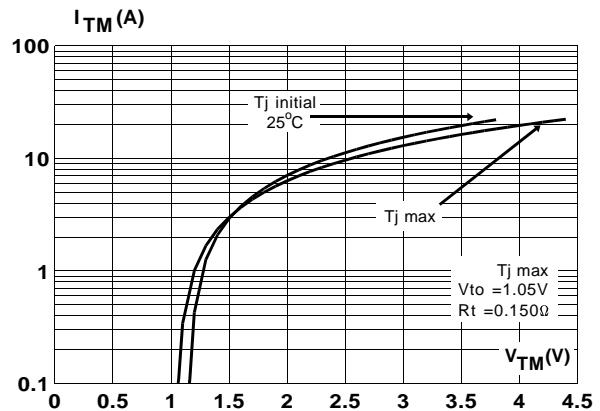
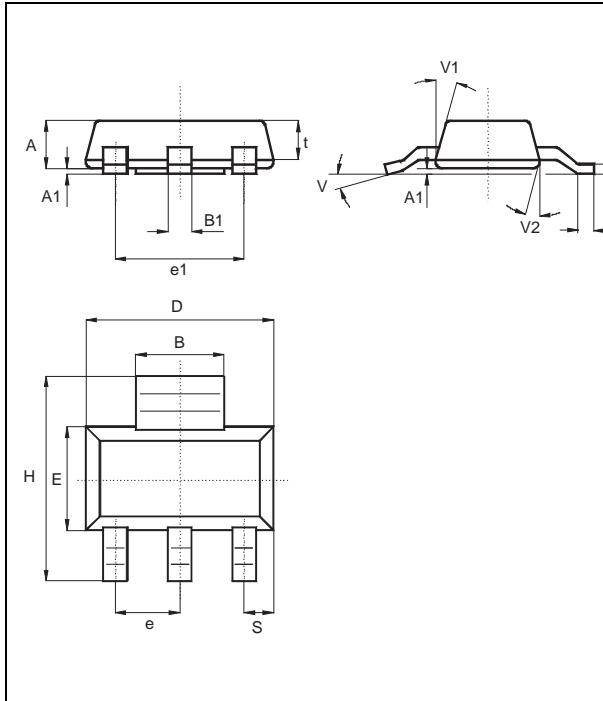


Fig.8 : On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA

SOT223 (Plastic)

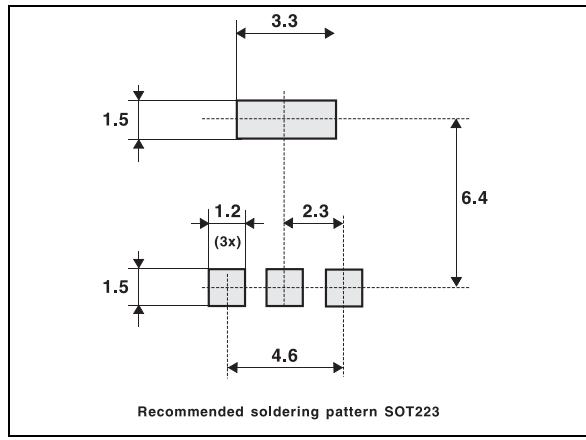


REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.50		1.70	0.059		0.067
A1	0.02		0.10	0.001		0.004
B	2.95		3.15	0.090		0.124
B1	0.65		0.85	0.026		0.033
C	0.25		0.35	0.010		0.014
D	6.30		6.70	0.248		0.264
e		2.3			0.091	
e1		4.6			0.181	
E	3.30		3.70	0.130		0.146
H	6.70		7.30	0.264		0.287
O	0.63	0.65	0.67	0.025	0.026	0.026
S	0.85		1.05	0.033		0.041
t	1.10		1.30	0.043		0.051
V	10° max					
V1	10° min 16°max					
V2	10° min 16°max					

Weight : 0.11 g

MARKING

FOOT PRINT



Type	Marking
X0202BN	X2B
X0202DN	X2D
X0202MN	X2M
X0202NN	X2N
X0203BN	X3B
X0203DN	X3D
X0203MN	X3M
X0203NN	X3N
X0205BN	X5B
X0205DN	X5D
X0205MN	X5M
X0205NN	X5N

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a trademark of STMicroelectronics

© 1998 STMicroelectronics – Printed in Italy – All Rights Reserved

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - France - Germany - Italy - Japan - Korea - Malaysia - Malta - Morocco - The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.

