

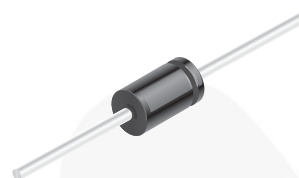


August 2016

UF4001 - UF4007 Fast Rectifiers

Features

- Low Forward Voltage Drop
- High Surge Current Capability
- High Reliability
- High Current Capability
- Glass-Passivated Junction



DO-41 (Plastic)
COLOR BAND DENOTES CATHODE

Ordering Information

Part Number	Top Mark	Package	Packing Method
UF4001	UF4001	DO-204AL (DO-41)	Tape and Reel
UF4002	UF4002	DO-204AL (DO-41)	Tape and Reel
UF4003	UF4003	DO-204AL (DO-41)	Tape and Reel
UF4004	UF4004	DO-204AL (DO-41)	Tape and Reel
UF4005	UF4005	DO-204AL (DO-41)	Tape and Reel
UF4006	UF4006	DO-204AL (DO-41)	Tape and Reel
UF4007	UF4007	DO-204AL (DO-41)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value							Unit
		UF 4001	UF 4002	UF 4003	UF 4004	UF 4005	UF 4006	UF 4007	
V_{RRM}	Maximum Repetitive Reverse Voltage	50	100	200	400	600	800	1000	V
$I_{F(AV)}$	Average Rectified Forward Current .375 " Lead Length at $T_A = 75^\circ\text{C}$	1.0							A
I_{FSM}	Non-Repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave	30							A
T_{STG}	Storage Temperature Range	-65 to +150							$^\circ\text{C}$
T_J	Operating Junction Temperature	-65 to +150							$^\circ\text{C}$

Thermal Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
P_D	Power Dissipation	2.08	W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	60	$^\circ\text{C/W}$
$R_{\theta JL}$	Thermal Resistance, Junction-to-Lead	30	$^\circ\text{C/W}$

Electrical Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Value							Unit
			UF 4001	UF 4002	UF 4003	UF 4004	UF 4005	UF 4006	UF 4007	
V _F	Maximum Forward Voltage	I _F = 1.0 A	1.0				1.7		V	
t _{rr}	Maximum Reverse Recovery Time	I _F = 0.5 A, I _R = 1.0 A, I _{RR} = 0.25 A	50				75		ns	
I _R	Maximum Reverse Current at Rated V _R	T _A = 25°C	10							μA
		T _A = 100°C	50							
C _T	Maximum Total Capacitance	V _R = 4.0 V, f = 1.0 MHz	17							pF

Typical Performance Characteristics

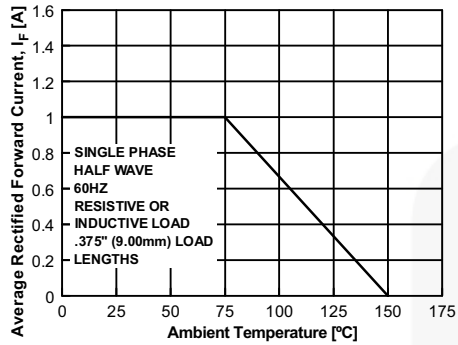


Figure 1. Forward Current Derating Curve

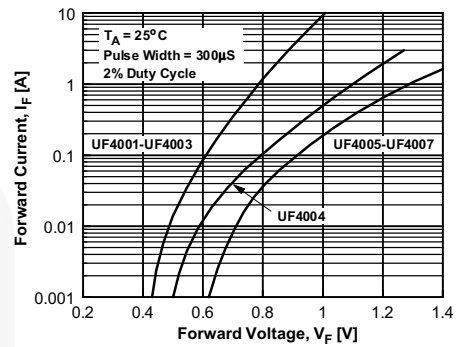


Figure 2. Forward Characteristics

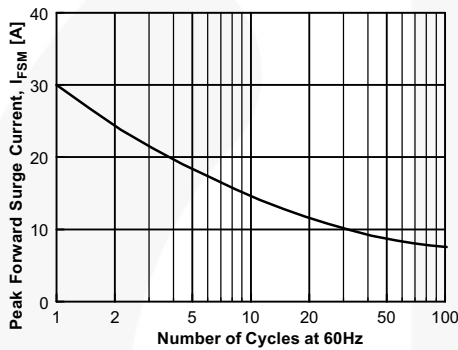


Figure 3. Non-Repetitive Surge Current

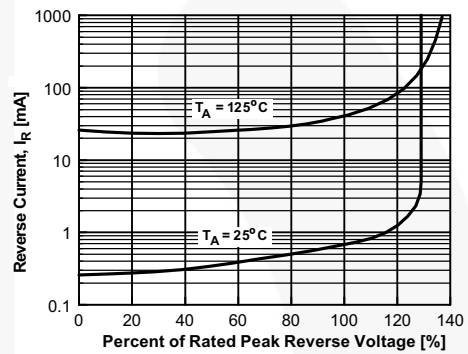


Figure 4. Reverse Characteristics

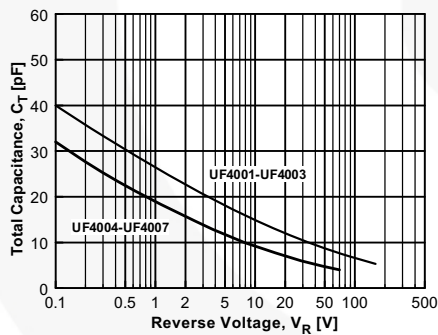
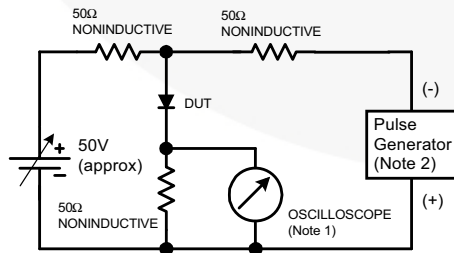
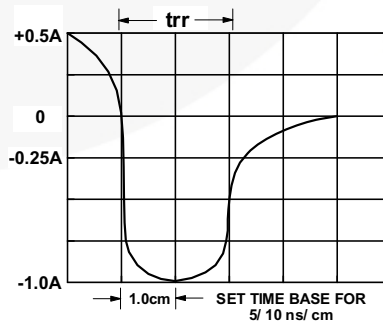


Figure 5. Typical Junction Capacitance



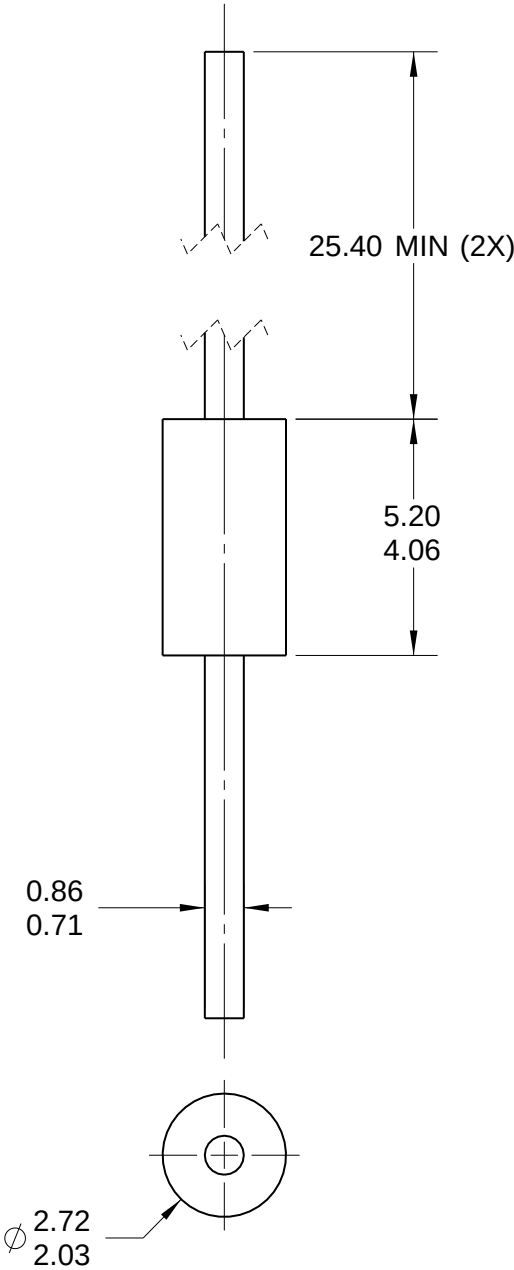
NOTES:
1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf.
2. Rise time = 10 ns max; Source impedance = 50 ohms.

Figure 6. Reverse Recovery Time Characteristic and Test Circuit Diagram



THIS DRAWING IS THE PROPERTY OF FAIRCHILD SEMICONDUCTOR CORPORATION. NO USE THEREOF SHALL BE MADE OTHER THAN AS A REFERENCE FOR PROPOSALS AS SUBMITTED TO FAIRCHILD SEMICONDUCTOR CORPORATION FOR JOBS TO BE EXECUTED IN CONFORMITY WITH SUCH PROPOSALS UNLESS THE CONSENT OF SAID FAIRCHILD SEMICONDUCTOR CORPORATION HAS PREVIOUSLY BEEN OBTAINED. NO PART OF THIS DRAWING SHALL BE COPIED OR DUPLICATED OR ITS CONTENTS DISCLOSED. THE INFORMATION CONTAINED ON THIS DRAWING IS CONFIDENTIAL AND PROPRIETARY.

REVISIONS			
NBR	DESCRIPTION	DATE	BY/APP'D
1	RELEASED TO DCC	29JUL08	HYANG/ SUZHOU
2	CHANGE 5.20 TO 5.21. CHANGE NOTE "B" ADDED PLASTCI BODY. REMOVE "GLASS" ON TITLE.	19SEP08	HYANG/ SUZHOU



NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE:
JEDEC DO-204 VARIATION AL.
- B) PACKAGE BODY CAN BE PLASTIC OR
HERMETICALLY SEALED GLASS .
- D) ALL DIMENSIONS ARE IN MILLIMETERS.
- E) DRAWING FILE NAME: DO41AREV2

APPROVALS		DATE		 FAIRCHILD SEMICONDUCTOR™	
DRAWN: BOBOY MALDO		19SEP08			
CHECKED: HENRY YANG					
APPROVED: BY HUANG					
APPROVED: HOWARD ALLEN					
				AXIAL LEADED, JEDEC DO204, VARIATION AL	
PROJECTION				SCALE 1:1	SIZE N/A
				DRAWING NUMBER MKT-DO41A	
				REV 2	
				FORMERLY: N/A	
				SHEET : 1 OF 1	

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local
Sales Representative