

# 1 Characteristics

				Va	lue	
Symbol	Par		TN805 TN815 TS820	TYN608	Uni	
I	RMS on-state current On-state rms cu	rrent	T <sub>C</sub> = 110 °C		0	•
I <sub>T(RMS)</sub>	(180° conduction angle)	(180° conduction angle)		8		A
	A	()	T <sub>C</sub> = 110 °C		-	
IT(AV)	Average on-state current (180° conduct		TO-220FPAB, T <sub>C</sub> = 91 °C	- 5		A
	Non repetitive surge peak on-state	t <sub>p</sub> = 8.3 ms	T 05 %0	73	100	
I <sub>TSM</sub>	current	t <sub>p</sub> = 10 ms	T <sub>j</sub> = 25 °C	70	95	A
l <sup>2</sup> t	I <sup>2</sup> t value for fusing	t <sub>p</sub> = 10 ms	T <sub>j</sub> = 25 °C	24.5	45	A <sup>2</sup> s
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , $t_r \le 100 \text{ ns}$	F = 60 Hz	T <sub>j</sub> = 125 °C	Ę	50	A/µ
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 125 °C		4	Α
P <sub>G(AV)</sub>	Average gate power dissipation		T <sub>j</sub> = 125 °C	1		W
T <sub>stg</sub>	Storage junction temperature range			-40 to	o +150	°C
Tj	Operating junction temperature range			-40 to	o +125	°C
V <sub>RGM</sub>	Maximum peak reverse gate voltage (f	or TN8x5 and	TYN608 only)		5	V

### Table 1. Absolute ratings (limiting values)

# Table 2. Sensitive electrical characteristics (T<sub>j</sub> = 25 °C, unless otherwise specified)

Symbol	Parameter			TS820	Unit
I <sub>GT</sub>	$V_{D} = 12 V, R_{I} = 140 \Omega$		Max.	200	μA
V <sub>GT</sub>	$= \sqrt{D} - 12 \sqrt{N} + 140 \sqrt{2}$		Max.	0.8	V
V <sub>RG</sub>	I <sub>RG</sub> = 10 μA		Min.	8	V
$V_{GD}$	$V_D$ = $V_{DRM}$ , $R_L$ = 3.3 k $\Omega$ , $R_{GK}$ = 220 $\Omega$	T <sub>j</sub> = 125 °C	Min.	0.1	V
Ι <sub>Η</sub>	$I_T$ = 50 mA, R <sub>GK</sub> = 1 k $\Omega$		Max.	5	mA
١L	$I_G$ = 1mA, $R_{GK}$ = 1 k $\Omega$		Max.	6	mA
dV/dt	$V_D$ = 65% $V_{DRM}$ , $R_{GK}$ = 220 $\Omega$	T <sub>j</sub> = 125 °C	Min.	5	V/µs
V <sub>TM</sub>	I <sub>TM</sub> = 16 A, t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C	Max.	1.6	V
V <sub>t0</sub>	Threshold voltage	T <sub>j</sub> = 125 °C	Max.	0.85	V
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125 °C	Max.	46	mΩ
I <sub>DRM</sub>	V	T <sub>j</sub> = 25 °C	Mox	5	μA
I <sub>RRM</sub>	$V_{\text{DRM}} = V_{\text{RRM}}, R_{\text{GK}} = 220 \ \Omega$	T <sub>j</sub> = 125 °C	Max.	1	mA

Symbol	Parameter			TN805	TN815	TYN608	Unit
I <sub>GT</sub>			Min.	0.5	2	2	mA
GI	$V_D$ = 12 V, $R_L$ = 140 $\Omega$		Max.	5	15	15	III/A
$V_{GT}$	Max.	1			1.3		V
$V_{GD}$	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$ $T_j = 125 \text{ °C}$ Min.				0.2		V
Ι <sub>Η</sub>	I <sub>T</sub> = 100 mA, gate open		Max.	25	40	30	mA
١L	I <sub>G</sub> = 1.2 I <sub>GT</sub>		Max.	30	50	70	mA
dV/dt	V <sub>D</sub> = 67% V <sub>DRM</sub> , gate open	T <sub>j</sub> = 125 °C	Min.	50	150	150	V/µs
V <sub>TM</sub>	I <sub>TM</sub> = 16 A, t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C	Max.		1.6		V
V <sub>t0</sub>	Threshold voltage	T <sub>j</sub> = 125 °C	Max.	0.85			V
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125 °C	Max.	46		mΩ	
I <sub>DRM</sub>		T <sub>j</sub> = 25 °C	May		5		μA
I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub>	T <sub>j</sub> = 125 °C	Max.	2			mA

Table 3. Standard electrical characteristics ( $T_j$  = 25 °C, unless otherwise specified)

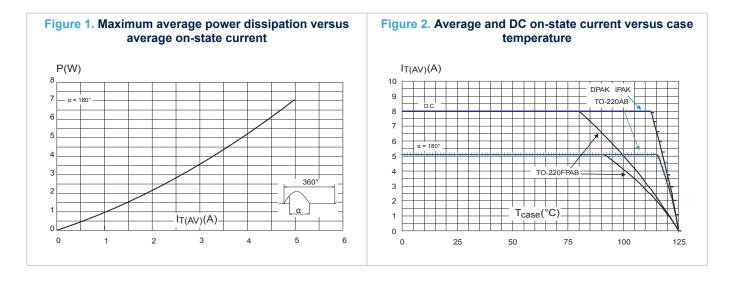
### Table 4. Thermal resistance (maximum values)

Symbol	Parameter				Unit
R <sub>th(j-c)</sub>	Junction to case (DC)		IPAK / DPAK / TO-220AB	1.3	°C/W
<b>,</b>			TO-220FPAB	4.6	
	Junction to ambient	S <sup>(1)</sup> = 0.5 cm <sup>2</sup>	DPAK	70	
R <sub>th(j-a)</sub>	Junction to ambient	·	TO-220AB / TO-220FPAB	60	°C/W
			IPAK	100	

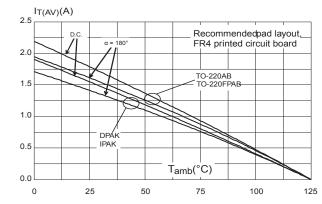
1. S = Copper surface under tab



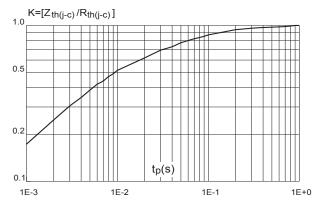
#### 1.1 **Characteristics curves**







to case versus pulse duration



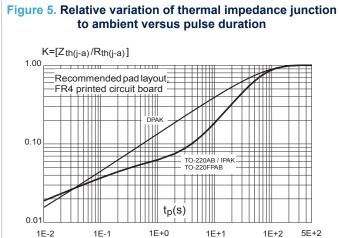
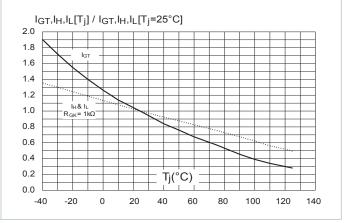


Figure 6. Relative variation of gate trigger current and holding current versus junction temperature for TS820





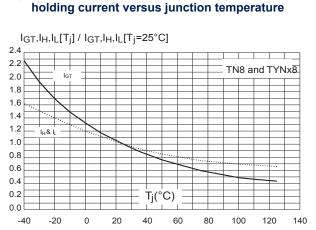
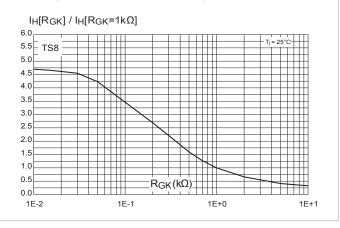


Figure 7. Relative variation of gate trigger current and

Figure 8. Relative variation of holding current versus gate-cathode resistance (typical values)



#### Figure 9. Relative variation of dV/dt immunity versus gatecathode resistance (typical values) for TS820

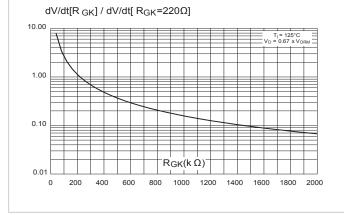
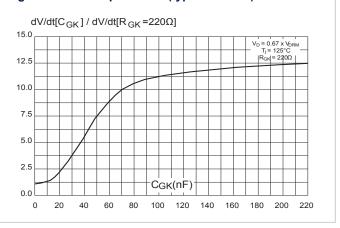
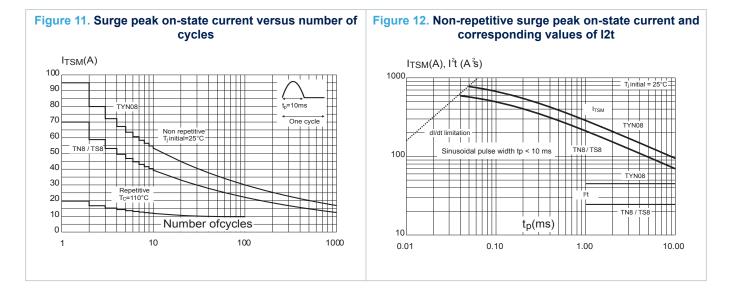
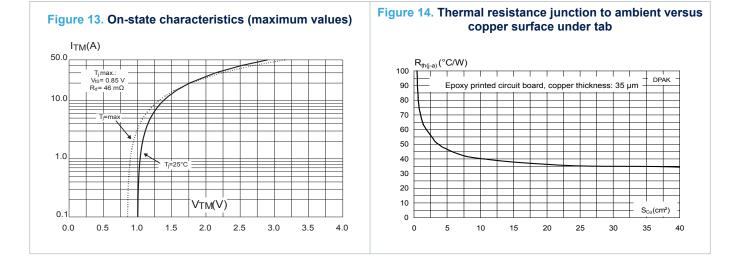


Figure 10. Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values) for TS820











# 2 Package information

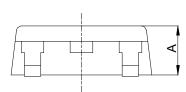
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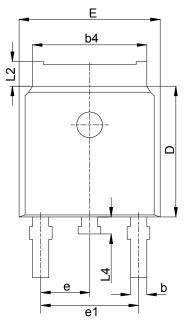
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

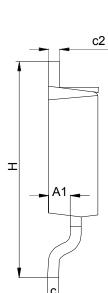
# 2.1 DPAK package information

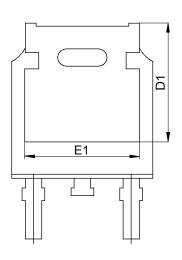
- Molding compouned resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free package leads plating

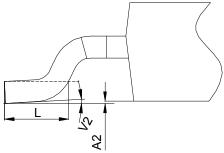
#### Figure 15. DPAK package outline











DS2118 - Rev 9							
Downloaded from	Arrow.com.						

	Dimensions								
Ref.		Millimeters			Inches <sup>(1)</sup>				
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А	2.18		2.40	0.0858		0.0945			
A1	0.90		1.10	0.0354		0.0433			
A2	0.03		0.23	0.0012		0.0091			
b	0.64		0.90	0.0252		0.354			
b4	4.95		5.46	0.1949		0.2150			
С	0.46		0.61	0.0181		0.0240			
c2	0.46		0.60	0.0181		0.0236			
D	5.97		6.22	0.2350		0.2449			
D1	4.95		5.60	0.1949		0.2205			
Е	6.35		6.73	0.2500		0.2650			
E1	4.32		5.50	0.1701		0.2165			
е		2.286			0.0900				
e1	4.40		4.70	0.1732		0.1850			
Н	9.35		10.40	0.3681		0.4094			
L	1.00		1.78	0.0394		0.0701			
L2			1.27			0.0500			
L4	0.60		1.02	0.0236		0.0402			
V2 <sup>(2)</sup>	-8°		+8°	-8°		+8°			

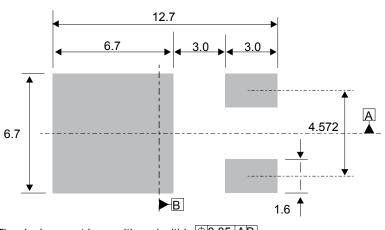
#### Table 5. DPAK package mechanical data

1. Dimensions in inches are given for reference only

2. Degree

*Note:* This package drawing may slightly differ from the physical package. However, all the specified dimensions are guaranteed.



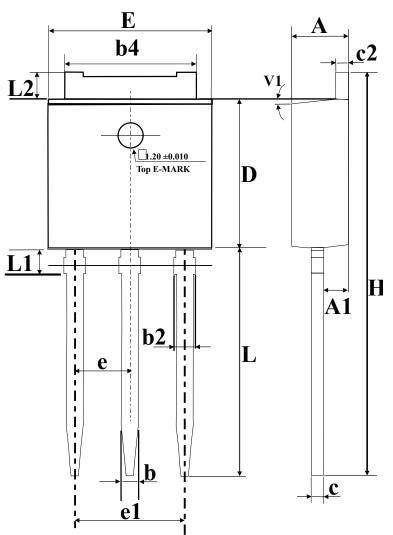


The device must be positioned within  $\bigcirc 0.05 |A|B$ 



# 2.2 IPAK package information

- Molding compouned resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free package leads plating





			Dimer	nsions					
Ref.		MillimetersInches (for reference only)							
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А	2.20		2.40	0.086		0.094			
A1	0.90		1.10			0.035			
b	0.64		0.90	0.025		0.035			
b2			0.95			0.037			
b4	5.20		5.43						
С	0.45		0.60						
c2	0.46		0.60						
D	6		6.20						
E	6.40		6.70	0.252		0.263			
е		2.28			0.090				
e1	4.40		4.60	0.173		0.181			
Н		16.10			0.634				
L	9		9.60	0.354		0.377			
L1	0.8		1.20	0.031		0.047			
L2		0.80	1.25		0.031	0.049			
V1	10°				10°				

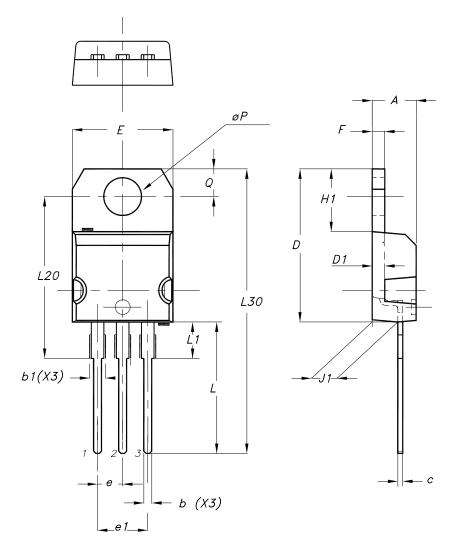
## Table 6. IPAK package mechanical data



# 2.3 TO-220AB package information

- Molding compouned resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free package leads plating
- Cooling method: by conduction (C)
- Recommended torque value: 0.55 N⋅m
- Maximum torque value: 0.70 N·m





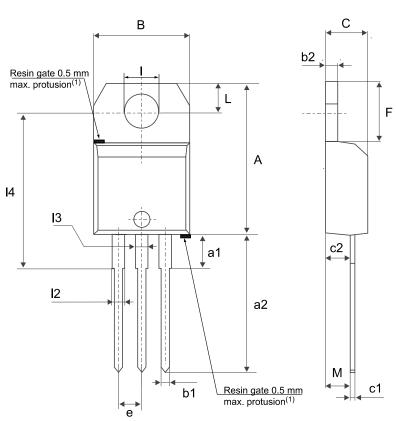
	Dimensions						
Ref.	Milli	meters	Inches (for re	ference only)			
	Min.	Max.	Min.	Max.			
A	4.40	4.60	0.173	0.181			
b	0.61	0.88	0.240	0.035			
b1	1.14	1.55	0.045	0.061			
С	0.48	0.70	0.019	0.028			
D	15.25	15.75	0.600	0.620			
D1	1.2	7 typ.	0.050 typ.				
E	10.00	10.40	0.394	0.409			
e	2.40	2.70	0.094	0.106			
e1	4.95	5.15	0.195	0.203			
F	1.23	1.32	0.048	0.052			
H1	6.20	6.60	0.244	0.260			
J1	2.40	2.72	0.094	0.107			
L	13.00	14.00	0.512	0.551			
L1	3.50	3.93	0.138	0.155			
L20	16.4	ł0 typ.	0.646	6 typ.			
L30	28.9	90 typ.	1.138	B typ.			
θΡ	3.75	3.85	0.148	0.152			
Q	2.65	2.95	0.104	0.116			

## Table 7. TO-220AB package mechanical data

# 2.4 TO-220AB package information

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- Molding compound resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free plating package leads
- Recommended torque: 0.4 to 0.6 N·m



### Figure 19. TO-220AB package outline

(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

	Dimensions								
Ref.	Millimeters				Inches <sup>(1)</sup>				
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А	15.20		15.90	0.5984		0.6260			
a1		3.75			0.1476				
a2	13.00		14.00	0.5118		0.5512			
В	10.00		10.40	0.3937		0.4094			
b1	0.61		0.88	0.0240		0.0346			
b2	1.23		1.32	0.0484		0.0520			
С	4.40		4.60	0.1732		0.1811			
c1	0.49		0.70	0.0193		0.0276			
c2	2.40		2.72	0.0945		0.1071			
е	2.40		2.70	0.0945		0.1063			
F	6.20		6.60	0.2441		0.2598			
I	3.73		3.88	0.1469		0.1528			
L	2.65		2.95	0.1043		0.1161			
12	1.14		1.70	0.0449		0.0669			
13	1.14		1.70	0.0449		0.0669			
14	15.80	16.40	16.80	0.6220	0.6457	0.6614			
М		2.6			0.1024				

## Table 8. TO-220AB package mechanical data

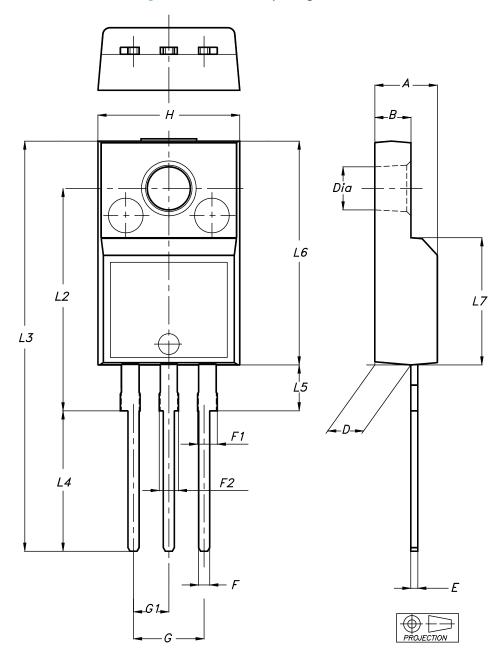
1. Inch dimensions are for reference only.



# 2.5 TO-220FPAB package information

- Epoxy meets UL94, V0
- Recommended torque: 0.4 to 0.6 N·m

### Figure 20. TO-220FPAB package outline



	Dimensions						
Ref.	Millin	neters	Inch	ies			
	Min.	Max.	Min.	Max.			
A	4.40	4.60	0.1739	0.1818			
В	2.5	2.7	0.0988	0.1067			
D	2.50	2.75	0.0988	0.1087			
E	0.45	0.70	0.0178	0.0277			
F	0.75	1.0	0.0296	0.0395			
F1	1.15	1.70	0.0455	0.0672			
F2	1.15	1.70	0.0455	0.0672			
G	4.95	5.20	0.1957	0.2055			
G1	2.40	2.70	0.0949	0.1067			
Н	10.00	10.40	0.3953	0.4111			
L2	16.0	0 typ.	0.6324	4 typ.			
L3	28.60	30.60	1.1304	1.2095			
L4	9.80	10.6	0.3874	0.4190			
L5	2.90	3.60	0.1146	0.1423			
L6	15.90	16.40	0.6285	0.6482			
L7	9.00	9.30	0.3557	0.3676			
Dia	3.0	3.20	0.1186	0.1265			

## Table 9. TO-220FPAB package mechanical data

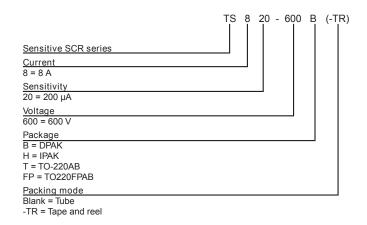
# **3** Ordering information

### Figure 21. TN8 series

	ΤN	8	05	- 60	0 E	Ŗ	-TR
Standard SCR series							
Current							
8 = 8 A							
Sensitivity							
05 = 5 mA							
15 = 15 mA							
Voltage							
600 = 600 V							
800 = 800 V							
Package							
B = DPAK H = IPAK							
Packing mode							

Blank = Tube -TR = Tape and reel

### Figure 22. TS8 series



#### Figure 23. TYNx08 series

	IYN	6	80	RG
Standard SCR series				
Voltage				
6 = 600 V				
Current				
8 = 8 A				
Packing mode				
RG = Tube				_

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN805-600B-TR	TN805600	DPAK	0.3 g	2500	Tape and reel
TN815-600B-TR	TN815600	DPAK	0.3 g	2500	Tape and reel
TN815-800B-TR	TN815800	DPAK	0.3 g	2500	Tape and reel
TN815-800H	TN815800	IPAK	0.4 g	75	Tube
TS820-600B	TS820600	DPAK	0.3 g	75	Tube
TS820-600B-TR	TS820600	DPAK	0.3 g	2500	Tape and reel
TS820-600H	TS820600	IPAK	0.4 g	75	Tube
TS820-600T	TS820600T	TO-220AB	2.3 g	50	Tube
TS820-600FP	TS820600	TO-220FPAB	2.0 g	50	Tube
TYN608RG	TYN608	TO-220AB	2.3 g	50	Tube

## Table 10. Ordering information



# **Revision history**

Date	Revision	Changes	
Apr-2002	4A	Last update.	
13-Feb-2006	5	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.	
22-Jan-2010	6	Alpha definition updated in Figure 1. Thermal resistance, junction to case, updated in Table 5.	
10-Oct-2011	7	Added TO-220FPAB package. Removed 700 V and 1000 V products.	
14-May-2014	8	Updated DPAK and IPAK package information and reformatted to current standard.	
03-Nov-2021	9	Added TN815-800H product information. Minor text changes.	

### Table 11. Document revision history

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