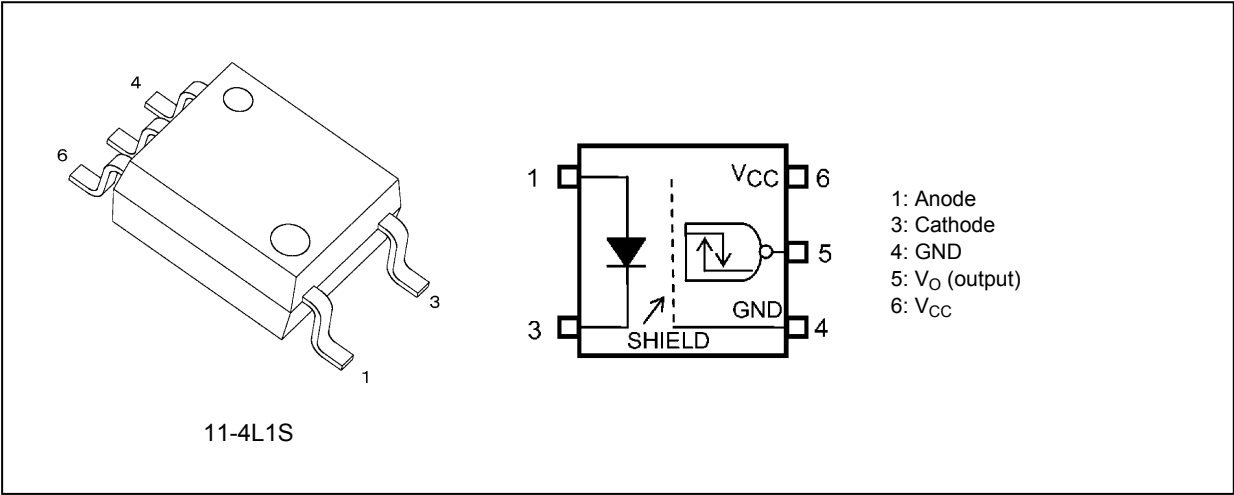
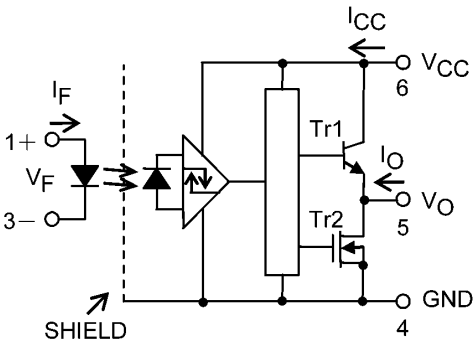


4. Packaging and Pin Assignment



5. Internal Circuit (Note)



Note: A 0.1-μF bypass capacitor must be connected between pin 6 and pin 4.

6. Principle of Operation

6.1. Truth Table

| Input | LED | Output |
|-------|-----|--------|
| H | ON | L |
| L | OFF | H |

6.2. Mechanical Parameters

| Characteristics | Min | Unit |
|------------------------------|-----|------|
| Creepage distances | 5.0 | mm |
| Clearance | 5.0 | |
| Internal isolation thickness | 0.4 | |

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25\text{ }^{\circ}\text{C}$)

| | Characteristics | Symbol | Note | Rating | Unit |
|----------|--|-------------------------------|----------|------------|------------------------|
| LED | Input forward current | I_F | | 20 | mA |
| | Input forward current derating ($T_a \geq 116\text{ }^{\circ}\text{C}$) | $\Delta I_F / \Delta T_a$ | | -0.6 | mA/ $^{\circ}\text{C}$ |
| | Peak transient input forward current | I_{FPT} | (Note 1) | 1 | A |
| | Peak transient input forward current derating ($T_a \geq 110\text{ }^{\circ}\text{C}$) | $\Delta I_{FPT} / \Delta T_a$ | | -25 | mA/ $^{\circ}\text{C}$ |
| | Input power dissipation | P_D | | 40 | mW |
| | Input power dissipation derating ($T_a \geq 110\text{ }^{\circ}\text{C}$) | $\Delta P_D / \Delta T_a$ | | -1.0 | mW/ $^{\circ}\text{C}$ |
| | Input reverse voltage | V_R | | 5 | V |
| Detector | Output current ($T_a \leq 25\text{ }^{\circ}\text{C}$) | I_O | | 25/-15 | mA |
| | Output current ($T_a = 125\text{ }^{\circ}\text{C}$) | I_O | | 5/-5 | |
| | Output voltage | V_O | | -0.5 to 20 | V |
| | Output power dissipation ($T_a \leq 25\text{ }^{\circ}\text{C}$) | P_O | | 75 | mW |
| | Output power dissipation derating ($T_a \geq 25\text{ }^{\circ}\text{C}$) | $\Delta P_O / \Delta T_a$ | | -0.6 | mW/ $^{\circ}\text{C}$ |
| | Supply voltage | V_{CC} | | -0.5 to 20 | V |
| Common | Operating temperature | T_{opr} | | -40 to 125 | $^{\circ}\text{C}$ |
| | Storage temperature | T_{stg} | | -55 to 125 | |
| | Lead soldering temperature (10 s) | T_{sol} | | 260 | |
| | Isolation voltage (AC, 60 s, R.H. $\leq 60\%$) | BV_S | (Note 2) | 3750 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Pulse width (PW) $\leq 1\text{ }\mu\text{s}$, 300 pps

Note 2: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

8. Recommended Operating Conditions (Note)

| Characteristics | Symbol | Note | Min | Typ. | Max | Unit |
|-------------------------|--------------|----------|-----|------|-----|--------------------|
| Input on-state current | $I_{F(ON)}$ | (Note 1) | 2 | — | 10 | mA |
| Input off-state voltage | $V_{F(OFF)}$ | | 0 | — | 0.8 | V |
| Supply voltage | V_{CC} | (Note 2) | 3 | — | 20 | |
| Operating temperature | T_{opr} | (Note 2) | -40 | — | 125 | $^{\circ}\text{C}$ |

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.

Note: A ceramic capacitor (0.1 μF) should be connected between pin 6 and pin 4 to stabilize the operation of a high-gain linear amplifier. Otherwise, this photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

Note 1: The rise and fall times of the input on-current should be less than 0.5 μs .

Note 2: Denotes the operating range, not the recommended operating condition.

9. Electrical Characteristics (Note)

(Unless otherwise specified, $T_a = -40$ to 125 °C, $V_{CC} = 3$ to 20 V)

| Characteristics | Symbol | Note | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
|---|---------------------------|----------|--------------|---|------|------|------|-------|
| Input forward voltage | V_F | | — | $I_F = 10$ mA, $T_a = 25$ °C | 1.45 | 1.55 | 1.70 | V |
| Input forward voltage temperature coefficient | $\Delta V_F / \Delta T_a$ | | — | $I_F = 10$ mA | — | -2.0 | — | mV/°C |
| Input reverse current | I_R | | — | $V_R = 5$ V, $T_a = 25$ °C | — | — | 10 | μA |
| Input capacitance | C_t | | — | $V = 0$ V, $f = 1$ MHz | — | 60 | — | pF |
| Low-level output voltage | V_{OL} | | Fig. 12.1.1 | $I_O = 3.5$ mA, $I_F = 5$ mA | — | 0.1 | 0.6 | V |
| High-level output voltage | V_{OH} | (Note 1) | Fig. 12.1.2 | $V_{CC} = 3$ V, $I_O = -2.6$ mA, $V_F = 0.8$ V | 1.78 | 2.1 | — | |
| | | | | $V_{CC} = 20$ V, $I_O = -2.6$ mA, $V_F = 0.8$ V | 17.4 | 19.1 | — | |
| Low-level supply current | I_{CCL} | | Fig. 12.1.3 | $V_{CC} = 3.6$ V, $I_F = 5$ mA | — | 1.4 | 3.0 | mA |
| | | | | $V_{CC} = 20$ V, $I_F = 5$ mA | — | 1.5 | 3.0 | |
| High-level supply current | I_{CCH} | | Fig. 12.1.4 | $V_{CC} = 3.6$ V, $V_F = 0$ V | — | 1.9 | 3.0 | |
| | | | | $V_{CC} = 20$ V, $V_F = 0$ V | — | 2.0 | 3.0 | |
| Low-level short-circuit output current | I_{OSL} | (Note 2) | Fig. 12.1.5 | $V_{CC} = V_O = 3.6$ V, $I_F = 5$ mA | 15 | 100 | — | |
| | | | | $V_{CC} = V_O = 20$ V, $I_F = 5$ mA | 20 | 140 | — | |
| High-level short-circuit output current | I_{OSH} | (Note 2) | Fig. 12.1.6 | $V_{CC} = 3.6$ V, $V_F = 0$ V, $V_O = \text{GND}$ | — | -14 | -5 | |
| | | | | $V_{CC} = 20$ V, $V_F = 0$ V, $V_O = \text{GND}$ | — | -24 | -10 | |
| Threshold input current (H/L) | I_{FHL} | | — | $I_O = 3.5$ mA, $V_O < 0.4$ V | — | 0.6 | 1.6 | |
| Threshold input voltage (L/H) | V_{FLH} | | — | $I_O = -2.6$ mA, $V_O > 2.4$ V | 0.8 | — | — | V |
| Input current hysteresis | I_{HYS} | | — | $V_{CC} = 5$ V | — | 0.05 | — | mA |

Note: All typical values are at $V_{CC} = 5$ V, $T_a = 25$ °C.

Note 1: $V_{OH} = V_{CC} - V_O$ (V)

Note 2: Duration of output short circuit time should not exceed 10 ms.

10. Isolation Characteristics (Unless otherwise specified, $T_a = 25$ °C)

| Characteristics | Symbol | Note | Test Conditions | Min | Typ. | Max | Unit |
|-------------------------------------|--------|----------|---------------------------------|-----------|-----------|-----|------|
| Total capacitance (input to output) | C_S | (Note 1) | $V_S = 0$ V, $f = 1$ MHz | — | 0.8 | — | pF |
| Isolation resistance | R_S | (Note 1) | $V_S = 500$ V, R.H. ≤ 60 % | 10^{12} | 10^{14} | — | Ω |
| Isolation voltage | BV_S | (Note 1) | AC, 60 s | 3750 | — | — | Vrms |

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

11. Switching Characteristics (Note)

(Unless otherwise specified, $T_a = -40$ to 125 °C, $V_{CC} = 3$ to 20 V)

| Characteristics | Symbol | Note | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
|---|-----------------------|------|--------------|--|----------|----------|-----|-------------|
| Propagation delay time (L/H) | t_{pLH} | | Fig. 12.1.7 | $I_F = 3 \rightarrow 0$ mA | — | 110 | 250 | ns |
| Propagation delay time (H/L) | t_{pHL} | | | $I_F = 0 \rightarrow 3$ mA | — | 90 | 250 | |
| Pulse width distortion | $ t_{pHL} - t_{pLH} $ | | | $I_F = 3$ mA | — | 20 | 70 | |
| Propagation delay skew (device to device) | t_{psk} | | | | -130 | — | 130 | |
| Rise time | t_r | | | $I_F = 3 \rightarrow 0$ mA, $V_{CC} = 5$ V | — | 15 | 75 | |
| Fall time | t_f | | | $I_F = 0 \rightarrow 3$ mA, $V_{CC} = 5$ V | — | 12 | 75 | |
| High-level common-mode transient immunity | CM_H | | Fig. 12.1.8 | $V_{CM} = 1000 V_{p-p}$, $I_F = 0$ mA, $V_{CC} = 20$ V, $T_a = 25$ °C | ± 20 | ± 25 | — | kV/ μ s |
| Low-level common-mode transient immunity | CM_L | | | $V_{CM} = 1000 V_{p-p}$, $I_F = 5$ mA, $V_{CC} = 20$ V, $T_a = 25$ °C | ± 20 | ± 25 | — | |

Note: All typical values are at $V_{CC} = 5$ V, $T_a = 25$ °C.

12. Test Circuits and Characteristics Curves

12.1. Test Circuits

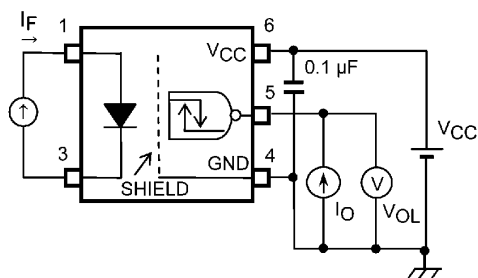


Fig. 12.1.1 VOL Test Circuit

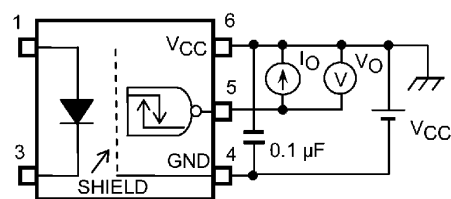


Fig. 12.1.2 VOH Test Circuit

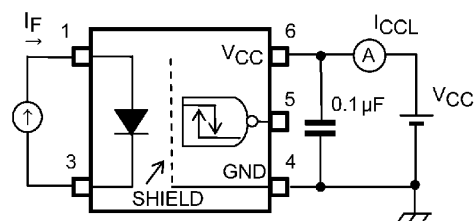


Fig. 12.1.3 ICCL Test Circuit

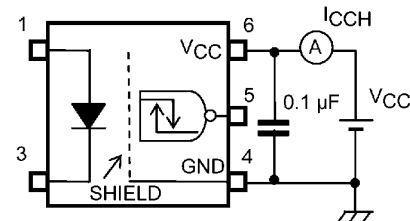


Fig. 12.1.4 ICCH Test Circuit

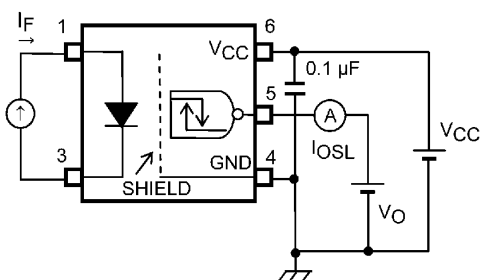


Fig. 12.1.5 IOSL Test Circuit

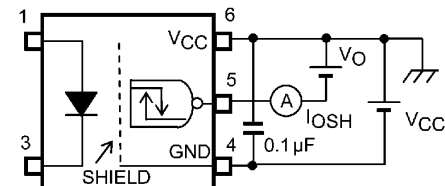
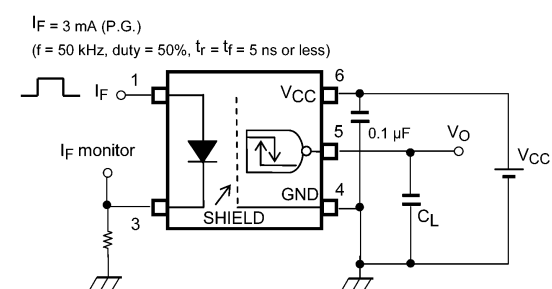


Fig. 12.1.6 IOSH Test Circuit



*C_L includes probe and stray capacitance.

P.G.: Pulse generator

Fig. 12.1.7 Switching Time Test Circuit and Waveform

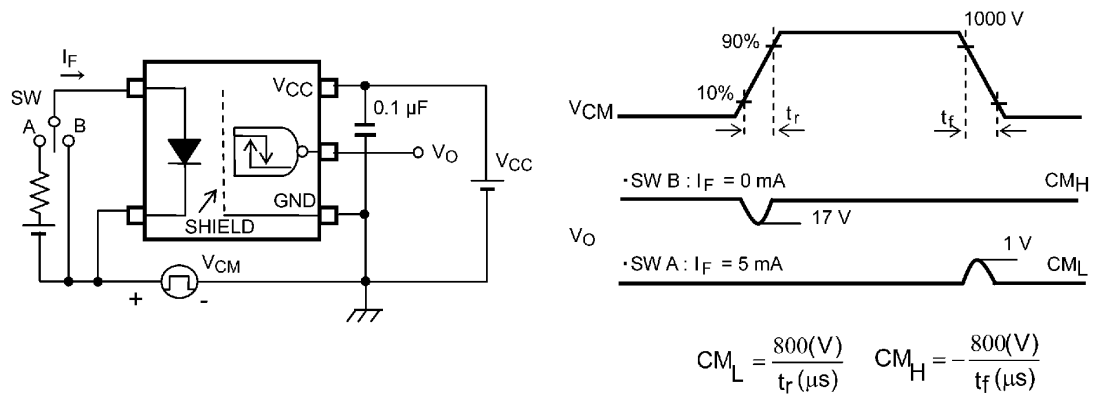


Fig. 12.1.8 Common-Mode Transient Immunity Test Circuit and Waveform

12.2. Characteristics Curves (Note)

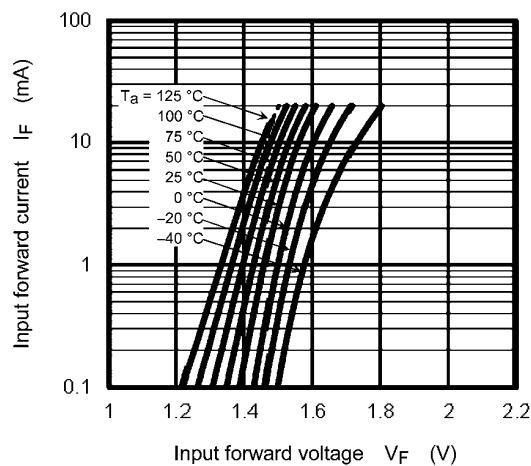


Fig. 12.2.1 $I_F - V_F$

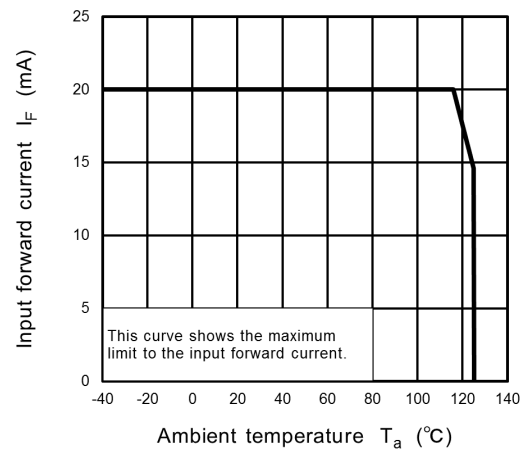


Fig. 12.2.2 $I_F - T_a$

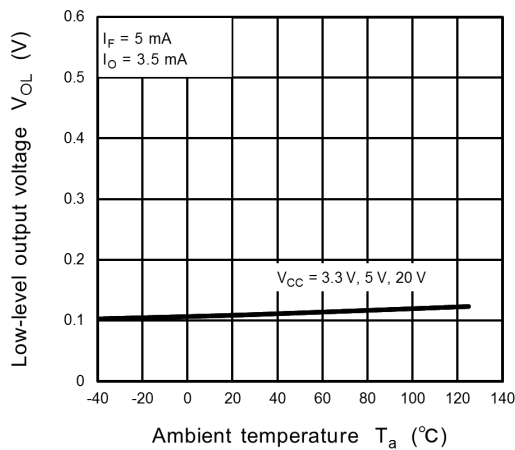


Fig. 12.2.3 $V_{OL} - T_a$

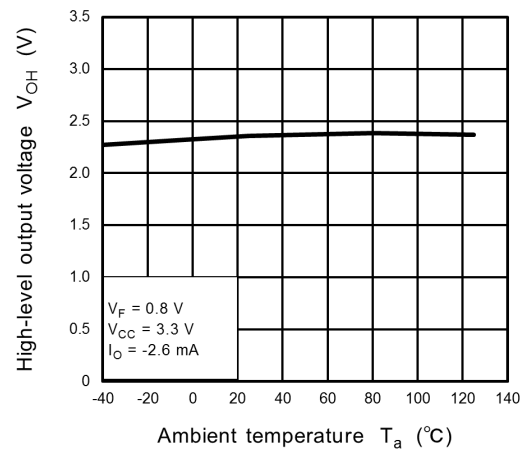


Fig. 12.2.4 $V_{OH} - T_a$

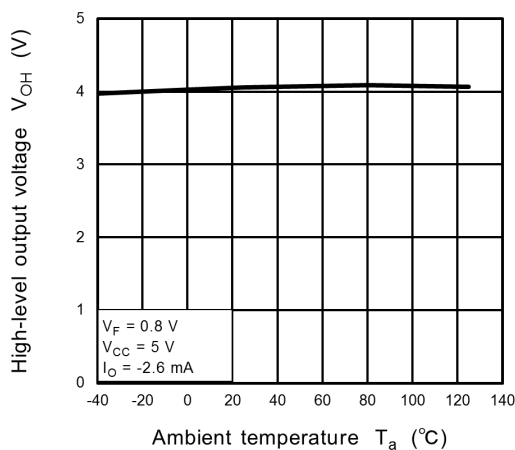


Fig. 12.2.5 $V_{OH} - T_a$

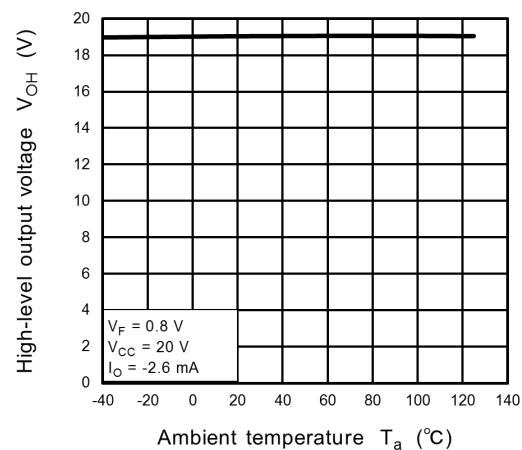


Fig. 12.2.6 $V_{OH} - T_a$

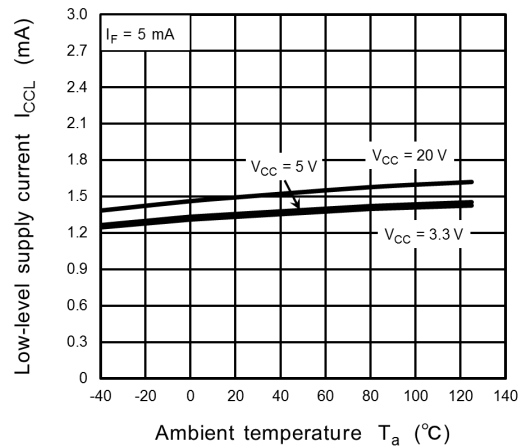


Fig. 12.2.7 $I_{CCL} - T_a$

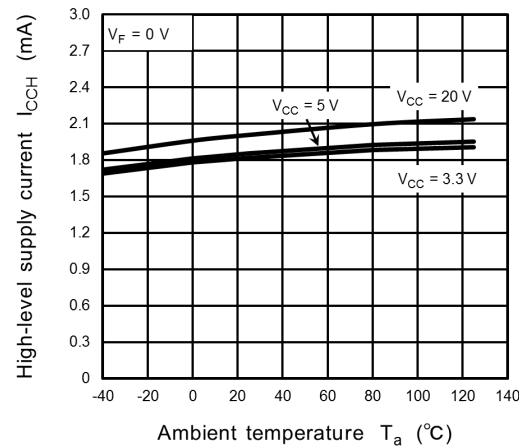


Fig. 12.2.8 $I_{CCH} - T_a$

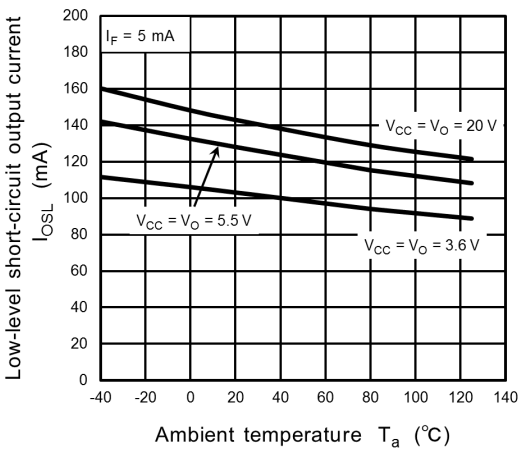


Fig. 12.2.9 $I_{OL} - T_a$

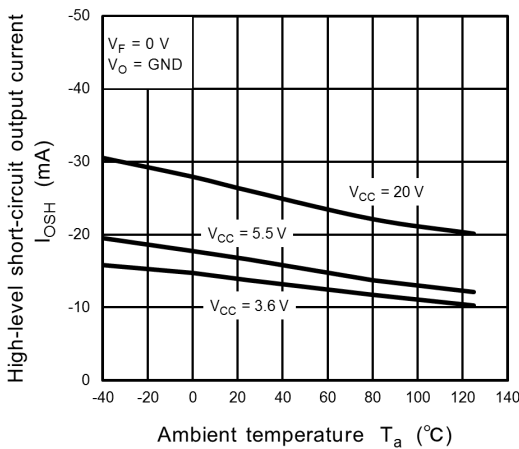


Fig. 12.2.10 $I_{OH} - T_a$

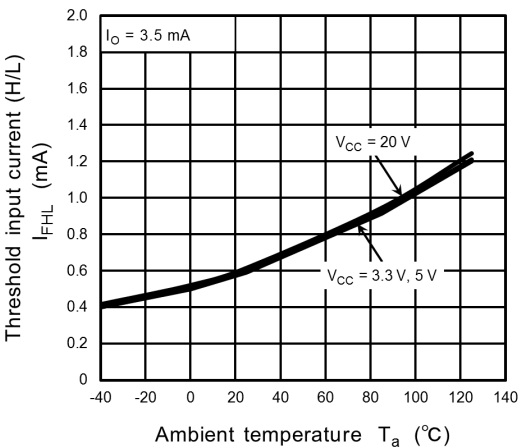


Fig. 12.2.11 $I_{FHL} - T_a$

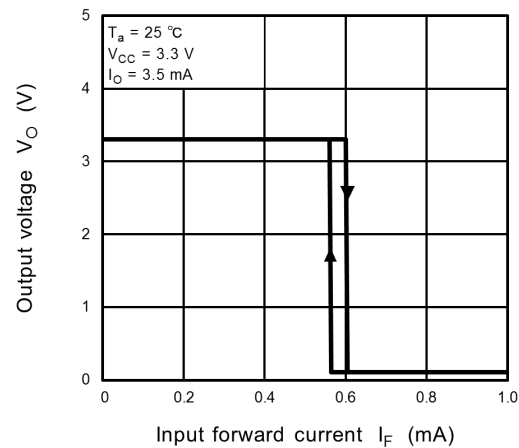


Fig. 12.2.12 $V_O - I_F$

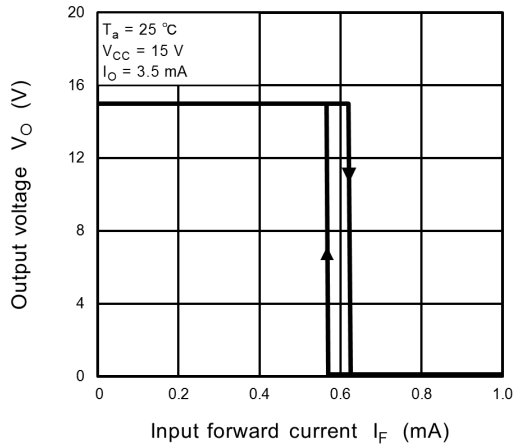


Fig. 12.2.13 $V_O - I_F$

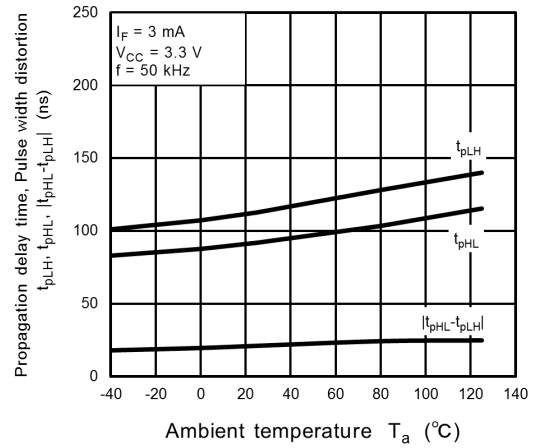


Fig. 12.2.14 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - T_a$

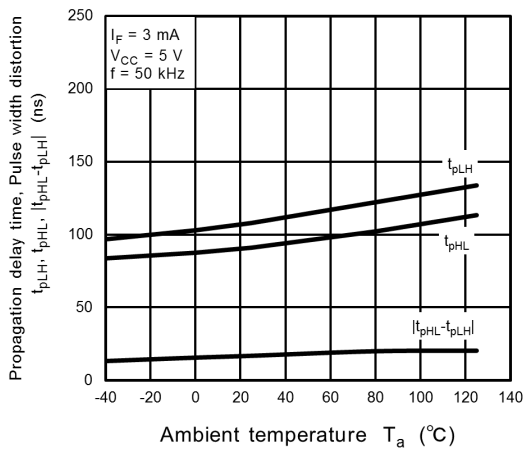


Fig. 12.2.15 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - T_a$

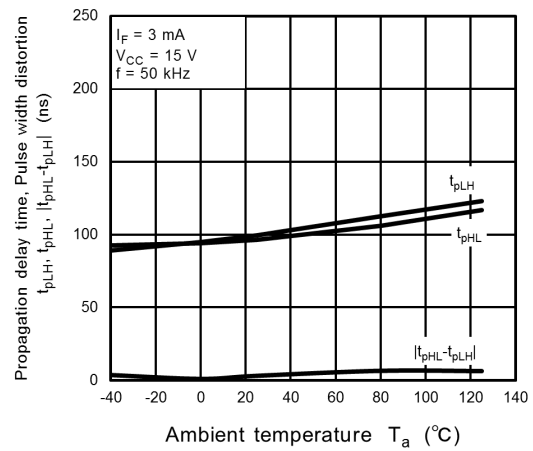


Fig. 12.2.16 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - T_a$

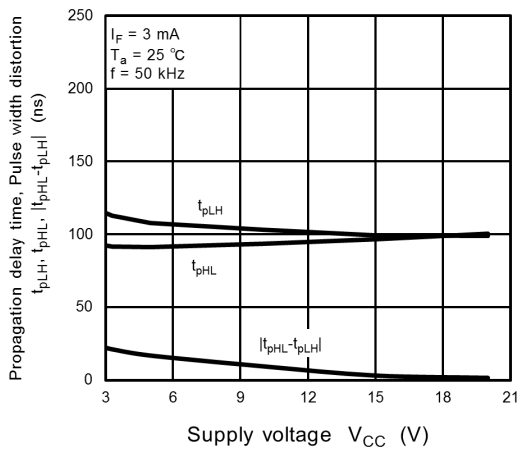


Fig. 12.2.17 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - V_{CC}$

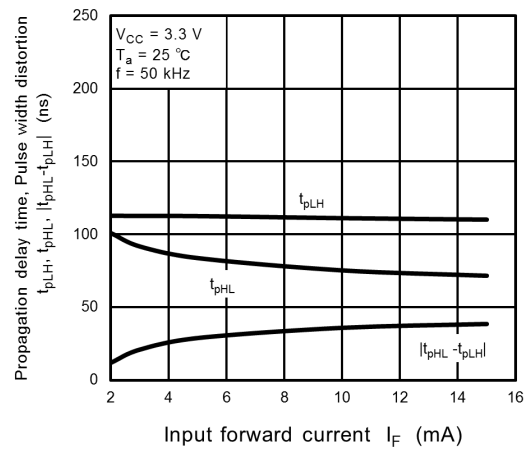


Fig. 12.2.18 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - I_F$

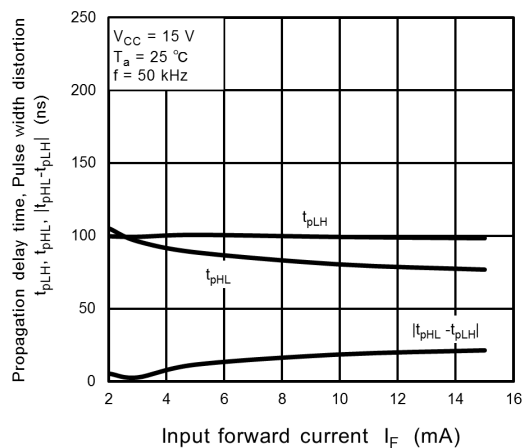


Fig. 12.2.19 $t_{pHL}, t_{pLH}, |t_{pHL} - t_{pLH}| - I_F$

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

13. Soldering and Storage

13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

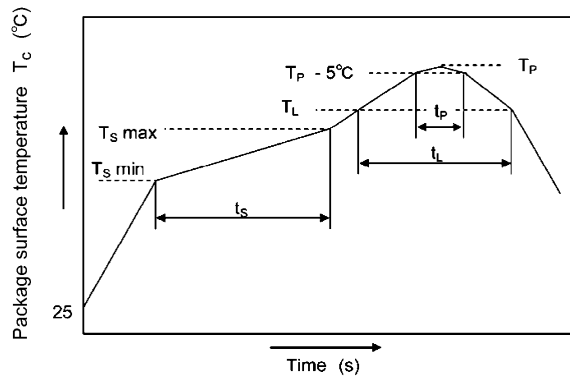
- When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



| | Symbol | Min | Max | Unit |
|--|--------|-----|-----|------|
| Preheat temperature | T_S | 150 | 200 | °C |
| Preheat time | t_S | 60 | 120 | s |
| Ramp-up rate (T_L to T_P) | | | 3 | °C/s |
| Liquidus temperature | T_L | 217 | | °C |
| Time above T_L | t_L | 60 | 150 | s |
| Peak temperature | T_P | | 260 | °C |
| Time during which T_c is between ($T_P - 5$) and T_P | t_P | | 30 | s |
| Ramp-down rate (T_P to T_L) | | | 6 | °C/s |

An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

- When using soldering flow

Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260 °C within 10 seconds is recommended.

Flow soldering must be performed once.

- When using soldering Iron

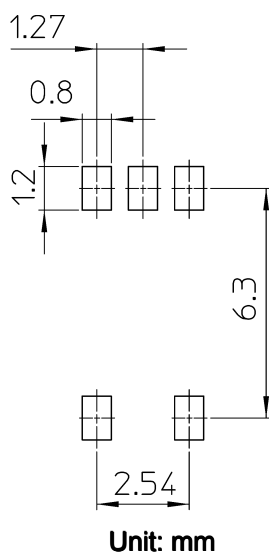
Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C

Heating by soldering iron must be done only once per lead.

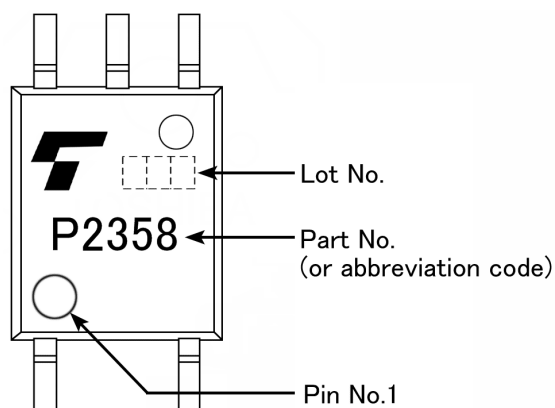
13.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

14. Land Pattern Dimensions (for reference only)



15. Marking



16. EN 60747-5-5 Option (V4) Specification

- Part number: TLP2358 (**Note 1**)
- The following part naming conventions are used for the devices that have been qualified according to option (V4) of EN 60747.

Example: TLP2358(V4-TPL,E

V4: EN 60747 option

TPL: Tape type

E: [[G]]/RoHS COMPATIBLE (**Note 2**)

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP2358(V4-TPL,E → TLP2358

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

| Description | Symbol | Rating | Unit |
|--|-------------------------------|---|----------------|
| Application classification for rated mains voltage ≤ 150 Vrms for rated mains voltage ≤ 300 Vrms | | I-IV I-III | — |
| Climatic classification | | 40 / 125 / 21 | — |
| Pollution degree | | 2 | — |
| Maximum operating insulation voltage | V_{IORM} | 707 | Vpeak |
| Input to output test voltage, Method A $V_{pr} = 1.6 \times V_{IORM}$, type and sample test $t_p = 10$ s, partial discharge < 5 pC | V_{pr} | 1131 | Vpeak |
| Input to output test voltage, Method B $V_{pr} = 1.875 \times V_{IORM}$, 100 % production test $t_p = 1$ s, partial discharge < 5 pC | V_{pr} | 1325 | Vpeak |
| Highest permissible overvoltage (transient overvoltage, $t_{pr} = 60$ s) | V_{TR} | 6000 | Vpeak |
| Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I_F , $P_{SO} = 0$) power (output or total power dissipation) temperature | I_{si} P_{SO} T_s | 250 400 150 | mA mW °C |
| Insulation resistance $V_{IO} = 500$ V, $T_a = 25$ °C $V_{IO} = 500$ V, $T_a = 100$ °C $V_{IO} = 500$ V, $T_a = T_s$ | R_{si} | $\geq 10^{12}$ $\geq 10^{11}$ $\geq 10^9$ | Ω |

Fig. 16.1 EN 60747 Isolation Characteristics

| | | |
|------------------------------|-----|--------|
| Minimum creepage distance | Cr | 5.0 mm |
| Minimum clearance | Cl | 5.0 mm |
| Minimum insulation thickness | ti | 0.4 mm |
| Comparative tracking index | CTI | 175 |

Fig. 16.2 Insulation Related Specifications (Note)

Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data.
Maintenance of the safety data shall be ensured by means of protective circuits.



Fig. 16.3 Marking on Packing

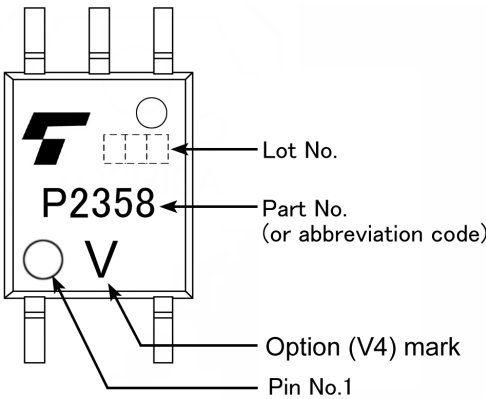


Fig. 16.4 Marking Example (Note)

Note: The above marking is applied to the photocouplers that have been qualified according to option (V4) of EN 60747.

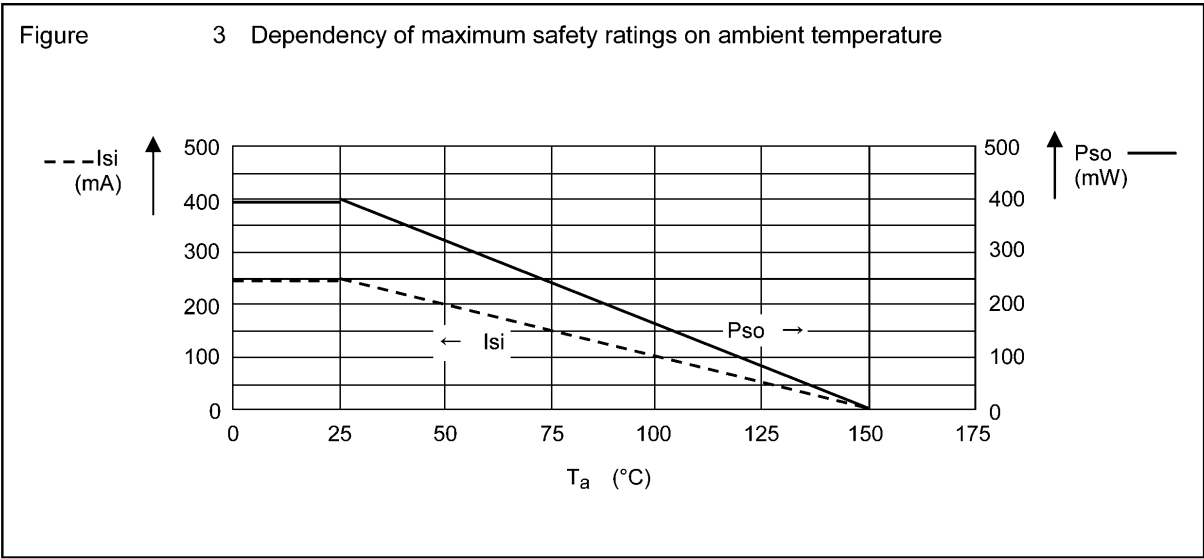
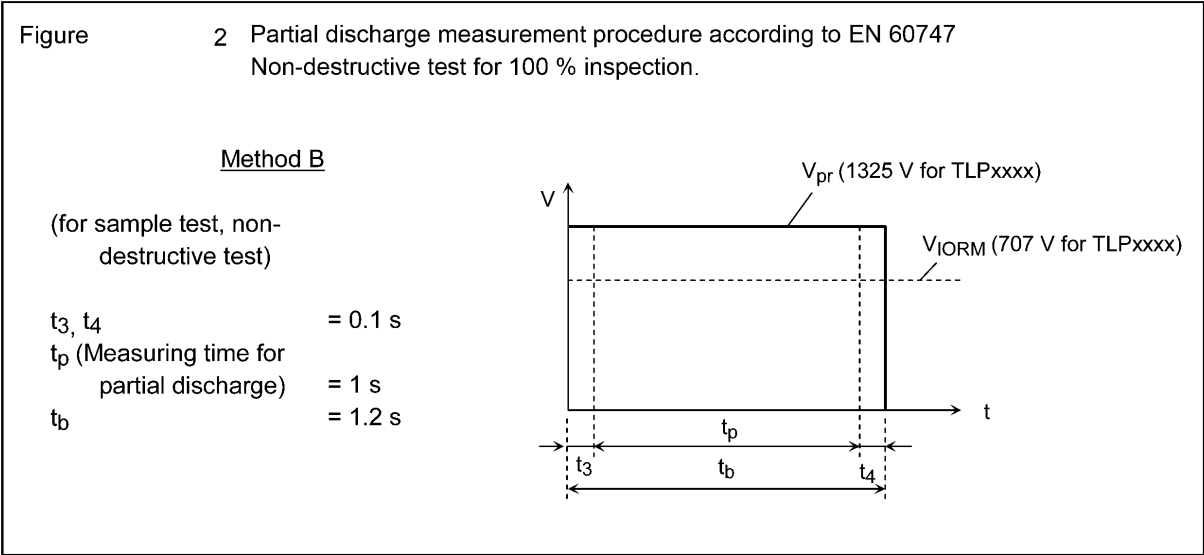
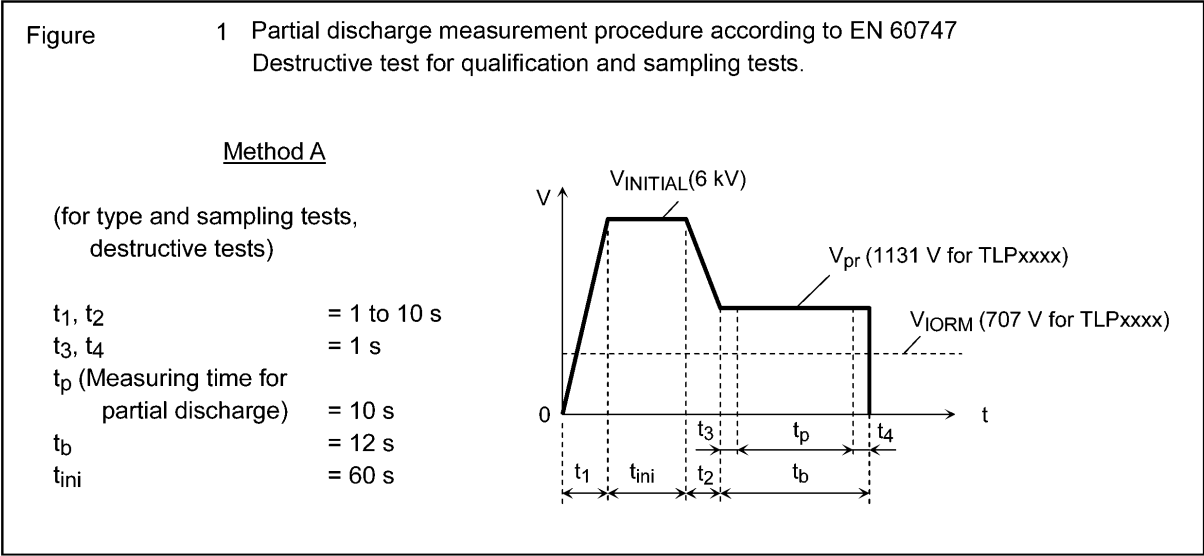


Fig. 16.5 Measurement Procedure

17. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

Example) TLP2358(TPL,E 3000 pcs

Part number: TLP2358

Tape type: TPL (12-mm pitch)

[[G]]/RoHS COMPATIBLE: E (**Note 1**)

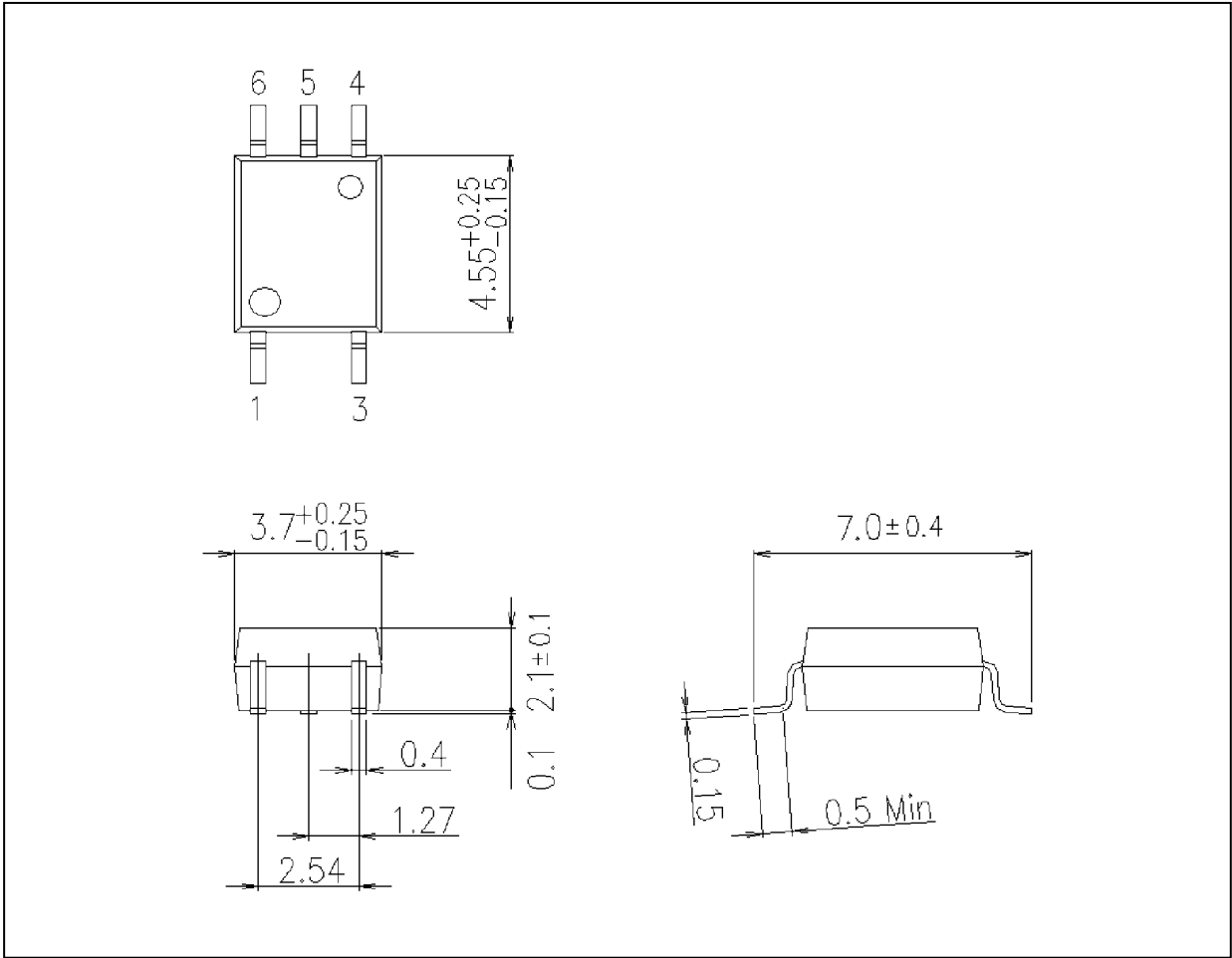
Quantity (must be a multiple of 3000): 3000 pcs

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Package Dimensions

Unit: mm



Weight: 0.08 g (typ.)

| Package Name(s) |
|------------------|
| TOSHIBA: 11-4L1S |

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