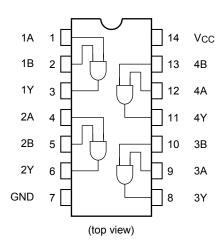
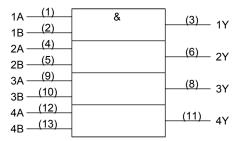


Pin Assignment



IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	lıĸ	-20	mA
Output diode current	Іок	±20	mA
DC output current	lout	±25	mA
DC V _{cc} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	−40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V) 0 to 20 (V _{CC} = 5 ± 0.5 V)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
Silarasteriotics	- Cymbol	,,,,,,		Vcc (V)	Min	Тур.	Max	Min	Max	01
High-level input voltage	VIH	-		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7	1 1		1.50 V _{CC} × 0.7	1 1	V
Low-level input voltage	VIL	_		2.0 3.0 to 5.5		1 1	0.50 V _{CC} × 0.3	1 1	0.50 V _{CC} × 0.3	V
High-level output voltage	Voн	$V_{IN} = V_{IH}$	Ι _{ΟΗ} = -50 μΑ	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4	1 1 1	V
			$I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	3.0 4.5	2.58 3.94	_ _	_ _	2.48 3.80	_ _	
Low-level output voltage VoL Vin	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 50 \ \mu\text{A}$		2.0 3.0 4.5		0.0 0.0 0.0	0.1 0.1 0.1		0.1 0.1 0.1	V	
			$I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	3.0 4.5			0.36 0.36		0.44 0.44	
Input leakage current	liN	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1	1	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5		- 1	2.0	1	20.0	μА

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AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit	
	Cymbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onne
		_	3.3 ± 0.3 · 5.0 ± 0.5 ·	15	_	6.2	8.8	1.0	10.5	- ns
	t _{pLH} t _{pHL}			50	_	8.7	12.3	1.0	14.0	
				15	_	4.3	5.9	1.0	7.0	
				5.0 ± 0.5	50	_	5.8	7.9	1.0	9.0
Input capacitance	C _{IN}	-			_	4	10	_	10	pF
Power dissipation capacitance	CPD			(Note)	1	18	-	-	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

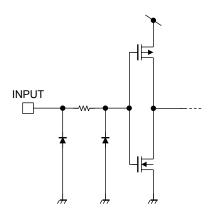
Average operating current can be obtained by the equation:

ICC (opr) = $CPD \cdot VCC \cdot fIN + ICC/4$ (per gate)

Noise Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta =	I In it	
			V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic VoL	VOLP	C _L = 50 pF	5.0	0.3	8.0	V
Quiet output minimum dynamic V _{OL}	Volv	C _L = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	VILD	C _L = 50 pF	5.0	-	1.5	V

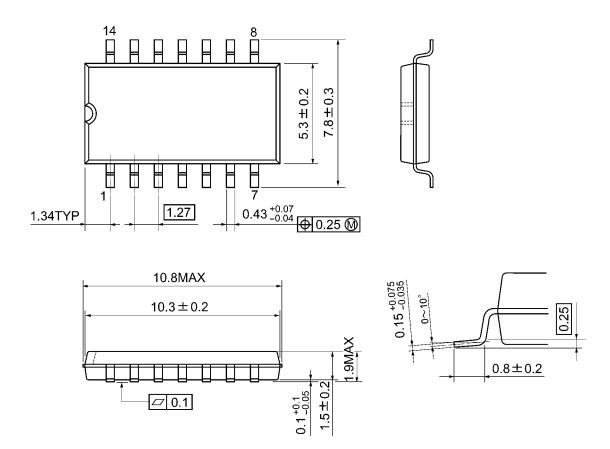
Input Equivalent Circuit





Package Dimensions

SOP14-P-300-1.27A Unit: mm

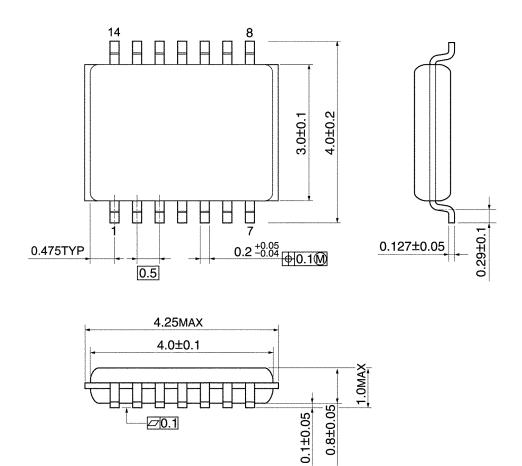


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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