SD101AW-V, SD101BW-V, SD101CW-V

Vishay Semiconductors



Thermal Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		R_{thJA}	300 ¹⁾	K/W
Junction temperature		T _j	125 ¹⁾	°C
Storage temperature range		T _{stg}	- 65 to + 150	°C

¹⁾ Valid provided that electrodes are kept at ambient temperature

Electrical Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Reverse breakdown voltage	I _R = 10 μA	SD101AW-V	V _(BR)	60			V
		SD101BW-V	V _(BR)	50			V
		SD101CW-V	V _(BR)	40			V
Leakage current	V _R = 50 V	SD101AW-V	I _R			200	nA
	V _R = 40 V	SD101BW-V	I _R			200	nA
	V _R = 30 V	SD101CW-V	I _R			200	nA
Forward voltage drop	I _F = 1 mA	SD101AW-V	V _F			410	mV
		SD101BW-V	V _F			400	mV
		SD101CW-V	V_{F}			390	mV
	I _F = 15 mA	SD101AW-V	V_{F}			1000	mV
		SD101BW-V	V_{F}			950	mV
		SD101CW-V	V_{F}			900	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	SD101AW-V	C _D			2	pF
		SD101BW-V	C _D			2.1	pF
		SD101CW-V	C _D			2.2	pF
Reverse recovery time	$I_F = I_R = 5$ mA, recover to 0.1 I_R		t _{rr}			1	ns

Typical Characteristics

T_{amb} = 25 °C, unless otherwise specified

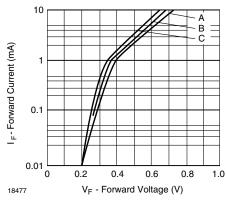


Figure 1. Typical Variation of Forward Current vs. Forward Voltage

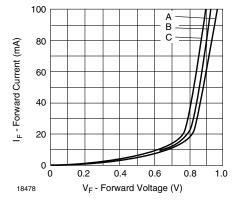


Figure 2. Typical Forward Conduction Curve

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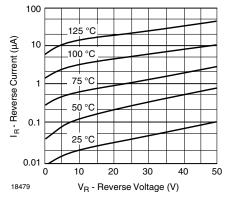


Figure 3. Typical Variation of Reverse Current at Various Temperatures

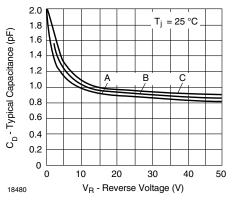
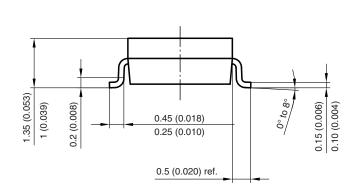
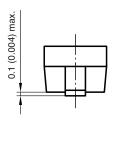


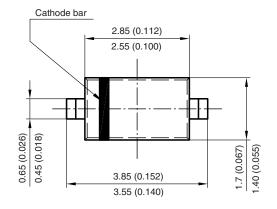
Figure 4. Typical Capacitance Curve as a Function of Reverse Voltage

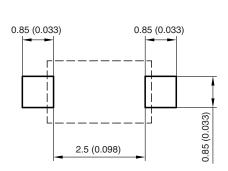
Package Dimensions in millimeters (inches): SOD-123





Mounting Pad Layout





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