Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
	characteristics			-71		
Q_{GD}	gate-drain charge	V_{GS} = 4.5 V; I_D = 20 A; V_{DS} = 15 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	2.85	-	nC
Q _{G(tot)}	total gate charge	V_{GS} = 4.5 V; I_D = 20 A; V_{DS} = 15 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	9.6	-	nC

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source		
2	S	source	mb	D
3	S	source		G (EX)
4	G	gate	[9]	
mb	D	mounting base; connected to drain	1 2 3 4	mbb076 \$
			SOT669 (LFPAK; Power-SO8)	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN4R5-30YLC	LFPAK; Power-SO8	plastic single-ended surface-mounted package; 4 leads	SOT669

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PSMN4R5-30YLC	4C530L

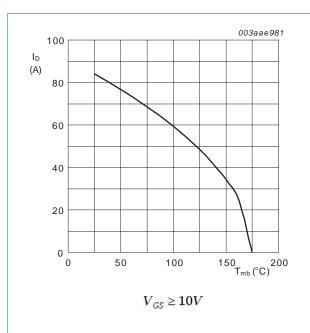
^{[1] % =} placeholder for manufacturing site code.

5. Limiting values

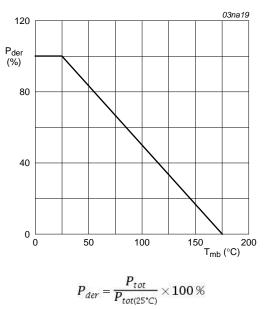
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

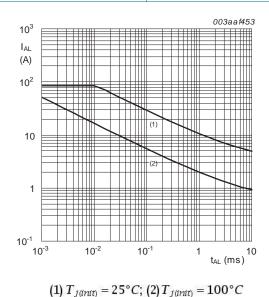
Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	25 °C ≤ T _j ≤ 175 °C	-	30	V
V_{DGR}	drain-gate voltage	25 °C ≤ T_j ≤ 175 °C; R_{GS} = 20 kΩ	-	30	V
V_{GS}	gate-source voltage		-20	20	V
I_D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 100 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	-	60	Α
		V _{GS} = 10 V; T _{mb} = 25 °C; see <u>Figure 1</u>	-	84	А
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 \text{ °C}$; see Figure 4	-	334	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	61	W
T _{stg}	storage temperature		-55	175	°C
T _j	junction temperature		-55	175	°C
T _{sld(M)}	peak soldering temperature		-	260	°C
V _{ESD}	electrostatic discharge voltage	MM (JEDEC JESD22-A115)	270	-	V
Source-drain	diode				
Is	source current	T _{mb} = 25 °C	-	55	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 ^{\circ}C$	-	334	А
Avalanche ru	ggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 84 A; $V_{sup} \le$ 30 V; R_{GS} = 50 Ω; unclamped; see <u>Figure 3</u>	-	14.5	mJ



Continuous drain current as a function of mounting base temperature



Normalized total power dissipation as a Fig 2. function of mounting base temperature



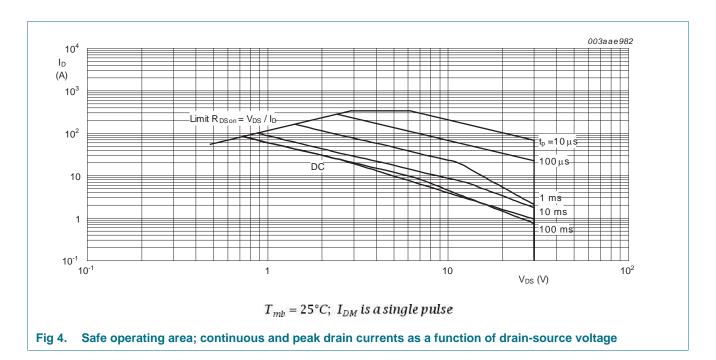
Single pulse avalanche rating; avalanche current as a function of avalanche time

PSMN4R5-30YLC

Fig 3.

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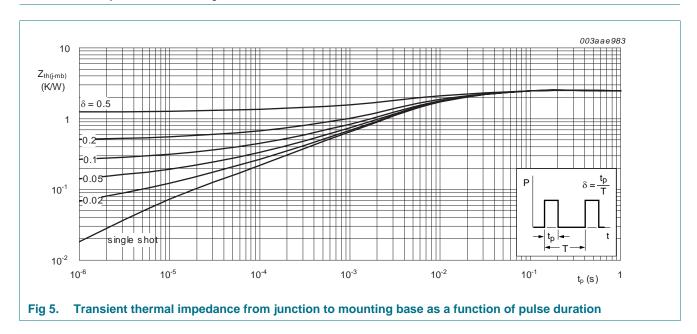
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6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 5	-	2.26	2.48	K/W



7. Characteristics

Table 7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
V _{(BR)DSS}	drain-source breakdown	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
	voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	27	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ °C}$; see Figure 10	1.05	1.54	1.95	V
		$I_D = 10 \text{ mA}; V_{DS} = V_{GS}; T_j = 150 \text{ °C}$	0.5	-	-	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = -55$ °C; see Figure 11	-	-	2.25 V	V
I _{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μA
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 150 °C	-	-	100	μA
I _{GSS}	gate leakage current	$V_{GS} = 16 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
		$V_{GS} = -16 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
R _{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 20 \text{ A}; T_j = 25 \text{ °C};$ see Figure 12	-	5.1	6.1	mΩ
		$V_{GS} = 4.5 \text{ V}; I_D = 20 \text{ A}; T_j = 150 ^{\circ}\text{C};$ see <u>Figure 12</u> ; see <u>Figure 13</u>	-	- 11	11	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 20 \text{ A}; T_j = 25 \text{ °C};$ see Figure 12	-	4	4.8	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 20 \text{ A}; T_j = 150 °C;$ see <u>Figure 12</u> ; see <u>Figure 13</u>	-	-	8.6	mΩ
R_G	gate resistance	f = 1 MHz	-	2.1	4.2	Ω
Dynamic	characteristics					
$Q_{G(tot)}$	total gate charge	$I_D = 20 \text{ A}$; $V_{DS} = 15 \text{ V}$; $V_{GS} = 10 \text{ V}$; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	20.5	-	nC
		$I_D = 20 \text{ A}$; $V_{DS} = 15 \text{ V}$; $V_{GS} = 4.5 \text{ V}$; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	9.6	-	nC
		$I_D = 0 \text{ A}; V_{DS} = 0 \text{ V}; V_{GS} = 10 \text{ V};$ see Figure 14	-	18.5	-	nC
Q_{GS}	gate-source charge	$I_D = 20 \text{ A}$; $V_{DS} = 15 \text{ V}$; $V_{GS} = 4.5 \text{ V}$;	-	3.2	-	nC
Q _{GS(th)}	pre-threshold gate-source charge	see <u>Figure 14</u> ; see <u>Figure 15</u>	-	2.1	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	1.14	-	nC
Q_{GD}	gate-drain charge		-	2.85	-	nC
V _{GS(pl)}	gate-source plateau voltage	I _D = 20 A; V _{DS} = 15 V; see <u>Figure</u> <u>14</u> ; see <u>Figure 15</u>	-	2.74	-	V
C _{iss}	input capacitance	$V_{DS} = 15 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	1324	-	pF
Coss	output capacitance	T _j = 25 °C; see <u>Figure 16</u>	-	288	-	pF
C _{rss}	reverse transfer capacitance		-	97	-	pF

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 Table 7.
 Characteristics ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t _{d(on)}	turn-on delay time	$V_{DS} = 15 \text{ V}; R_L = 0.75 \Omega;$ $V_{GS} = 4.5 \text{ V}; R_{G(ext)} = 4.7 \Omega$	-	17.2	-	ns
t _r	rise time		-	18.7	-	ns
t _{d(off)}	turn-off delay time		-	24.3	-	ns
t _f	fall time		-	8.75	-	ns
Q _{oss}	output charge	$V_{GS} = 0 \text{ V}; V_{DS} = 15 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}$	-	7.9	-	nC
Source-drain	n diode					
V _{SD}	source-drain voltage	$I_S = 20 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C};$ see <u>Figure 17</u>	-	0.8	1.1	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s};$	-	29.8	-	ns
Q _r	recovered charge	$V_{GS} = 0 \text{ V}; V_{DS} = 15 \text{ V}$	-	27.8	-	nC
t _a	reverse recovery rise time	$V_{GS} = 0 \text{ V}; I_S = 20 \text{ A};$	-	18.8	-	ns
t _b	reverse recovery fall time	$dI_S/dt = -100 A/\mu s; V_{DS} = 15 V;$ see Figure 18	-	11	-	ns

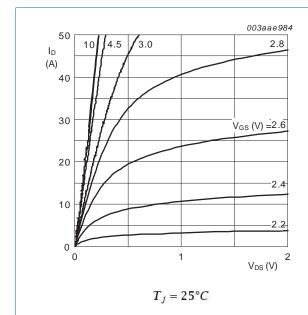


Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values

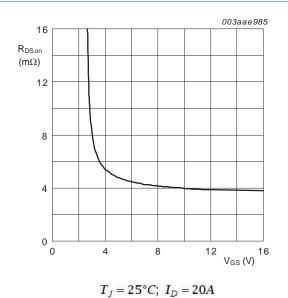


Fig 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

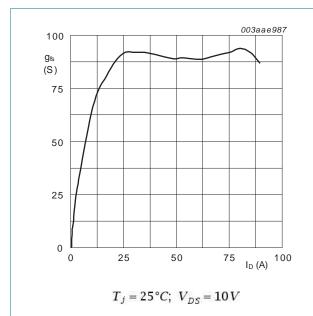


Fig 8. Forward transconductance as a function of drain current; typical values

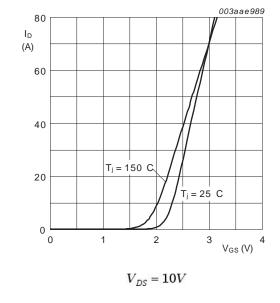


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

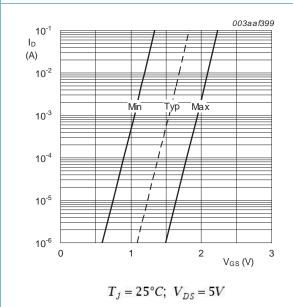


Fig 10. Sub-threshold drain current as a function of gate-source voltage

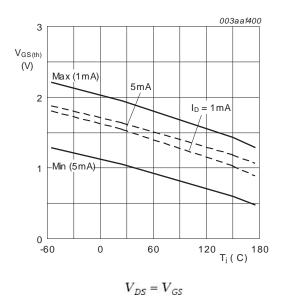


Fig 11. Gate-source threshold voltage as a function of junction temperature

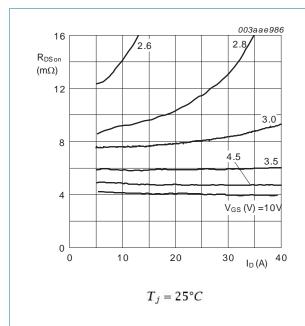


Fig 12. Drain-source on-state resistance as a function of drain current; typical values

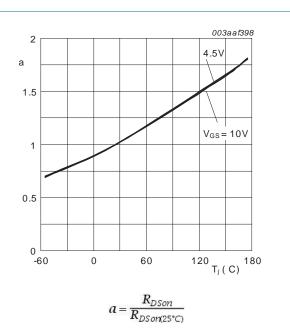


Fig 13. Normalized drain-source on-state resistance factor as a function of junction temperature

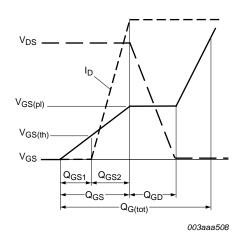


Fig 14. Gate charge waveform definitions

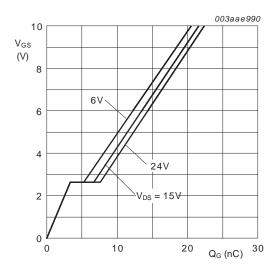


Fig 15. Gate-source voltage as a function of gate charge; typical values

 $T_j = 25^{\circ}C; \ I_D = 20A$

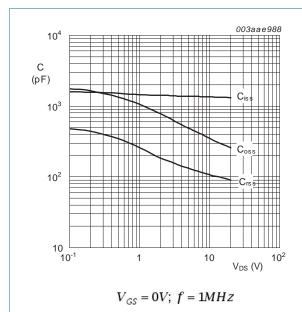


Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

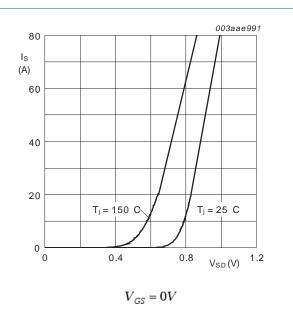


Fig 17. Source current as a function of source-drain voltage; typical values

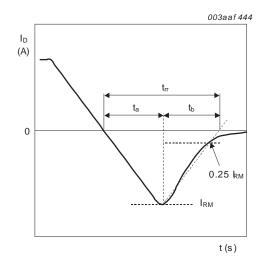


Fig 18. Reverse recovery timing definition

8. Package outline

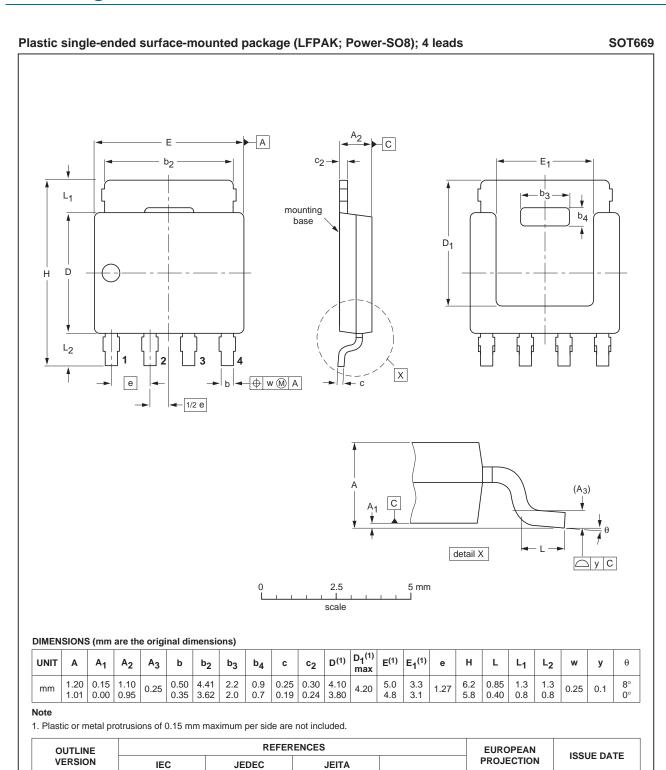


Fig 19. Package outline SOT669 (LFPAK; Power-SO8)

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SOT669

9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN4R5-30YLC v.3	20110705	Product data sheet	-	PSMN4R5-30YLC v.2
Modifications:	 Various changes to 	content.		
PSMN4R5-30YLC v.2	20101130	Product data sheet	-	PSMN4R5-30YLC v.1

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10. Legal information

10.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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PSMN4R5-30YLC

N-channel 30 V 4.8 m Ω logic level MOSFET in LFPAK using NextPower

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12. Contents

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