

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

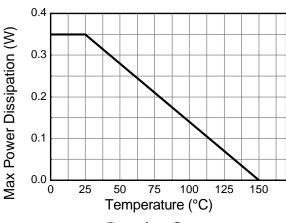
Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-6.0	V
Collector Current - Continuous (Note 7)	Ic	-600	mA

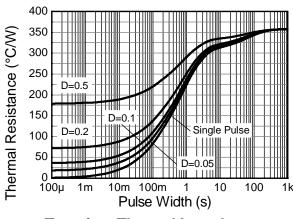
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Collector Dower Dissipation	(Note 5)	D-	310		
Collector Power Dissipation	(Note 6)	P _D	350	mW	
Thermal Resistance, Junction to Ambient	(Note 5)	D	403	00/14/	
	(Note 6)	$R_{\theta JA}$	357	°C/W	
Thermal Resistance, Junction to Leads	(Note 7)	$R_{\theta JL}$	350	°C/W	
Operating and Storage Temperature Range		T_{J} , T_{STG}	-55 to +150	°C	

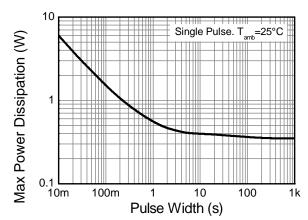
Notes:

- 5. For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
 6. For the device mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- 7. Thermal resistance from junction to solder-point (at the end of the collector lead).





Derating Curve



Transient Thermal Impedance

Pulse Power Dissipation



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)						
Collector-Base Breakdown Voltage	BV _{CBO}	-40		V	$I_C = -100 \mu A$, $I_E = 0$	
Collector-Emitter Breakdown Voltage	BV _{CEO}	-40	_	V	$I_C = -10.0 \text{mA}, I_B = 0$	
Emitter-Base Breakdown Voltage	BV _{EBO}	-6.0	_	V	$I_E = -100 \mu A, I_C = 0$	
Collector Cutoff Current	I _{CEX}		-100	nA	$V_{CE} = -35V, V_{EB(OFF)} = -0.4V$	
Base Cutoff Current	I _{BL}		-100	nA	$V_{CE} = -35V, V_{EB(OFF)} = -0.4V$	
ON CHARACTERISTICS (Note 8)						
DC Current Gain	h _{FE}	30 60 100 100 20	 300 	_	$\begin{split} I_C &= -100 \mu A, \ V_{CE} = -1.0 V \\ I_C &= -1.0 m A, \ V_{CE} = -1.0 V \\ I_C &= -10 m A, \ V_{CE} = -1.0 V \\ I_C &= -150 m A, \ V_{CE} = -2.0 V \\ I_C &= -500 m A, \ V_{CE} = -2.0 V \end{split}$	
Collector-Emitter Saturation Voltage	V _{CE(sat)}		-0.40 -0.75	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$ $I_C = -500 \text{mA}, I_B = -50 \text{mA}$	
Base-Emitter Saturation Voltage	V _{BE(sat)}	-0.75 —	-0.95 -1.30	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$ $I_C = -500 \text{mA}, I_B = -50 \text{mA}$	
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{obo}	_	8.5	pF	$V_{CB} = -10V$, $f = 1.0MHz$, $I_{E} = 0$	
Input Capacitance	C _{ibo}	_	30	pF	$V_{EB} = -0.5V$, $f = 1.0MHz$, $I_C = 0$	
Input Impedance	h _{ie}	1.5	15	kΩ		
Voltage Feedback Ratio	h _{re}	0.1	8.0	x 10 ⁻⁴	$V_{CE} = -10V, I_{C} = -1.0mA,$	
Small Signal Current Gain	h _{fe}	60	500	_	f = 1.0kHz	
Output Admittance	h _{oe}	1.0	100	μS		
Current Gain-Bandwidth Product	f⊤	200		MHz	$V_{CE} = -10V$, $I_{C} = -20mA$, $f = 100MHz$	
SWITCHING CHARACTERISTICS						
Delay Time	t _d	_	15	ns	$V_{CC} = -30V, I_{C} = -150mA,$	
Rise Time	t _r		20	ns	$V_{BE(off)} = -2.0V, I_{B1} = -15mA$	
Storage Time	ts	_	225	ns	$V_{CC} = -30V, I_{C} = -150mA,$	
Fall Time	t _f		30	ns	$I_{B1} = I_{B2} = -15\text{mA}$	

Notes:

8. Short duration pulse test used to minimize self-heating effect.



Typical Electrical Characteristics

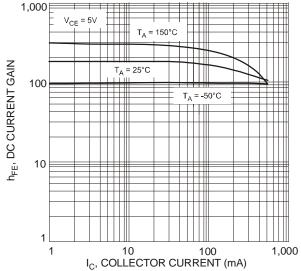


Figure 1 Typical DC Current Gain vs. Collector Current

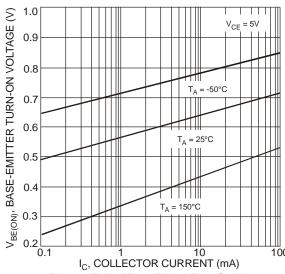


Figure 3 Typical Base-Emitter Turn-On Voltage vs. Collector Current

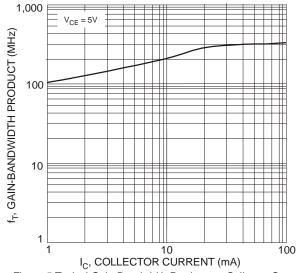


Figure 5 Typical Gain-Bandwidth Product vs. Collector Current

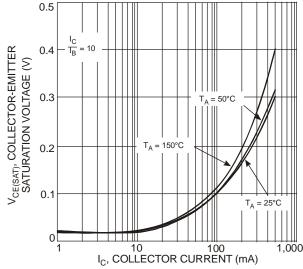


Figure 2 Typical Collector-Emitter Saturation Voltage vs. Collector Current

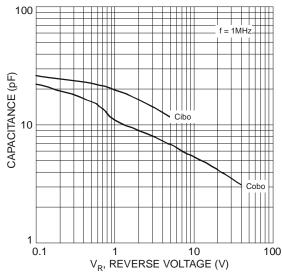


Figure 4 Typical Capacitance Characteristics

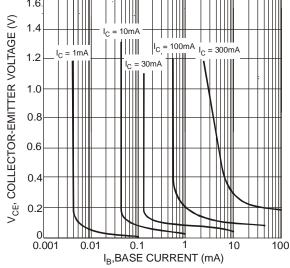
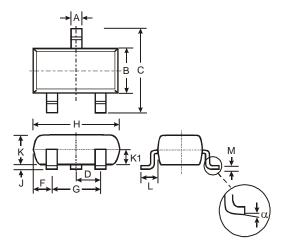


Figure 6 Typical Collector Saturation Region



Package Outline Dimensions

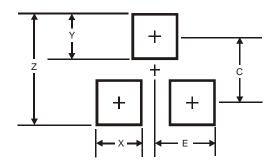
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23			
Dim	Min	Max	Тур	
Α	0.37	0.51	0.40	
В	1.20	1.40	1.30	
С	2.30	2.50	2.40	
D	0.89	1.03	0.915	
F	0.45	0.60	0.535	
G	1.78	2.05	1.83	
Н	2.80	3.00	2.90	
J	0.013	0.10	0.05	
K	0.903	1.10	1.00	
K1	-	-	0.400	
L	0.45	0.61	0.55	
M	0.085	0.18	0.11	
α	0°	8°	-	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
Е	1.35



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