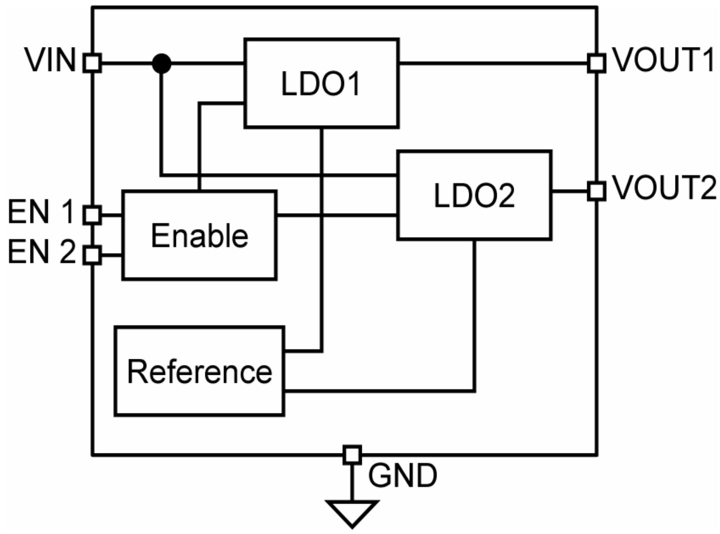
MIC5335 Block Diagram



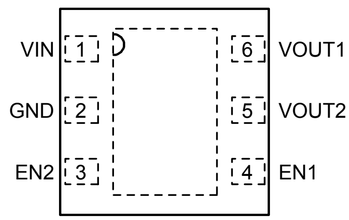
Ordering Information

Part number	Manufacturing Part Number	Marking	Voltage*	Junction Temp. Range	Package
MIC5335-1.8/1.5YMT	MIC5335-GFYMT	GPF	1.8V/1.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-1.8/1.6YMT	MIC5335-GWYMT	GPW	1.8V/1.6V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-1.8/1.8YMT	MIC5335-GGYMT	GPG	1.8V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.5/1.8YMT	MIC5335-JGYMT	JPG	2.5V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.5/2.5YMT	MIC5335-JJYMT	JPJ	2.5V/2.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.6/1.85YMT	MIC5335-KDYMT	KPD	2.6V/1.85	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.6/1.8YMT	MIC5335-KGYMT	KPG	2.6V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.7/2.7YMT	MIC5335-LLYMT	LPL	2.7V/2.7V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.8/1.5YMT	MIC5335-MFYMT	MPF	2.8V/1.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.8/1.8YMT	MIC5335-MGYMT	MPG	2.8V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.8/2.6YMT	MIC5335-MKYMT	MPK	2.8V/2.6V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.8/2.8YMT	MIC5335-MMYMT	MPM	2.8V/2.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.85/1.85YMT	MIC5335-NDYMT	NPD	2.85V/1.85V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.85/2.6YMT	MIC5335-NKYMT	NPK	2.85V/2.6V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.85/2.85YMT	MIC5335-NNYMT	NPN	2.85V/2.85V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.9/1.5YMT	MIC5335-OFYMT	OPF	2.9V/1.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.9/1.8YMT	MIC5335-OGYMT	OPG	2.9V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-2.9/2.9YMT	MIC5335-OOYMT	OPO	2.9V/2.9V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/1.8YMT	MIC5335-PGYMT	PPG	3.0V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/2.5YMT	MIC5335-PJYMT	PPJ	3.0V/2.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/2.6YMT	MIC5335-PKYMT	PPK	3.0V/2.6V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/2.8YMT	MIC5335-PMYMT	PPM	3.0V/2.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/2.85YMT	MIC5335-PNYMT	PPN	3.0V/2.85V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.0/3.0YMT	MIC5335-PPYMT	PPP	3.0V/3.0V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/1.5YMT	MIC5335-SFYMT	SPF	3.3V/1.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/1.8YMT	MIC5335-SGYMT	SPG	3.3V/1.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.5YMT	MIC5335-SJYMT	SPJ	3.3V/2.5V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.6YMT	MIC5335-SKYMT	SPK	3.3V/2.6V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.7YMT	MIC5335-SLYMT	SPL	3.3V/2.7V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.8YMT	MIC5335-SMYMT	SPM	3.3V/2.8V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.85YMT	MIC5335-SNYMT	SPN	3.3V/2.85V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/2.9YMT	MIC5335-SOYMT	SPO	3.3V/2.9V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/3.0YMT	MIC5335-SPYMT	SPP	3.3V/3.0V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/3.2YMT	MIC5335-SRYMT	SPR	3.3V/3.2V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®
MIC5335-3.3/3.3YMT	MIC5335-SSYMT	SPS	3.3V/3.3V	–40°C to +125°C	6-Pin 1.6x1.6 Thin MLF®

Note:

* For other voltages available. Contact Micrel Marketing for details.

Pin Configuration



6-pin 1.6mm × 1.6mm Thin MLF®
Top View

Pin Description

Pin Number Thin MLF-6	Pin Name	Pin Function
1	VIN	Supply Input.
2	GND	Ground
3	EN2	Enable Input (regulator 2). Active High Input. Logic High = On; Logic Low = Off; Do not leave floating.
4	EN1	Enable Input (regulator 1). Active High Input. Logic High = On; Logic Low = Off; Do not leave floating.
5	VOUT2	Regulator Output – LDO2
6	VOUT1	Regulator Output – LDO1
HS Pad	EPAD	Exposed heatsink pad connected to ground internally.

Absolute Maximum Ratings[1]

Supply Voltage (V_{IN})....................................0V to +6V
Enable Input Voltage (V_{EN})...........................0V to +6V
Power Dissipation..........................Internally Limited[3]
Lead Temperature (soldering, 3sec ....260°C
Storage Temperature (T_S)................. -65°C to +150°C
ESD Rating[4] 2kV

Operating Ratings[2]

Supply voltage (V_{IN}).............................. +2.3V to +5.5V
Enable Input Voltage (V_{EN})............................. 0V to V_{IN}
Junction Temperature ....-40°C to +125°C
Junction Thermal Resistance
Thin MLF®-6 (θ_{JA}) .... 100°C/W

Electrical Characteristics[5]

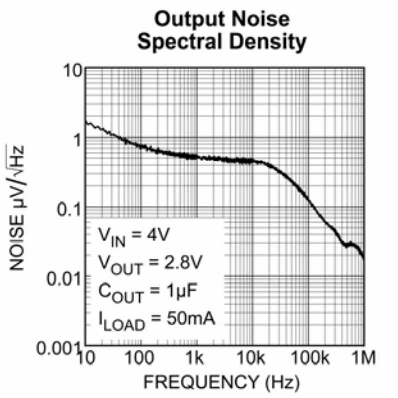
V_{IN} = EN1 = EN2 = V_{OUT} + 1.0V; higher of the two regulator outputs, $I_{OUTLDO1}$ = $I_{OUTLDO2}$ = 100µA; C_{OUT1} = C_{OUT2} = 1µF; T_J = 25°C, **bold** values indicate –40°C ≤ T_J ≤ +125°C, unless noted.

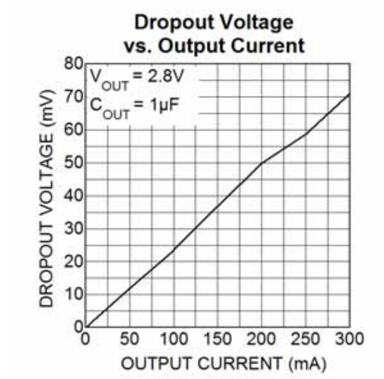
Parameter	Conditions	Min	Typ	Max	Units
Output Voltage Accuracy	Variation from nominal V_{OUT}	-2.0		+2.0	%
	Variation from nominal V_{OUT}; –40°C to +125°C	**-3.0**		**+3.0**	%
Line Regulation	V_{IN} = V_{OUT} + 1V to 5.5V; I_{OUT} = 100µA		0.02	0.3 **0.6**	%/V %/V
Load Regulation	I_{OUT} = 100µA to 300mA		0.3	**2.0**	%
Dropout Voltage (**Note 6**)	I_{OUT} = 100µA		0.1		mV
	I_{OUT} = 100mA		25	**75**	mV
	I_{OUT} = 150mA		35	**100**	mV
	I_{OUT} = 300mA		75	**200**	mV
Ground Current	EN1 = High; EN2 = Low; I_{OUT} = 100µA to 300mA		90	**125**	µA
	EN1 = Low; EN2 = High; I_{OUT} = 100µA to 300mA		90	**125**	µA
	EN1 = EN2 = High; I_{OUT1} = 300mA, I_{OUT2} = 300mA		150	**220**	µA
Ground Current in Shutdown	EN1 = EN2 = 0V		0.01	2	µA
Ripple Rejection	f = 1kHz; C_{OUT} = 1.0µF		65		dB
	f = 20kHz; C_{OUT} = 1.0µF		45		
Current Limit	V_{OUT} = 0V	**340**	550	**950**	mA
Output Voltage Noise	C_{OUT} = 1.0µF; 10Hz to 100kHz		90		μV_{RMS}
Enable Inputs (EN1 / EN2)					
Enable Input Voltage	Logic Low			**0.2**	V
	Logic High	**1.1**			V
Enable Input Current	V_{IL} ≤ 0.2V		0.01	1	µA
	V_{IH} ≥ 1.0V		0.01	1	µA
Turn-on Time (See Timing Diagram)					
Turn-on Time (LDO1 and 2)	C_{OUT} = 1.0µF		30	**100**	µs

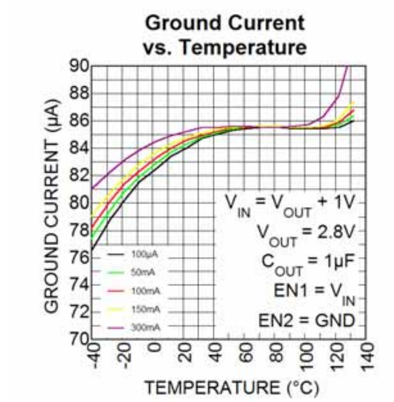
Notes:

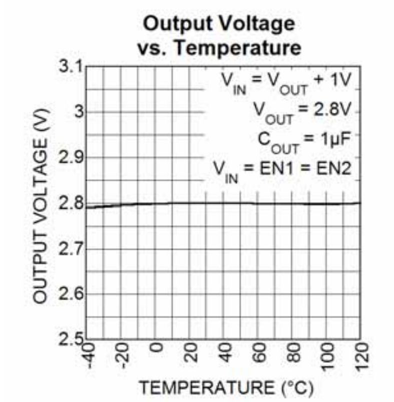
1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. The maximum allowable power dissipation of any T_A (ambient temperature) is $P_{D(max)} = (T_{J(max)} – T_A) / \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
4. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.
5. Specification for packaged product only.
6. Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal V_{OUT}. For outputs below 2.3V, the dropout voltage is the input-to-output differential with the minimum input voltage 2.3V.

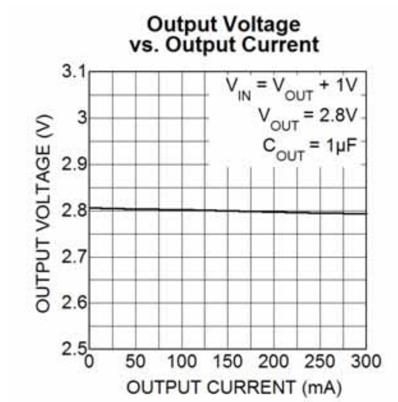
Typical Characteristics

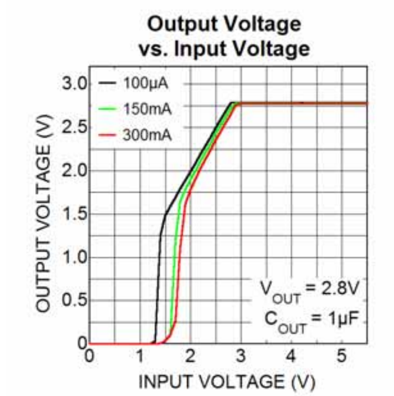


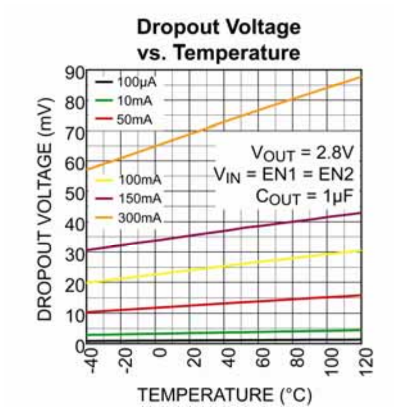


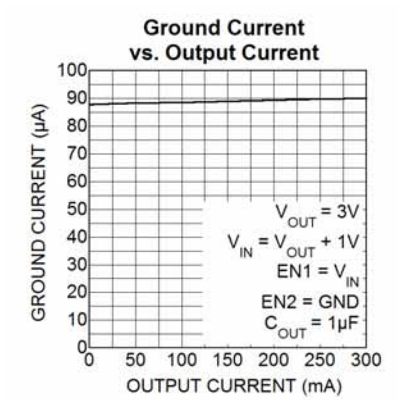


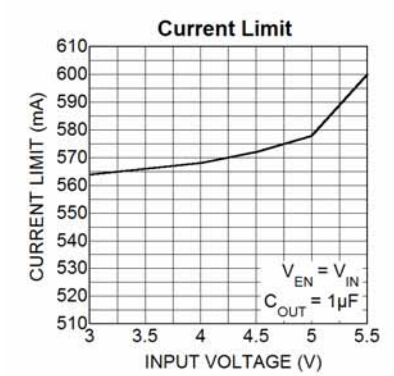




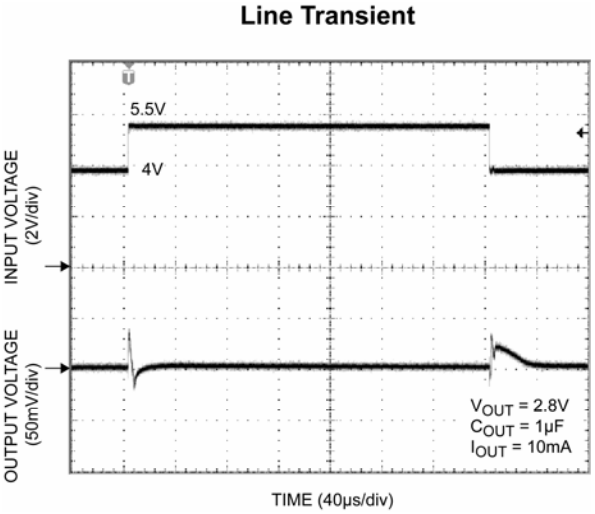


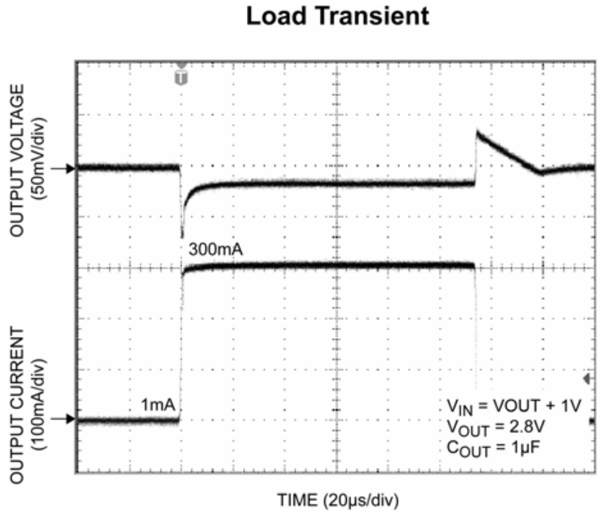


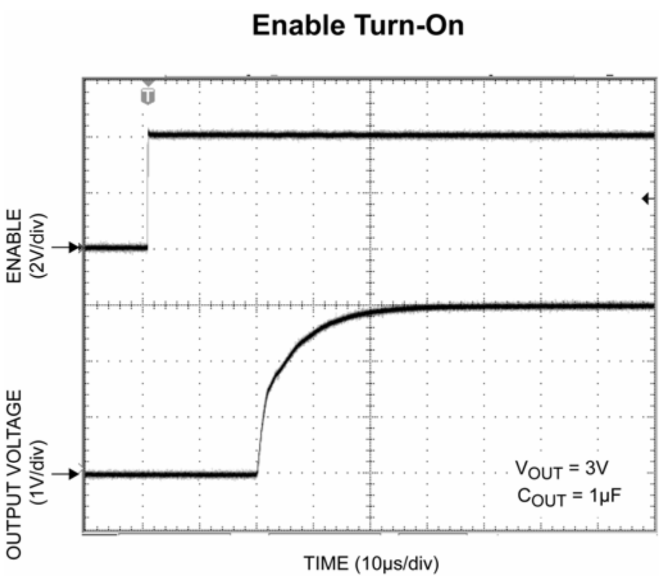




Functional Characteristics







Applications Information

Enable/Shutdown

The MIC5335 comes with dual active-high enable pins that allow each regulator to be enabled independently. Forcing the enable pin low disables the regulator and sends it into a “zero” off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

Input Capacitor

The MIC5335 is a high-performance, high bandwidth device. Therefore, it requires a well-bypassed input supply for optimal performance. A 1µF capacitor is required from the input-to-ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small-valued NPO dielectric-type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit.

Output Capacitor

The MIC5335 requires an output capacitor of 1µF or greater to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High ESR capacitors may cause high frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1µF ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric-type ceramic capacitors are recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors on the market. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

No-Load Stability

Unlike many other voltage regulators, the MIC5335 will remain stable and in regulation with no load. This is especially important in CMOS RAM keep-alive applications.

Thermal Considerations

The MIC5335 is designed to provide 300mA of continuous current for both outputs in a very small package. Maximum ambient operating temperature can be calculated based upon the output current and the voltage drop across the part. Given that the input voltage is 3.3V, the output voltage is 2.8V for V_{OUT1}, 2.5V for V_{OUT2} and the output current = 300mA. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT1})\, I_{OUT1} + (V_{IN} - V_{OUT2})\, I_{OUT2} + V_{IN}\, I_{GND}$$

Because this device is CMOS and the ground current is typically <100µA over the load range, the power dissipation contributed by the ground current is < 1% and can be ignored for this calculation.

$$P_D = (3.3V - 2.8V) \times 300mA + (3.3V - 2.5V) \times 300mA$$

$$P_D = 0.39W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(max)} = \left(\frac{T_{J(max)} - T_A}{\theta_{JA}} \right)$$

$T_{J(max)}$ = 125°C, the maximum junction temperature of the die θ_{JA} thermal resistance = 100°C/W.

The table that follows shows junction-to-ambient thermal resistance for the MIC5335 in the Thin MLF® package.

Package	θ_{JA} Recommended Minimum Footprint	θ_{JC}
6-Pin 1.6 X1.6 Thin MLF™	100°C/W	2°C/W

Thermal Resistance

Substituting P_D for $P_{D(max)}$ and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is 100°C/W.

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC5335-MFYML at an input voltage of 3.3V and 300mA loads on each output with a minimum footprint layout, the maximum ambient operating temperature T_A can be determined as follows:

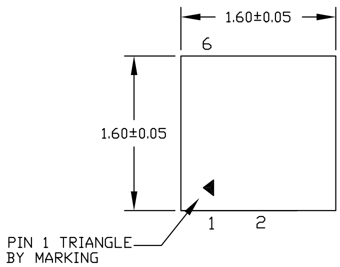
$$0.39W = (125°C - T_A)/(100°C/W)$$

T_A=86°C

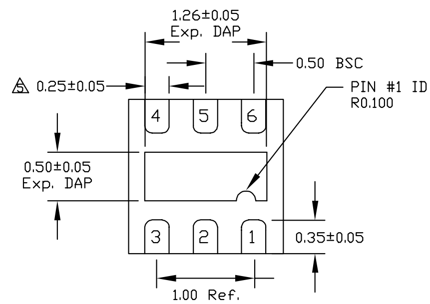
Therefore, a 2.8V/2.5V application with 300mA at each output current can accept an ambient operating temperature of 86°C in a 1.6mm x 1.6mm Thin MLF® package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the "Regulator Thermals" subsection of *Micrel's Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

http://www.micrel.com/_PDF/other/LDOBk_ds.pdf

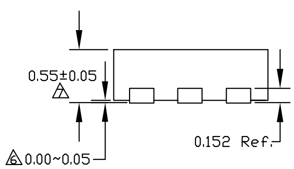
Package Information



TOP VIEW



BOTTOM VIEW



NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. MAX. PACKAGE WARPAGE IS 0.05 mm.
3. MAXIMUM ALLOWABE BURRS IS 0.076 mm IN ALL DIRECTIONS.
4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED.
5. DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL TIP.
6. APPLIED ONLY FOR TERMINALS.
7. APPLIED FOR EXPOSED PAD AND TERMINALS.

SIDE VIEW

6-Pin 1.6mm x 1.6mm Thin MLF® (MT)

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