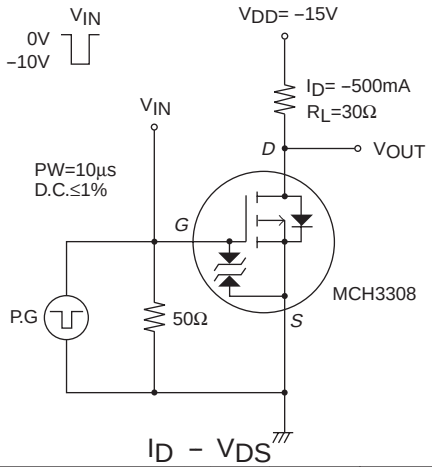
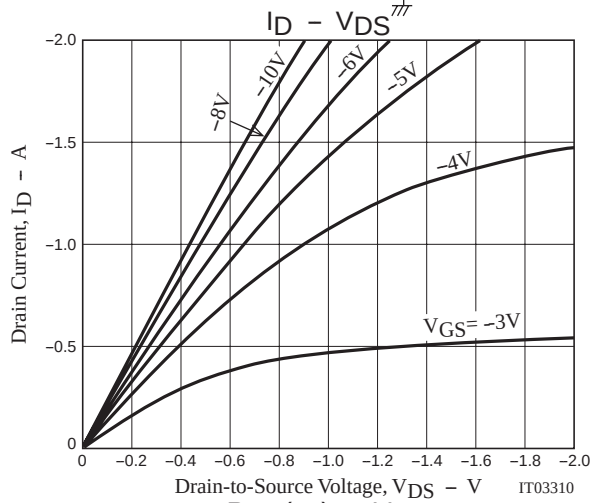
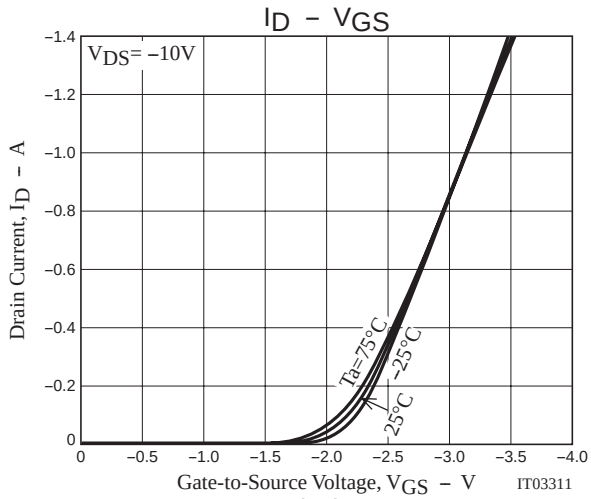
Continued from preceding page.

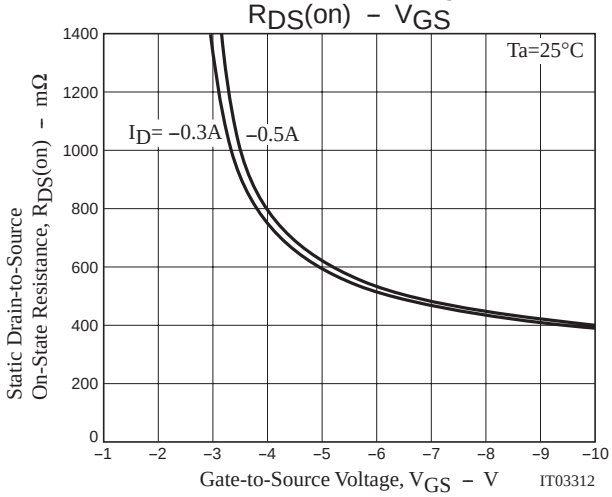
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=−10V, f=1MHz		75		pF
Output Capacitance	Coss	V_{DS}=−10V, f=1MHz		16		pF
Reverse Transfer Capacitance	Crss	V_{DS}=−10V, f=1MHz		9		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit		6		ns
Rise Time	t_{r}	See specified Test Circuit		4		ns
Turn-OFF Delay Time	t_{d}(off)	See specified Test Circuit		12		ns
Fall Time	t_{f}	See specified Test Circuit		4		ns
Total Gate Charge	Qg	V_{DS}=−10V, V_{GS}=−10V, I_{D}=−1A		2.6		nC
Gate-to-Source Charge	Qgs	V_{DS}=−10V, V_{GS}=−10V, I_{D}=−1A		0.5		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=−10V, V_{GS}=−10V, I_{D}=−1A		0.5		nC
Diode Forward Voltage	V_{SD}	I_{S}=−1A, V_{GS}=0		−0.89	−1.5	V

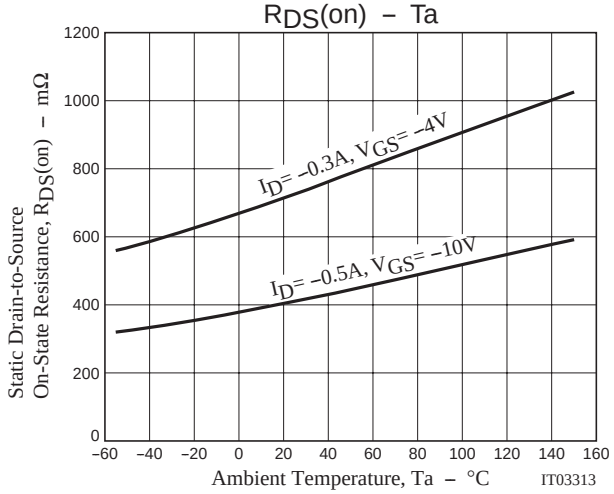
Switching Time Test Circuit

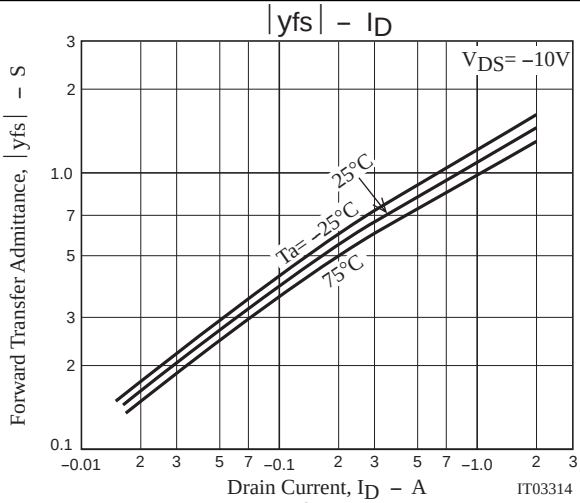


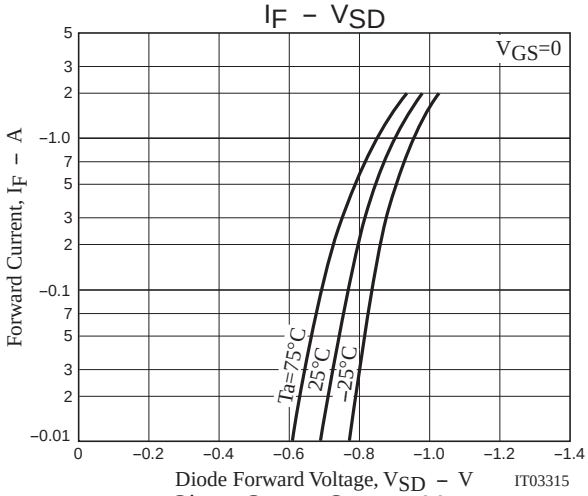


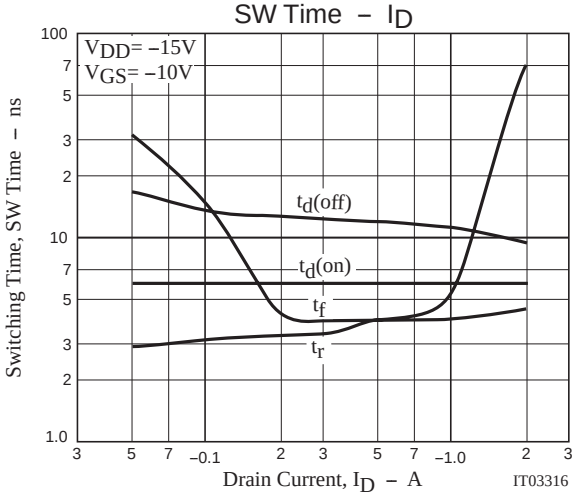


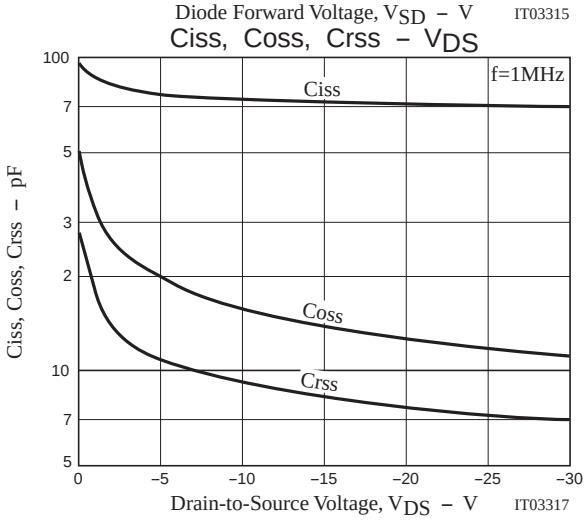


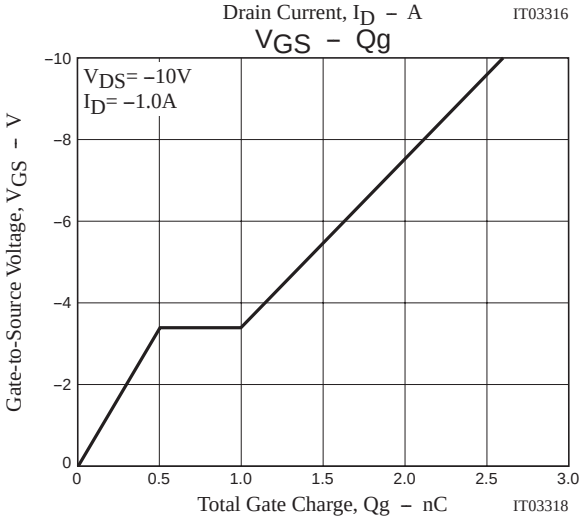


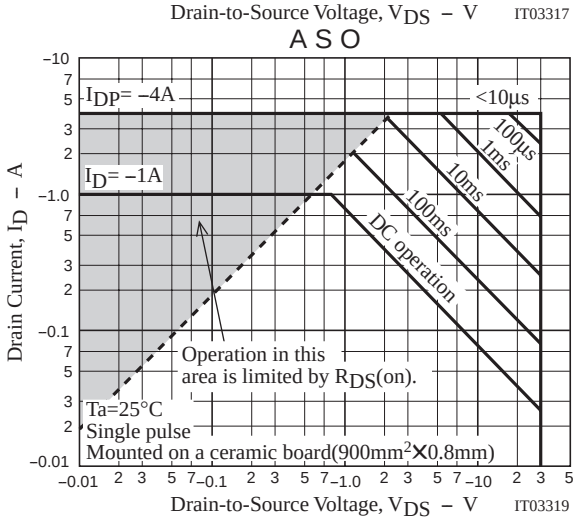


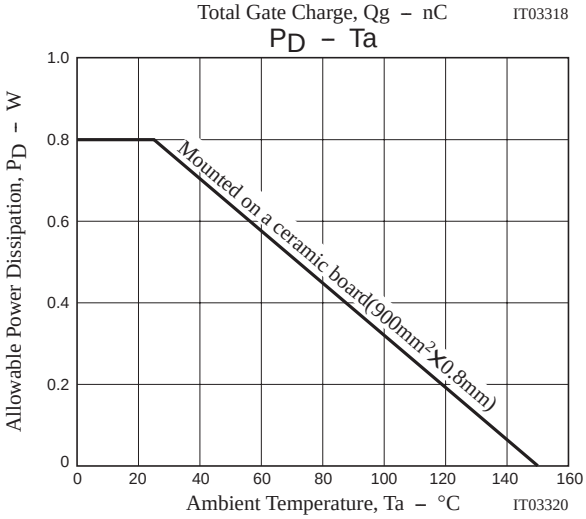












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