

VS-MBRB15..CTPbF, VS-MBR15..CT-1PbF Series

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
		15 A	T _J = 25 °C	0.84	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	T _{.1} = 125 °C	0.57	V
		15 A	1j = 125 C	0.72	
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1	- mA
Maximum instantaneous reverse current		T _J = 125 °C	hated DC voltage	15	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	ge 100 kHz to 1 MHz), 25 °C	400	pF
Typical series inductance	L _S	Measured from top of terr	minal to mounting plane	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R	_	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range		TJ		-65 to +150	°C	
Maximum storage temperature range		T _{Stg}		-65 to +175		
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	3.0		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W	
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	60		
Approximate weight				2	g	
				0.07	oz.	
minin				6 (5)	kgf · cm	
Mounting torque -	maximum				(lbf · in)	
Marking device		Case style D ² PAK		MBRB1	545CT	
			Case style TO-262	MBR1545CT-1		





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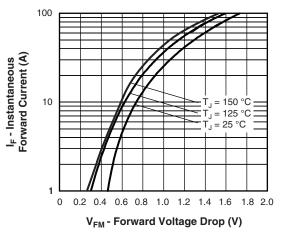


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

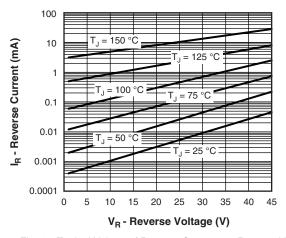


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

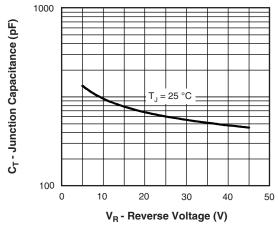


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

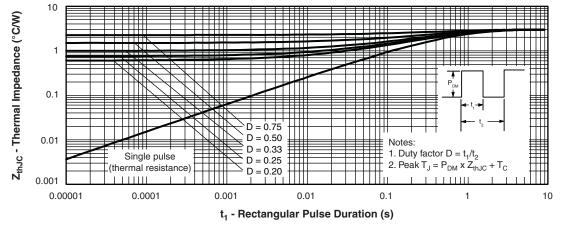


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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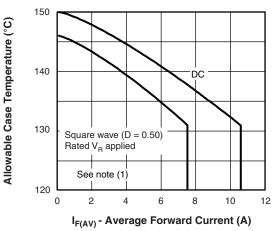


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

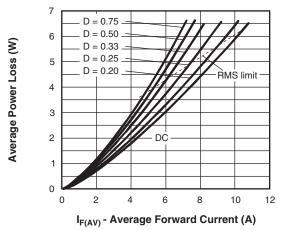


Fig. 6 - Forward Power Loss Characteristics

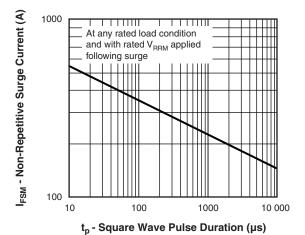


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

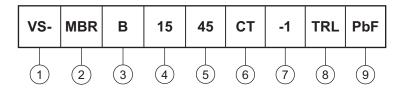


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ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Essential part number
- - None = TO-262 7 = -1
- Current rating (15 = 15 A)

 Voltage ratings 35 = 35 V
 45 = 45 V
- 6 CT = essential part number
- 7 • None = D²PAK 3 = B • -1 = TO-262 3 None
- None = tube (50 pieces)
 - TRL = tape and reel (left oriented for D²PAK only)
 - TRR = tape and reel (right oriented for D²PAK only)
- 9 • PbF = lead (Pb)-free (for TO-262 and D²PAK tube)
 - P = lead (Pb)-free (for D²PAK TRR and TRL)

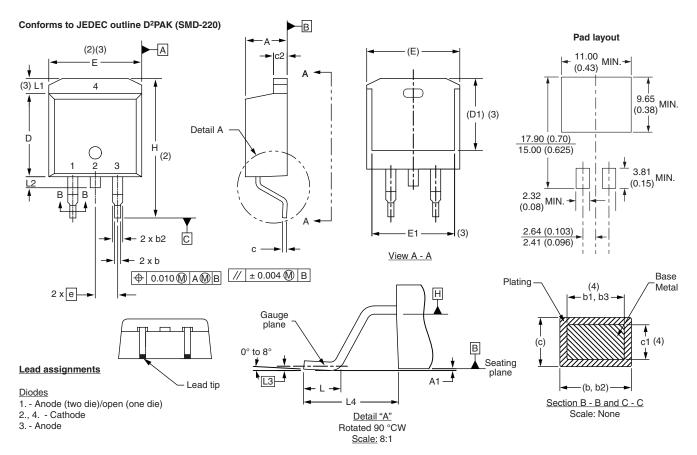
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			
SPICE model	www.vishay.com/doc?95294			



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D²PAK, TO-262

DIMENSIONS - D²PAK in millimeters and inches



	1		1		t .
SYMBOL	MILLIMETERS		INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIMETERS		INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

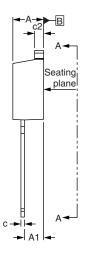
Document Number: 95014 Revision: 31-Mar-09

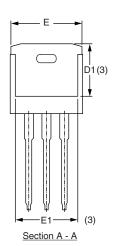
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D²PAK, TO-262



DIMENSIONS - TO-262 in millimeters and inches





⊕ 0.010 **M** A **M** B

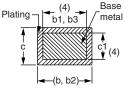
Lead assignments



Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIMETERS		INC	NOTEO	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
L	13.46	14.10	0.530	0.555	
L1	=	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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