

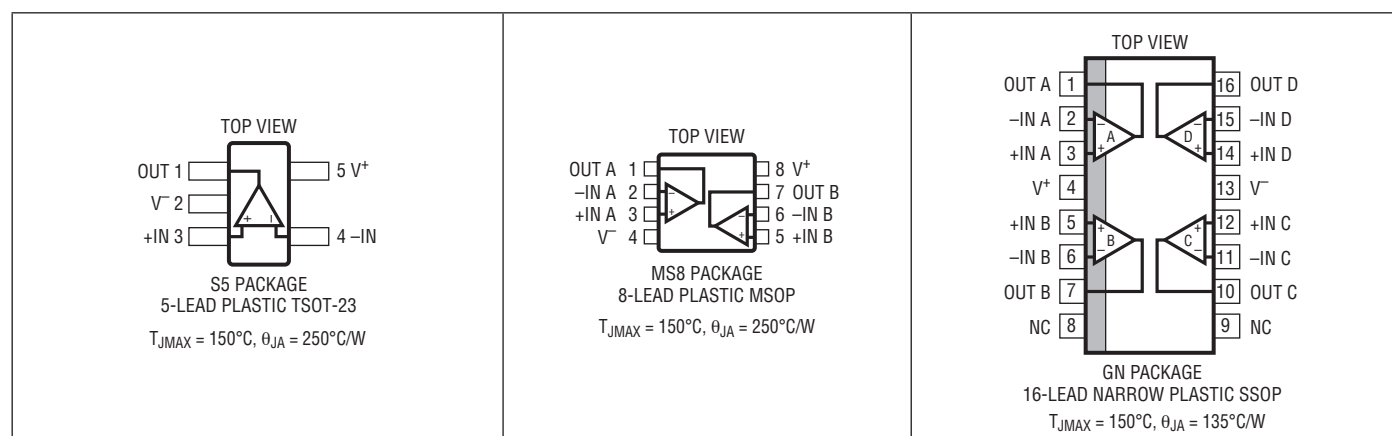
LT6205/LT6206/LT6207

ABSOLUTE MAXIMUM RATINGS (Note 1)

Total Supply Voltage (V^+ to V^-) 12.6V
 Input Current $\pm 10\text{mA}$
 Input Voltage Range (Note 2) $\pm V_S$
 Output Short-Circuit Duration (Note 3) Indefinite
 Pin Current While Exceeding Supplies (Note 9) ... $\pm 25\text{mA}$
 Operating Temperature Range (Note 4)
 LT6205C/LT6206C/LT6207C,
 LT6205I/LT6206I/LT6207I -40°C to 85°C
 LT6205H -40°C to 125°C

Specified Temperature Range (Note 4)
 LT6205C/LT6206C/LT6207C 0°C to 70°C
 LT6205I/LT6206I/LT6207I -40°C to 85°C
 LT6205H -40°C to 125°C
 Storage Temperature Range -65°C to 150°C
 Maximum Junction Temperature 150°C
 Lead Temperature (Soldering, 10 sec) 300°C

PIN CONFIGURATION



ORDER INFORMATION

LEAD FREE FINISH	TAPE AND REEL	PART MARKING*	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE
LT6205CS5#PBF	LT6205CS5#TRPBF	LTAEM	5-Lead Plastic TSOT-23	0°C to 70°C
LT6205IS5#PBF	LT6205IS5#TRPBF	LTAEM	5-Lead Plastic TSOT-23	-40°C to 85°C
LT6205HS5#PBF	LT6205HS5#TRPBF	LTAEM	5-Lead Plastic TSOT-23	-40°C to 125°C
LT6206CMS8#PBF	LT6206CMS8#TRPBF	LTH3	8-Lead Plastic MSOP	0°C to 70°C
LT6206IMS8#PBF	LT6206IMS8#TRPBF	LTH4	8-Lead Plastic MSOP	-40°C to 85°C
LT6207CGN#PBF	LT6207CGN#TRPBF	6207	16-Lead Narrow Plastic SSOP	0°C to 70°C
LT6207IGN#PBF	LT6207IGN#TRPBF	6207I	16-Lead Narrow Plastic SSOP	-40°C to 85°C

Consult LTC Marketing for parts specified with wider operating temperature ranges. *The temperature grade is identified by a label on the shipping container. Consult LTC Marketing for information on non-standard lead based finish parts.

For more information on lead free part marking, go to: <http://www.linear.com/leadfree/>

For more information on tape and reel specifications, go to: <http://www.linear.com/tapeandreel/>

ELECTRICAL CHARACTERISTICS The ● denotes specifications which apply over the specified temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_S = 3\text{V}$, 0V ; $V_S = 5\text{V}$, 0V ; $V_{\text{CM}} = V_{\text{OUT}} = 1\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT6205C/LT6206C/LT6207C LT6205I/LT6206I/LT6207I			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	●		1	3.5 5	mV mV
	Input Offset Voltage Match (Channel-to-Channel) (Note 5)	●		1	3 4	mV mV
	Input Offset Voltage Drift (Note 6)	●		7	15	$\mu\text{V}/^\circ\text{C}$
I_B	Input Bias Current	●		10	30	μA
I_{OS}	Input Offset Current	●		0.6	3	μA
	Input Noise Voltage	0.1Hz to 10Hz		2		$\mu\text{V}_{\text{P-P}}$
e_n	Input Noise Voltage Density	$f = 10\text{kHz}$		9		$\text{nV}/\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 10\text{kHz}$		4		$\text{pA}/\sqrt{\text{Hz}}$
	Input Resistance	$V_{\text{CM}} = 0\text{V}$ to $V^+ - 2\text{V}$		1		$\text{M}\Omega$
	Input Capacitance			2		pF
CMRR	Common Mode Rejection Ratio	$V_{\text{CM}} = 0\text{V}$ to $V^+ - 2\text{V}$	●	78	90	dB
	Input Voltage Range		●	0	$V^+ - 2$	V
PSRR	Power Supply Rejection Ratio	$V_S = 3\text{V}$ to 12V $V_{\text{CM}} = V_{\text{OUT}} = 0.5\text{V}$	●	67	75	dB
	Minimum Supply Voltage	$V_{\text{CM}} = 0.5\text{V}$	●		2.7	V
A_{VOL}	Large-Signal Voltage Gain	$V_S = 5\text{V}$, $V_O = 0.5\text{V}$ to 4.5V , $R_L = 1\text{k}$ $V_S = 5\text{V}$, $V_O = 1\text{V}$ to 3V , $R_L = 150\Omega$ $V_S = 3\text{V}$, $V_O = 0.5\text{V}$ to 2.5V , $R_L = 1\text{k}$	● ● ●	30 5 20	100 20 60	V/mV V/mV V/mV
V_{OL}	Output Voltage Swing Low (Note 7)	No Load, Input Overdrive = 30mV $I_{\text{SINK}} = 5\text{mA}$ $V_S = 5\text{V}$, $I_{\text{SINK}} = 25\text{mA}$ $V_S = 3\text{V}$, $I_{\text{SINK}} = 15\text{mA}$	● ● ● ●	10 75 300 200	25 150 500 350	mV mV mV mV
V_{OH}	Output Voltage Swing High (Note 7)	No Load, Input Overdrive = 30mV $I_{\text{SOURCE}} = 5\text{mA}$ $V_S = 5\text{V}$, $I_{\text{SOURCE}} = 25\text{mA}$ $V_S = 3\text{V}$, $I_{\text{SOURCE}} = 15\text{mA}$	● ● ● ●	60 150 650 300	100 250 1200 500	mV mV mV mV
I_{SC}	Short-Circuit Current	$V_S = 5\text{V}$, Output Shorted to GND	●	35 20	60	mA mA
		$V_S = 3\text{V}$, Output Shorted to GND	●	30 20	50	mA mA
I_S	Supply Current per Amplifier		●	3.75	5 5.75	mA mA
GBW	Gain Bandwidth Product	$f = 2\text{MHz}$	●	65	100	MHz
SR	Slew Rate	$V_S = 5\text{V}$, $A_V = 2$, $R_F = R_G = 1\text{k}$ $V_O = 1\text{V}$ to 4V , Measured from 1.5V to 3.5V		450		$\text{V}/\mu\text{s}$
	Channel Separation	$f = 10\text{MHz}$		90		dB
FPBW	Full Power Bandwidth	$V_{\text{OUT}} = 2V_{\text{P-P}}$ (Note 8)		71		MHz
t_s	Settling Time to 3% Settling Time to 1%	$V_S = 5\text{V}$, $\Delta V_{\text{OUT}} = 2\text{V}$, $A_V = -1$, $R_L = 150\Omega$		15 25		ns ns
	Differential Gain Differential Phase	$V_S = 5\text{V}$, $A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V $V_S = 5\text{V}$, $A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V		0.05 0.08		% Deg

LT6205/LT6206/LT6207

ELECTRICAL CHARACTERISTICS The ● denotes specifications which apply over the specified temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_S = \pm 5\text{V}$; $V_{CM} = V_{OUT} = 0\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		LT6205C/LT6206C/LT6207C LT6205I/LT6206I/LT6207I			UNITS
				MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●		1	4.5 6	mV mV
	Input Offset Voltage Match (Channel-to-Channel) (Note 5)		●		1	3 4	mV mV
	Input Offset Voltage Drift (Note 6)		●		10	18	$\mu\text{V}/^\circ\text{C}$
I_B	Input Bias Current		●		18	30	μA
I_{OS}	Input Offset Current		●		0.6	3	μA
	Input Noise Voltage	0.1Hz to 10Hz			2		μV_{P-P}
e_n	Input Noise Voltage Density	$f = 10\text{kHz}$			9		$\text{nV}/\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 10\text{kHz}$			4		$\text{pA}/\sqrt{\text{Hz}}$
	Input Resistance	$V_{CM} = -5\text{V}$ to 3V			1		$\text{M}\Omega$
	Input Capacitance				2		pF
CMRR	Common Mode Rejection Ratio	$V_{CM} = -5\text{V}$ to 3V	●	78	90		dB
	Input Voltage Range		●	-5		3	V
PSRR	Power Supply Rejection Ratio	$V_S = \pm 2\text{V}$ to $\pm 6\text{V}$	●	67	75		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = -4\text{V}$ to 4V , $R_L = 1\text{k}$	●	50	133		V/mV
		$V_O = -3\text{V}$ to 3V , $R_L = 150\Omega$	●	7.5	20		V/mV
	Output Voltage Swing	No Load, Input Overdrive = 30mV $I_{OUT} = \pm 5\text{mA}$ $I_{OUT} = \pm 25\text{mA}$	●	± 4.88	± 4.92		V
			●	± 4.75	± 4.85		V
			●	± 3.8	± 4.35		V
I_{SC}	Short-Circuit Current	Short to Ground	●	± 40 ± 30	± 60		mA mA
I_S	Supply Current per Amplifier		●		4	5.6 6.5	mA mA
GBW	Gain Bandwidth Product	$f = 2\text{MHz}$	●	65	100		MHz
SR	Slew Rate	$A_V = -1$, $R_L = 1\text{k}$ $V_O = -4\text{V}$ to 4V , Measured from -3V to 3V		350	600		$\text{V}/\mu\text{s}$
		Channel Separation			90		dB
FPBW	Full Power Bandwidth	$V_{OUT} = 8\text{V}_{P-P}$ (Note 8)		14	24		MHz
t_s	Settling Time to 3% Settling Time to 1%	$\Delta V_{OUT} = 2\text{V}$, $A_V = -1$, $R_L = 150\Omega$			15 25		ns ns
					0.05 0.08		% Deg
	Differential Gain	$A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V			0.05		%
	Differential Phase	$A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V			0.08		Deg

The ● denotes specifications which apply over the full specified temperature range, $-40^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_S = 3\text{V}$, 0V ; $V_S = 5\text{V}$, 0V ; $V_{CM} = V_{OUT} = 1\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		LT6205H			UNITS
				MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●		1	3.5 6	mV mV
	Input Offset Voltage Drift (Note 6)		●			20	$\mu\text{V}/^\circ\text{C}$
I_B	Input Bias Current		●			45	μA

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ELECTRICAL CHARACTERISTICS

The ● denotes specifications which apply over the full specified temperature range, $-40^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$, otherwise specifications are at $T_A = 25^{\circ}\text{C}$. $V_S = 3\text{V}$, 0V ; $V_S = 5\text{V}$, 0V ; $V_{\text{CM}} = V_{\text{OUT}} = 1\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT6205H			UNITS
			MIN	TYP	MAX	
I_{OS}	Input Offset Current	●			5	μA
	Input Noise Voltage	0.1Hz to 10Hz		2		$\mu\text{V}_{\text{P-P}}$
e_n	Input Noise Voltage Density	$f = 10\text{kHz}$		9		$\text{nV}/\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 10\text{kHz}$		4		$\text{pA}/\sqrt{\text{Hz}}$
	Input Resistance	$V_{\text{CM}} = 0\text{V}$ to $V^+ - 2\text{V}$		1		$\text{M}\Omega$
	Input Capacitance			2		pF
CMRR	Common Mode Rejection Ratio	$V_{\text{CM}} = 0\text{V}$ to $V^+ - 2\text{V}$	●	72		dB
	Input Voltage Range	●	0		$V^+ - 2$	V
PSRR	Power Supply Rejection Ratio	$V_S = 3\text{V}$ to 12V $V_{\text{CM}} = V_{\text{OUT}} = 0.5\text{V}$	●	62		dB
	Minimum Supply Voltage	$V_{\text{CM}} = 0.5\text{V}$	●		2.7	V
A_{VOL}	Large-Signal Voltage Gain	$V_S = 5\text{V}$, $V_0 = 0.5\text{V}$ to 4.5V , $R_L = 1\text{k}$ $V_S = 5\text{V}$, $V_0 = 1\text{V}$ to 3V , $R_L = 150\Omega$ $V_S = 3\text{V}$, $V_0 = 0.5\text{V}$ to 2.5V , $R_L = 1\text{k}$	● ● ●	25 3.5 15		V/mV V/mV V/mV
V_{OL}	Output Voltage Swing Low (Note 7)	No Load, Input Overdrive = 30mV $I_{\text{SINK}} = 5\text{mA}$ $V_S = 5\text{V}$, $I_{\text{SINK}} = 25\text{mA}$ $V_S = 3\text{V}$, $I_{\text{SINK}} = 15\text{mA}$	● ● ● ●		40 200 600 400	mV mV mV mV
V_{OH}	Output Voltage Swing High (Note 7)	No Load, Input Overdrive = 30mV $I_{\text{SOURCE}} = 5\text{mA}$ $V_S = 5\text{V}$, $I_{\text{SOURCE}} = 25\text{mA}$ $V_S = 3\text{V}$, $I_{\text{SOURCE}} = 15\text{mA}$	● ● ● ●		125 300 1400 600	mV mV mV mV
I_{SC}	Short-Circuit Current	$V_S = 5\text{V}$, Output Shorted to GND	●	35 20	60	mA mA
		$V_S = 3\text{V}$, Output Shorted to GND	●	30 15	50	mA mA
I_S	Supply Current per Amplifier	●		3.75	5 6.5	mA mA
GBW	Gain Bandwidth Product	$f = 2\text{MHz}$	●	50		MHz
SR	Slew Rate	$V_S = 5\text{V}$, $A_V = 2$, $R_F = R_G = 1\text{k}$ $V_0 = 1\text{V}$ to 4V , Measured from 1.5V to 3.5V		450		$\text{V}/\mu\text{s}$
	Channel Separation	$f = 10\text{MHz}$		90		dB
FPBW	Full Power Bandwidth	$V_{\text{OUT}} = 2\text{V}_{\text{P-P}}$ (Note 8)		71		MHz
t_s	Settling Time to 3% Settling Time to 1%	$V_S = 5\text{V}$, $\Delta V_{\text{OUT}} = 2\text{V}$, $A_V = -1$, $R_L = 150\Omega$		15 25		ns ns
	Differential Gain Differential Phase	$V_S = 5\text{V}$, $A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V $V_S = 5\text{V}$, $A_V = 2$, $R_L = 150\Omega$, Output Black Level = 1V		0.05 0.08		% Deg

The ● denotes specifications which apply over the full specified temperature range, $-40^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$, otherwise specifications are at $T_A = 25^{\circ}\text{C}$. $V_S = \pm 5\text{V}$; $V_{\text{CM}} = V_{\text{OUT}} = 0\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT6205H			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	●		1.3	4.5 7	mV mV
	Input Offset Voltage Drift (Note 6)	●			25	$\mu\text{V}/^{\circ}\text{C}$

LT6205/LT6206/LT6207

ELECTRICAL CHARACTERISTICS The ● denotes specifications which apply over the full specified temperature range, $-40^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$, otherwise specifications are at $T_A = 25^{\circ}\text{C}$. $V_S = \pm 5\text{V}$; $V_{CM} = V_{OUT} = 0\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT6205H			UNITS
			MIN	TYP	MAX	
I_B	Input Bias Current		●		50	μA
I_{OS}	Input Offset Current		●		5	μA
	Input Noise Voltage	0.1Hz to 10Hz		2		μV_{P-P}
e_n	Input Noise Voltage Density	$f = 10\text{kHz}$		9		$\text{nV}/\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 10\text{kHz}$		4		$\text{pA}/\sqrt{\text{Hz}}$
	Input Resistance	$V_{CM} = -5\text{V to } 3\text{V}$		1		$\text{M}\Omega$
	Input Capacitance			2		pF
CMRR	Common Mode Rejection Ratio	$V_{CM} = -5\text{V to } 3\text{V}$	●	72		dB
	Input Voltage Range		●	-5	3	V
PSRR	Power Supply Rejection Ratio	$V_S = \pm 2\text{V to } \pm 6\text{V}$	●	62		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = -4\text{V to } 4\text{V}, R_L = 1\text{k}$	●	40		V/mV
		$V_O = -3\text{V to } 3\text{V}, R_L = 150\Omega$	●	5		V/mV
	Output Voltage Swing	No Load, Input Overdrive = 30mV	●	± 4.85		V
		$I_{OUT} = \pm 5\text{mA}$	●	± 4.65		V
		$I_{OUT} = \pm 25\text{mA}$	●	± 3.5		V
I_{SC}	Short-Circuit Current	Short to Ground	●	± 40 ± 20	± 60	mA mA
I_S	Supply Current per Amplifier		●	4	5.6 7.5	mA mA
GBW	Gain Bandwidth Product	$f = 2\text{MHz}$	●	50		MHz
SR	Slew Rate	$A_V = -1, R_L = 1\text{k}$ $V_O = -4\text{V to } 4\text{V}$, Measured from $-3\text{V to } 3\text{V}$		350	600	$\text{V}/\mu\text{s}$
	Channel Separation	$f = 10\text{MHz}$		90		dB
FPBW	Full Power Bandwidth	$V_{OUT} = 8V_{P-P}$ (Note 8)		14	24	MHz
t_s	Settling Time to 3%	$\Delta V_{OUT} = 2\text{V}, A_V = -1, R_L = 150\Omega$		15		ns
	Settling Time to 1%			25		ns
	Differential Gain	$A_V = 2, R_L = 150\Omega$, Output Black Level = 1V		0.05		%
	Differential Phase	$A_V = 2, R_L = 150\Omega$, Output Black Level = 1V		0.08		Deg

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The inputs are protected by back-to-back diodes. If the differential input voltage exceeds 1.4V, the input current should be limited to less than 10mA.

Note 3: A heat sink may be required to keep the junction temperature below absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted.

Note 4: The LT6205C/LT6206C/LT6207C are guaranteed to meet specified performance from 0°C to 70°C and are designed, characterized and expected to meet specified performance from -40°C to 85°C but are not tested or QA sampled at these temperatures. The LT6205I/LT6206I/LT6207I are guaranteed to meet specified performance from -40°C to 85°C . The LT6205H is guaranteed to meet specified performance from -40°C to 125°C .

Note 5: Matching parameters are the difference between the two amplifiers A and D and between B and C of the LT6207; between the two amplifiers of the LT6206.

Note 6: This parameter is not 100% tested.

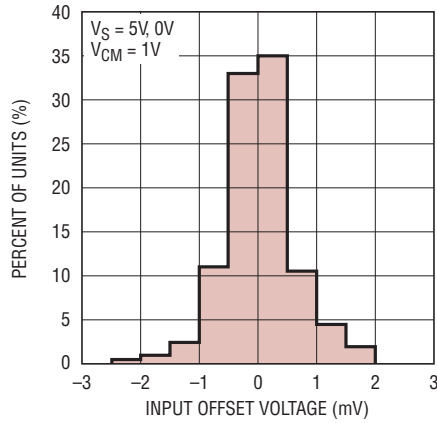
Note 7: Output voltage swings are measured between the output and power supply rails.

Note 8: Full power bandwidth is calculated from the slew rate measurement: $\text{FPBW} = \text{SR}/2\pi V_{PEAK}$.

Note 9: There are reverse biased ESD diodes on all inputs and outputs. If these pins are forced beyond either supply, unlimited current will flow through these diodes. If the current is transient in nature and limited to less than 25mA, no damage to the device will occur.

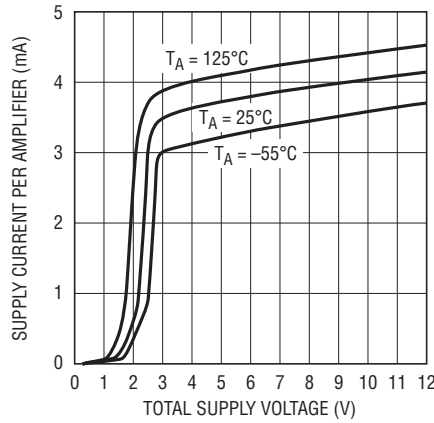
TYPICAL PERFORMANCE CHARACTERISTICS

V_{OS} Distribution



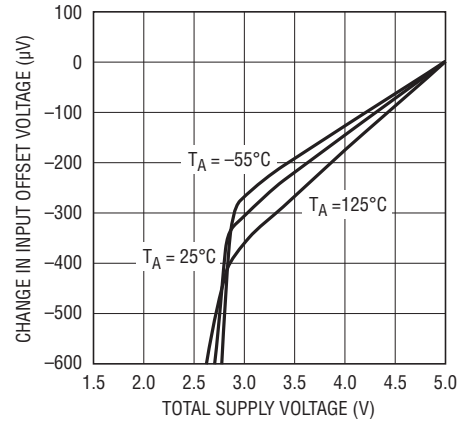
620567 G01

Supply Current per Amplifier vs Supply Voltage



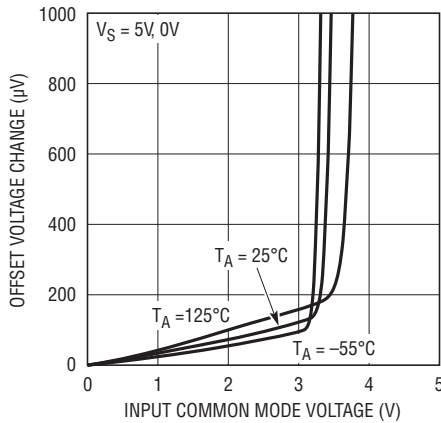
620567 G02

Minimum Supply Voltage



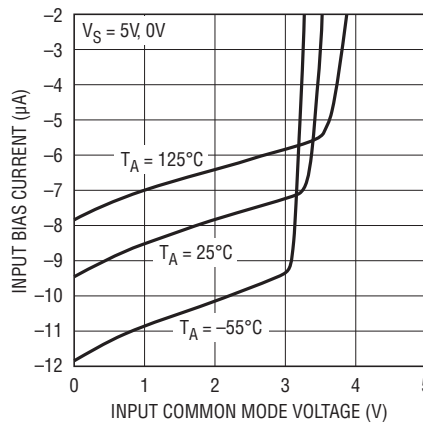
620567 G03

Change in Offset Voltage vs Input Common Mode Voltage



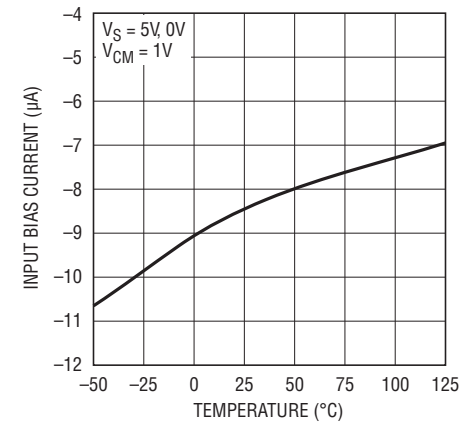
620567 G04

Input Bias Current vs Input Common Mode Voltage



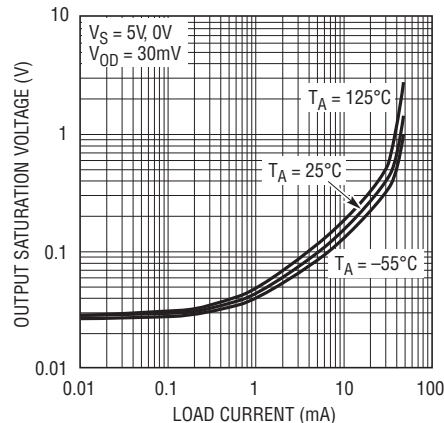
620567 G05

Input Bias Current vs Temperature



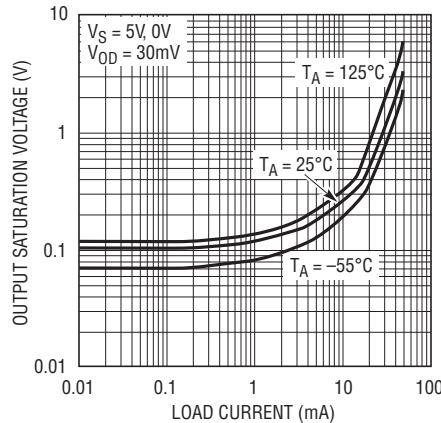
620567 G06

Output Saturation Voltage vs Load Current (Output Low)



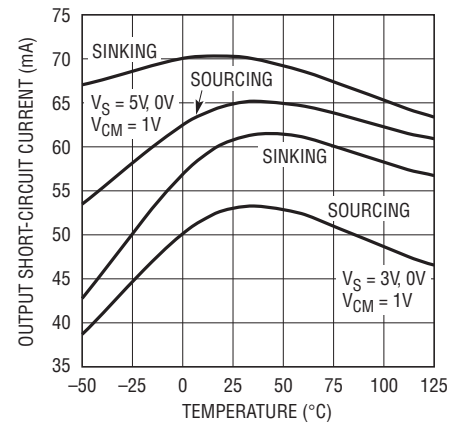
620567 G07

Output Saturation Voltage vs Load Current (Output High)



620567 G08

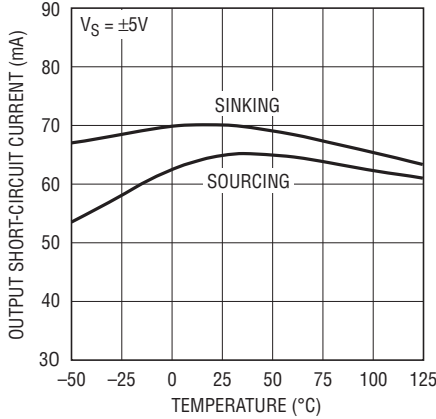
Short-Circuit Current vs Temperature



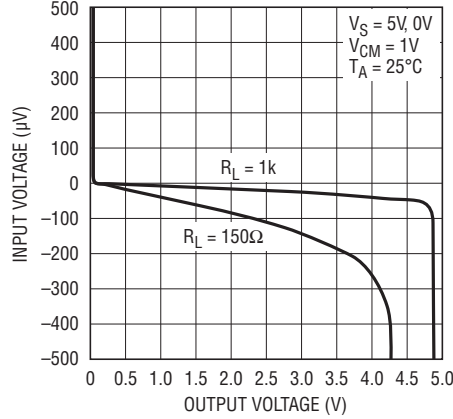
620567 G09

TYPICAL PERFORMANCE CHARACTERISTICS

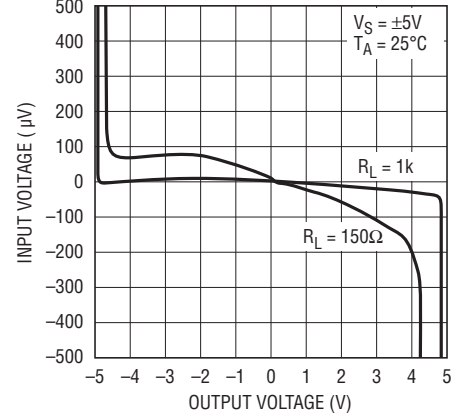
Short-Circuit Current vs Temperature



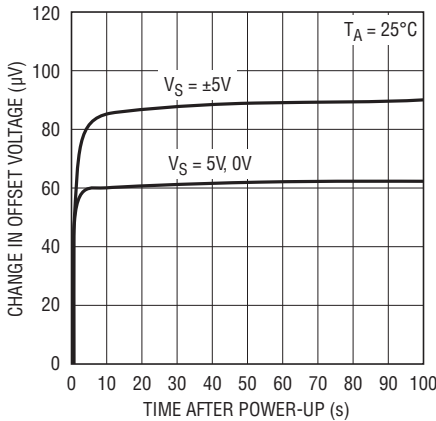
Open-Loop Gain



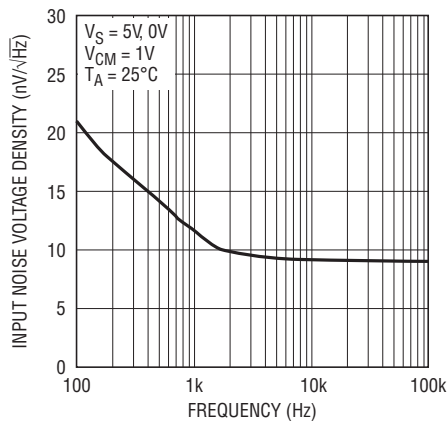
Open-Loop Gain



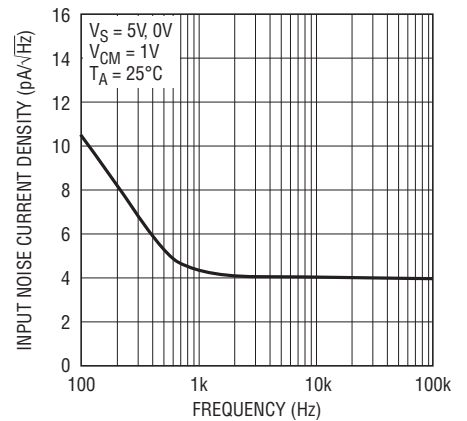
Warm Up Drift vs Time (LT6206)



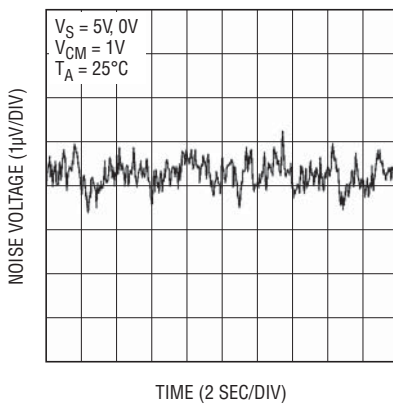
Input Noise Voltage Density vs Frequency



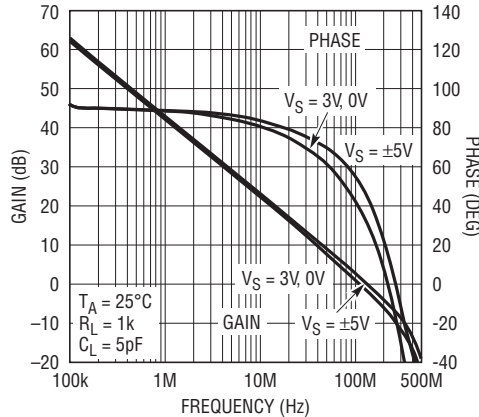
Input Noise Current Density vs Frequency



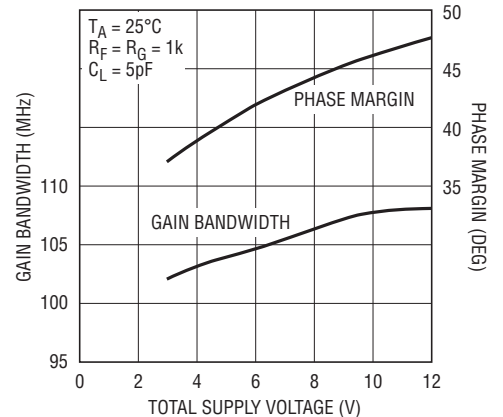
0.1Hz to 10Hz Noise Voltage



Gain and Phase vs Frequency

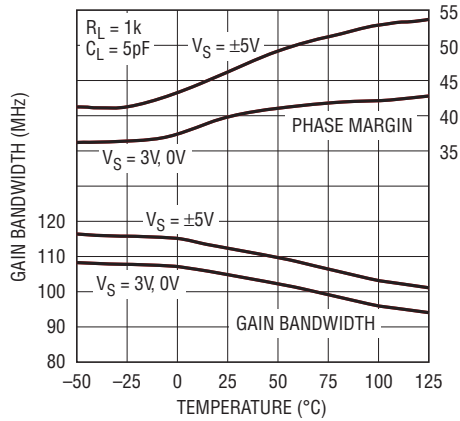


Gain Bandwidth and Phase Margin vs Supply Voltage



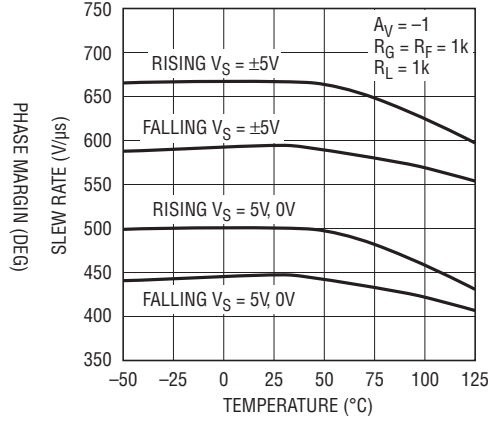
TYPICAL PERFORMANCE CHARACTERISTICS

Gain Bandwidth and Phase Margin vs Temperature



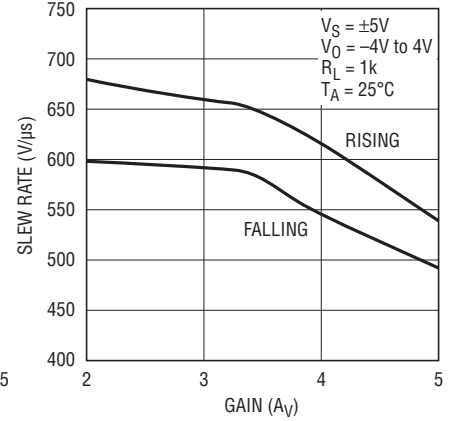
620567 G19

Slew Rate vs Temperature



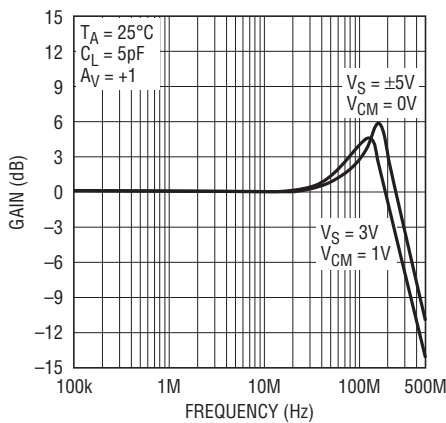
620567 G20

Slew Rate vs Closed-Loop Gain



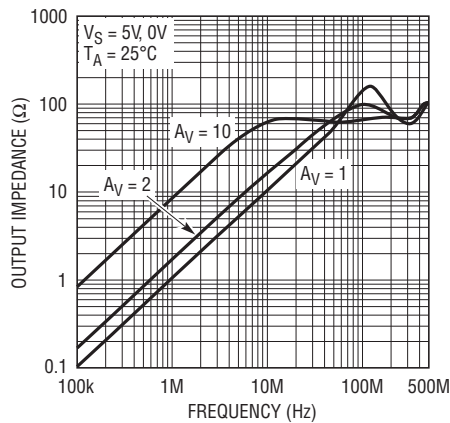
620567 G21

Closed-Loop Gain vs Frequency



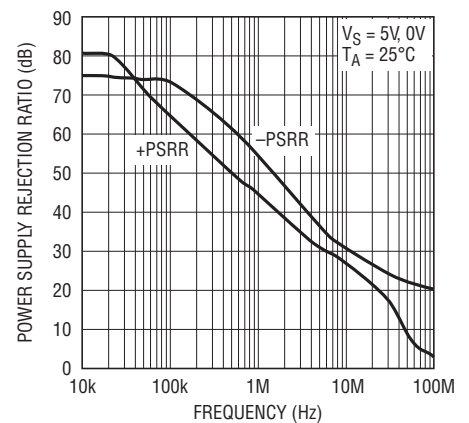
620567 G22

Output Impedance vs Frequency



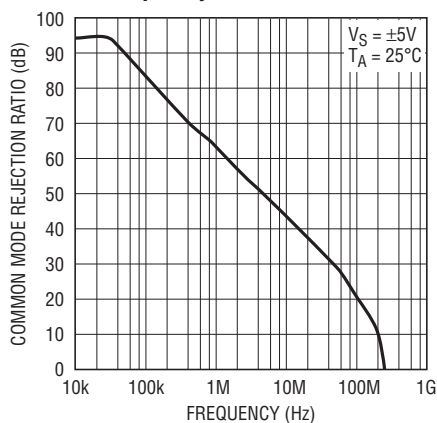
620567 G23

Power Supply Rejection Ratio vs Frequency



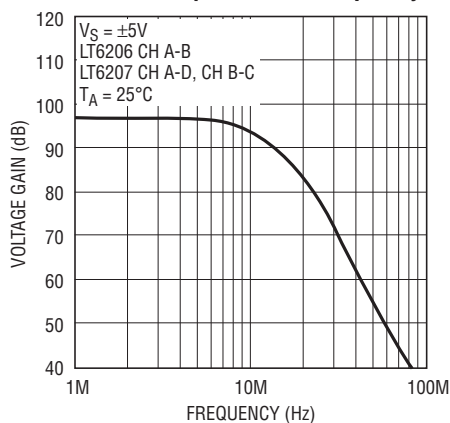
620567 G24

Common Mode Rejection Ratio vs Frequency



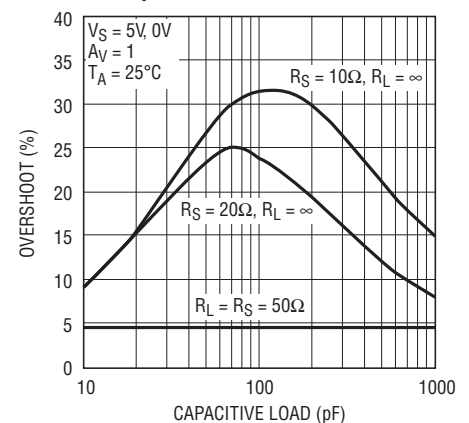
620567 G25

Channel Separation vs Frequency



620567 G26

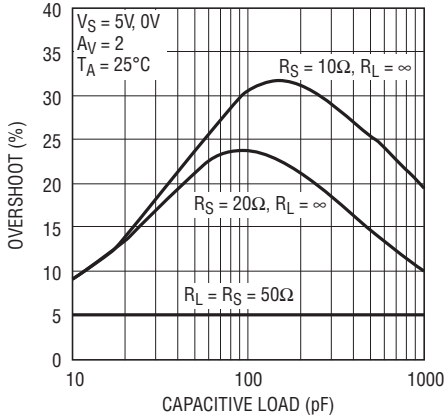
Series Output Resistor vs Capacitive Load



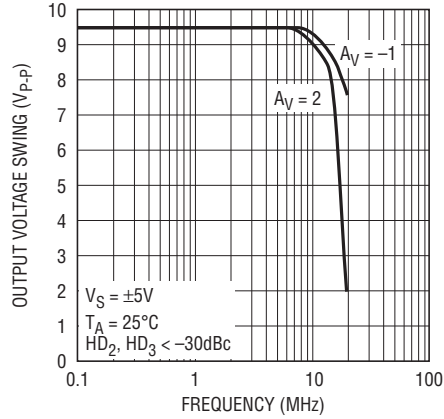
620567 G27

TYPICAL PERFORMANCE CHARACTERISTICS

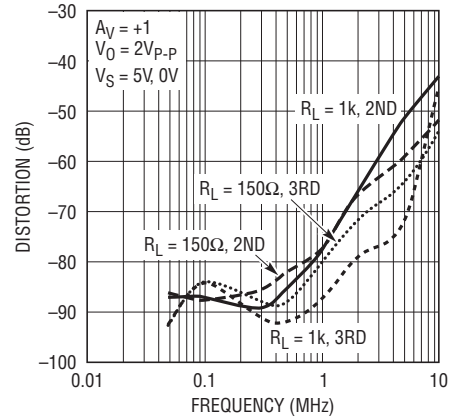
Series Output Resistor vs Capacitive Load



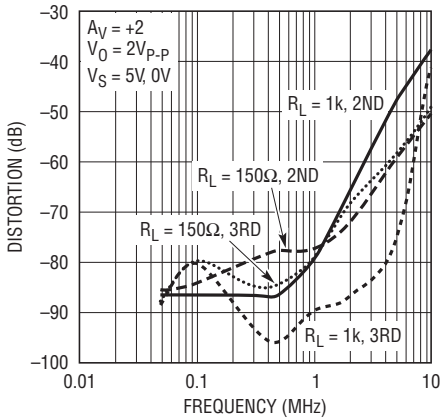
Maximum Undistorted Output Signal vs Frequency



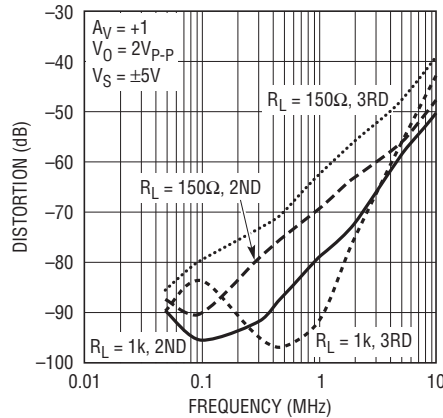
Distortion vs Frequency



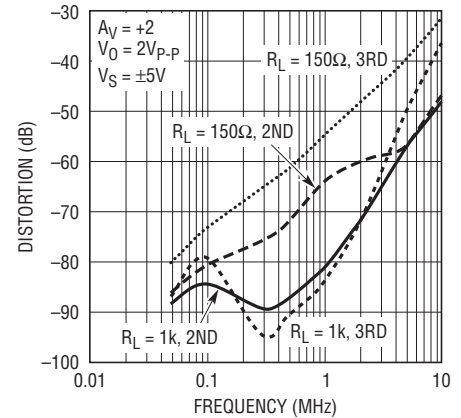
Distortion vs Frequency



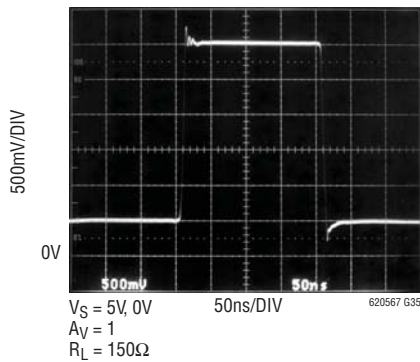
Distortion vs Frequency



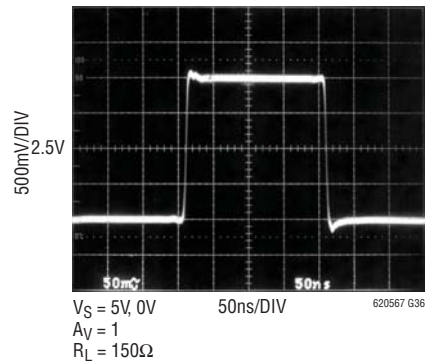
Distortion vs Frequency



Large Signal Response
VS = 5V, 0V



Small Signal Response
VS = 5V, 0V



APPLICATIONS INFORMATION

Amplifier Characteristics

Figure 1 shows a simplified schematic of the LT6205/LT6206/LT6207. The input stage consists of transistors Q1 to Q8 and resistor R1. This topology allows for high slew rates at low supply voltages. The input common mode range extends from ground to typically 1.75V from V_{CC} , and is limited by 2 VBEs plus a saturation voltage of a current source. There are back-to-back series diodes, D1 to D4, across the + and – inputs of each amplifier to limit the differential voltage to $\pm 1.4V$. R_{IN} limits the current through these diodes if the input differential voltage exceeds $\pm 1.4V$. The input stage drives the degeneration resistors of PNP and NPN current mirrors, Q9 to Q12, which convert the differential signals into a single-ended output. The complementary drive generator supplies current to the output transistors that swing from rail-to-rail.

The current generated through R1, divided by the capacitor C_M , determines the slew rate. Note that this current, and hence the slew rate, are proportional to the magnitude of the input step. The input step equals the output step divided by the closed loop gain. The highest slew rates are therefore obtained in the lowest gain configurations. The Typical Performance Characteristics curve of Slew Rate vs Closed-Loop Gain shows the details.

ESD

The LT6205/LT6206/LT6207 have reverse-biased ESD protection diodes on all inputs and outputs as shown in Figure 1. If these pins are forced beyond either supply unlimited current will flow through these diodes. If the current is transient, and limited to 25mA or less, no damage to the device will occur.

Layout and Passive Components

With a gain bandwidth product of 100MHz and a slew rate of 450V/ μ s the LT6205/LT6206/LT6207 require special attention to board layout and supply bypassing. Use a ground plane, short lead lengths and RF quality low ESR supply bypass capacitors. The positive supply pin should be bypassed with a small capacitor (typically 0.01 μ F to 0.1 μ F) within 0.25 inches of the pin. When driving heavy loads, an additional 4.7 μ F electrolytic capacitor should be used. When using split supplies, the same is true for the

negative supply pin. For optimum performance all feedback components and bypass capacitors should be contained in a 0.5 inch by 0.5 inch area. This helps ensure minimal stray capacitances.

The parallel combination of the feedback resistor and gain setting resistor on the inverting input can combine with the input capacitance to form a pole which can degrade stability. In general, use feedback resistors of 1k or less.

Capacitive Load

The LT6205/LT6206/LT6207 are optimized for wide bandwidth video applications. They can drive a capacitive load of 20pF in a unity-gain configuration. When driving a larger capacitive load, a resistor of 10 Ω to 50 Ω should be connected between the output and the capacitive load to avoid ringing or oscillation. The feedback should still be taken from the output pin so that the resistor will isolate the capacitive load and ensure stability. The Typical Performance Characteristics curves show the output overshoot when driving a capacitive load with different series resistors.

Video Signal Characteristics

Composite video is the most commonly used signal in broadcast grade products and includes luma (or luminance, the intensity information), chroma (the colorimetry information) and sync (vertical and horizontal raster timing) elements combined into a single signal, NTSC and PAL being the common formats. Component video for entertainment systems include separate signal(s) for the luma and chroma (i.e., Y/C or YPbPr) with sync generally applied to the luma channel (Y signal). In some instances, native RGB signals (separate intensity information for each primary color: red, green, blue) will have sync included as well. All the signal types that include sync are electrically similar from a voltage-swing standpoint, though various timing and bandwidth relationships exist depending on the applicable standard.

The typical video waveforms that include sync (including full composite) are specified to have nominal 1V_{P-P} amplitude. The lower 0.3V is reserved for sync tips that carry timing information, and by being at a lower potential than all the other information, represents blacker-than-

APPLICATIONS INFORMATION

black intensity, thereby causing scan retrace activity to be invisible on a CRT. The black level of the waveform is at (or set up very slightly above) the upper limit of the sync information. Waveform content above the black level is intensity information, with peak brightness represented at the maximum signal level. In the case of composite video, the modulated color subcarrier is superimposed on the waveform, but the dynamics remain inside the $1V_{P-P}$ limit (a notable exception is the chroma ramp used for differential-gain and differential-phase measurements, which can reach $1.15V_{P-P}$).

DC-Coupled Video Amplifier Considerations

Typically video amplifiers drive cables that are series terminated (back-terminated) at the source and load-terminated at the destination with resistances equal to the cable characteristic impedance, Z_0 (usually 75Ω). This configuration forms a 2:1 resistor divider in the cabling that must be accounted for in the driver amplifier by delivering $2V_{P-P}$ output into an effective $2 \cdot Z_0$ load (e.g., 150Ω). Driving the cable can require more than 13mA while the output is approaching the saturation limits of the amplifier output. The absolute minimum supply is: $V_{MIN} = 2 + V_{OH} + V_{OL}$. For example, the LT6206 dual operating on 3.3V as shown on the front page of this data sheet, with exceptionally low $V_{OH} \leq 0.5V$ and $V_{OL} \leq 0.35V$, provides a design margin of 0.45V. The design margin must be large enough to include supply variations and DC bias accuracy for the DC-coupled video input.

Handling AC-Coupled Video Signals

AC-coupled video inputs are intrinsically more difficult to handle than those with DC-coupling because the average signal voltage of the video waveform is effected by the picture content, meaning that the black level at the amplifier wanders with scene brightness. The wander is measured as 0.56V for a $1V_{P-P}$ NTSC waveform changing from black field to white field and vice-versa, so an additional 1.12V allowance must be made in the amplifier supply (assuming gain of 2, so $V_{MIN} = 3.12 + V_{OH} + V_{OL}$). For example, an LT6205 operating on 5V has a conservative design margin of 1.03V. The amplifier output (for gain of 2) must swing +1.47V to -1.65V around the DC-operating point, so the biasing circuitry needs to be designed accordingly for optimal fidelity.

Clamped AC-Input Cable Driver

A popular method of further minimizing supply requirements with AC-coupling is to employ a simple clamping scheme, as shown in Figure 2. In this circuit, the LT6205 operates from 3.3V by having the sync tips control the charge on the coupling capacitor C1, thereby reducing the black level input wander to $\approx 0.07V$. The only minor drawback to this circuit is the slight sync tip compression ($\approx 0.025V$ at input) due to the diode conduction current, though the picture content remains full fidelity. This circuit has nearly the design margin of its DC-coupled counterpart, at 0.31V (for this circuit, $V_{MIN} = 2.14 + V_{OH} + V_{OL}$). The clamp diode anode bias is selected to set the sync tip output voltage at or slightly above V_{OL} .

YPbPr to RGB Component Video Converter

The back page application uses the LT6207 quad to implement a minimum amplifier count topology to transcode consumer component video into RGB. In this circuit, signals only pass through one active stage from any input to any output, with passive additions being performed by the cable back-termination resistors. The compromise in using passive output addition is that the amplifier outputs must be twice as large as that of a conventional cable driver. The Y-channel section also has the demanding requirement that it single-handedly drives all three outputs to full brightness during times of white content, so a helper current source is used to assure unclipped video when operating from $\pm 5V$ supplies. This circuit maps sync-on-Y to sync on all the RGB channels, and for best results should have input black levels at 0V nominal to prevent clipping.

TYPICAL APPLICATION

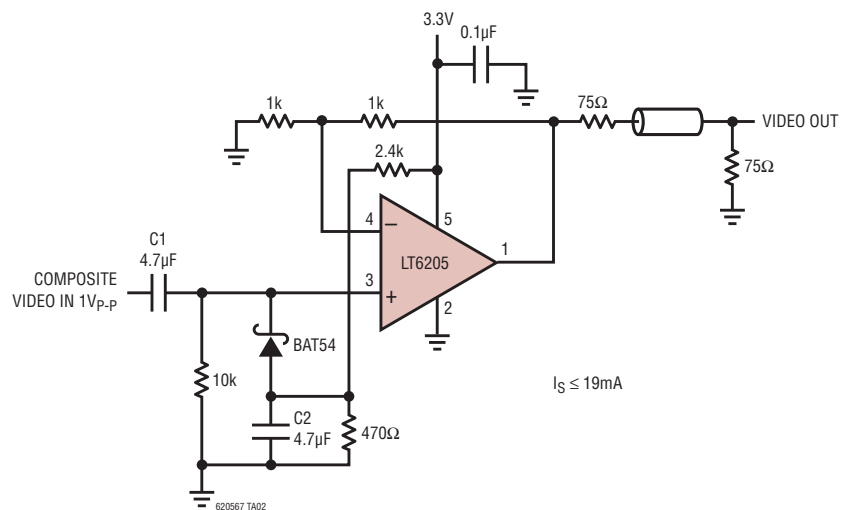
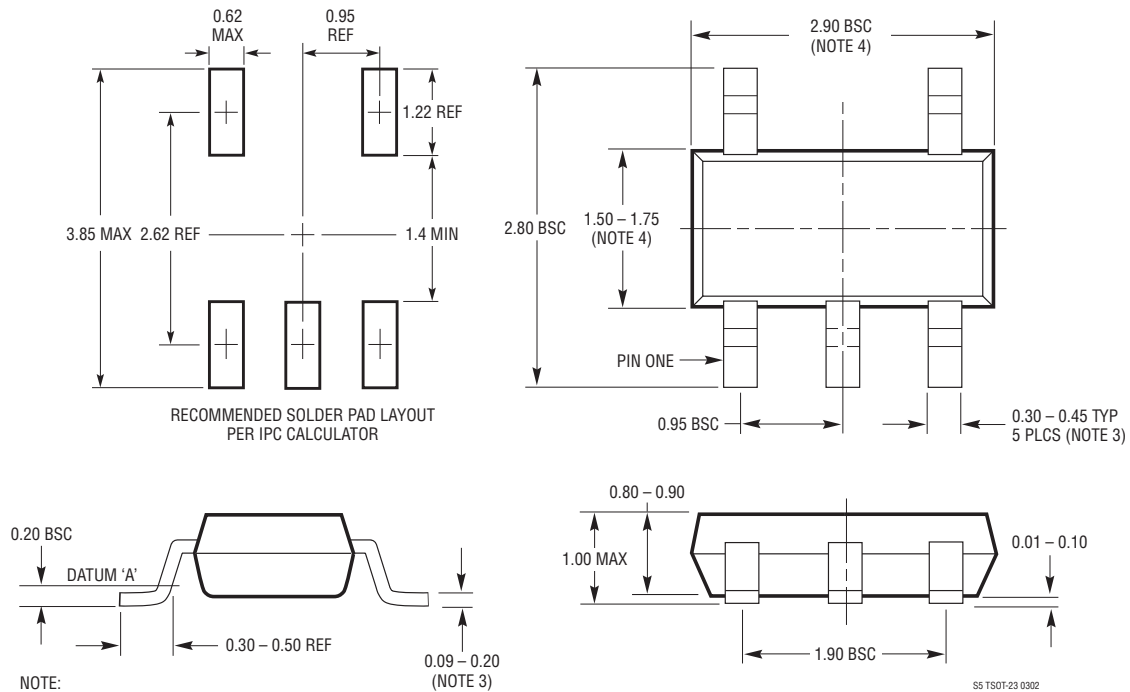


Figure 2. Clamped AC-Input Video Cable Driver

PACKAGE DESCRIPTION

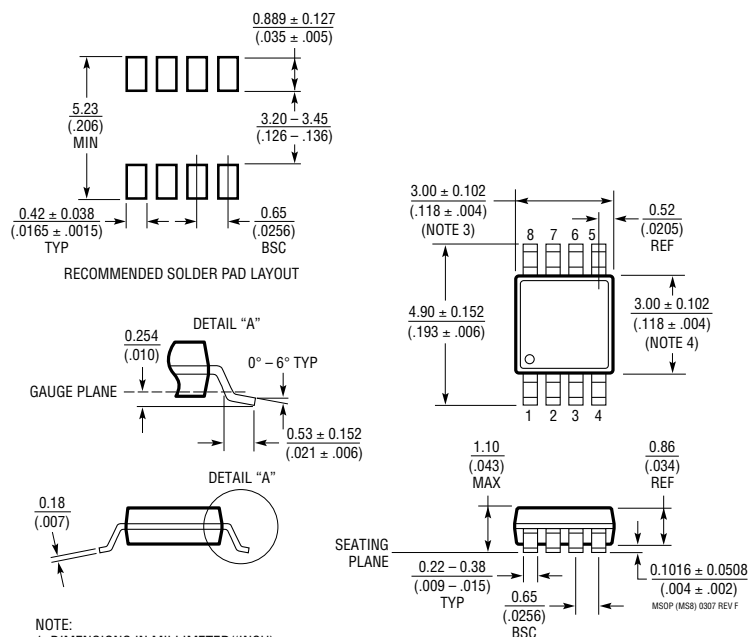
S5 Package 5-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1635)



PACKAGE DESCRIPTION

MS8 Package 8-Lead Plastic MSOP

(Reference LTC DWG # 05-08-1660)

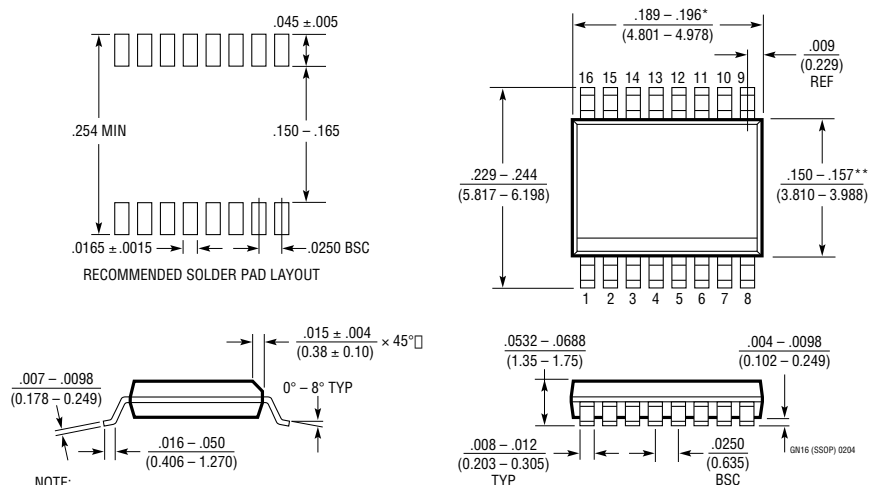


NOTE:

1. DIMENSIONS IN MILLIMETER/(INCH)
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX

GN Package 16-Lead Plastic SSOP (Narrow .150 Inch)

(Reference LTC DWG # 05-08-1641)



NOTE:

1. CONTROLLING DIMENSION: INCHES
2. DIMENSIONS ARE IN INCHES (MILLIMETERS)
3. DRAWING NOT TO SCALE
- *DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE
- **DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

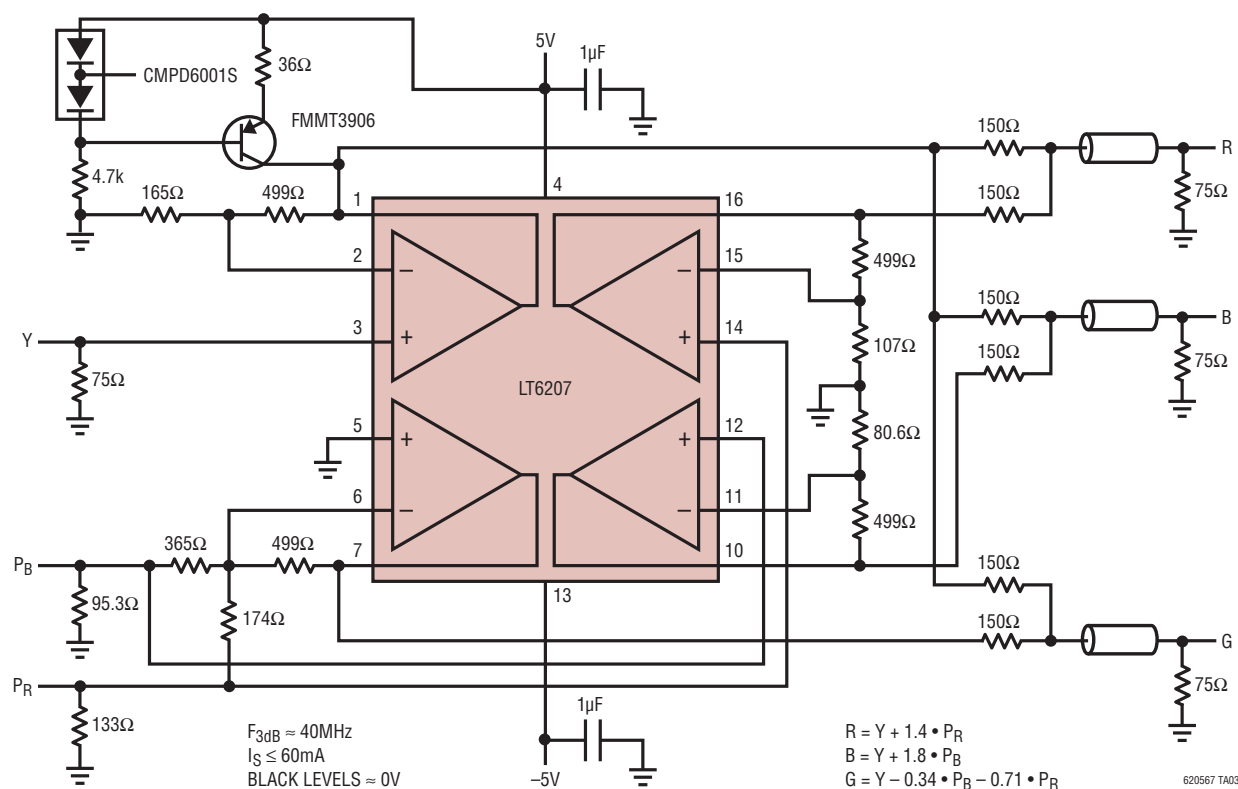
REVISION HISTORY (Revision history begins at Rev C)

REV	DATE	DESCRIPTION	PAGE NUMBER
C	3/10	C Grade Specified Temperature Range Changed in the Order Information Section	2

LT6205/LT6206/LT6207

TYPICAL APPLICATION

Y_PP_R to RGB Converter



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1253/LT1254	Low Cost Dual and Quad Video Amplifiers	-3dB Bandwidth = 90MHz, Current Feedback
LT1395/LT1396/LT1397	Single Dual Quad 400MHz Current Feedback Amplifiers	0.1dB Flatness to 100MHz, 80mA Output Drive
LT1675	RGB Multiplexer with Current Feedback Amplifiers	-3dB Bandwidth = 250MHz, 100MHz Pixel Switching
LT1809/LT1810	Single/Dual, 180MHz, Rail-to-Rail Input and Output Amplifiers	350V/μs Slew Rate, Shutdown, Low Distortion -90dBc at 5MHz
LT6550/LT6551	3.3V Triple and Quad Video Amplifiers	Internal Gain of 2, 110MHz -3dB Bandwidth, Input Common Modes to Ground
LT6552	3.3V Single Supply Video Difference Amplifier	Differential or Single-Ended Gain Block, 600V/μs Slew Rate, Input Common Modes to Ground