Pin Description

TYPE							
I	INPUT	Р	Power supply, GND	NC	NOT CONNECT		
0	OUTPUT						
В	BIDIRECTION						

■ I²C interface I2CCK I²C Clock pin В I2CDT В I²C Data pin ■D/A interface O D/A output (Hall bias input) **BIASO** ■Op-Amp interface **OPINP** I Op-Amp input Op-Amp input **OPINM** Ι ■Driver interface OUT1 O Actuator output pin OUT2 O Actuator output pin ■Power supply pin VDD P Digital power supply VSS P Digital GND VDDO P LDO power supply out P Motor power supply VMPower GND **PGND**

PIN TYPE "O" – Ensure that it is set to OPEN.

PIN TYPE "I" – OPEN is inhibited. Ensure that it is connected to the V_{DD} or V_{SS} even when it is unused.

(Please contact our company for more information about selection of V_{DD} or V_{SS}.)

PIN TYPE "B" – If you are unsure about processing method on the pin description of pin layout table, please contact us.

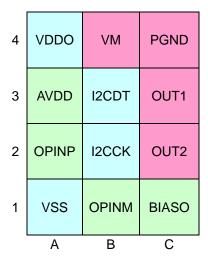
Note that incorrect processing of unused pins may result in defects.

If you have any question, please feel free to contact us.

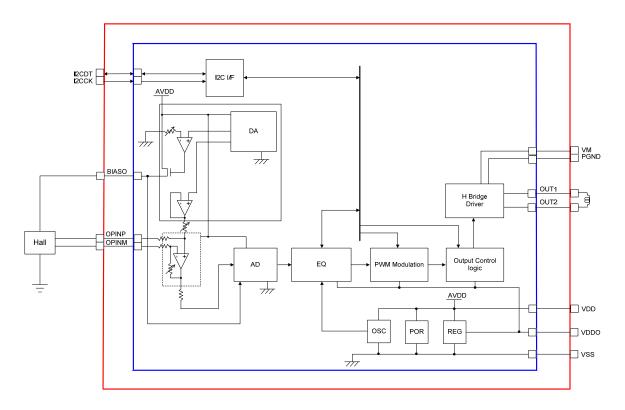
Pin Layout

Circuit Name	Number of Pins	Circuit Name	Number of Pins
Analog	4	Driver	4
Logic	4		

Backside pin layout diagram (Top View from the mold side)



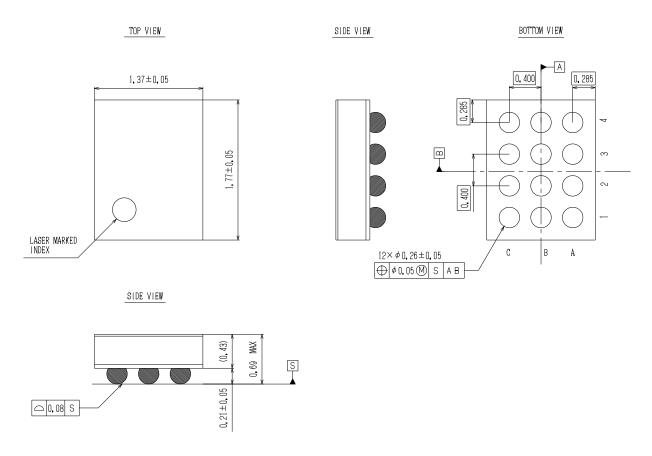
Block Diagram



Package Dimensions

WLP12K(1.77X1.37)

unit: mm



ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LC898212XC-MH	WLP12K(1.77X1.37) (Pb-Free / Halogen Free)	5000 / Tape & Reel

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