

## 5<sup>th</sup> Generation thinQ!™ SiC Schottky Diode

### 1 Description

ThinQ!™ Generation 5 represents Infineon leading edge technology for the SiC Schottky Barrier diodes. The Infineon proprietary diffusion soldering process, already introduced with G3 is now combined with a new, more compact design and thin-wafer technology. The result is a new family of products showing improved efficiency over all load conditions, resulting from both the improved thermal characteristics and a lower figure of merit (Qc x Vf).

The new thinQ! $^{\text{TM}}$  Generation 5 has been designed to complement our 650V CoolMOS $^{\text{TM}}$  families: this ensures meeting the most stringent application requirements in this voltage range.

#### **Features**

- Revolutionary semiconductor material Silicon Carbide
- Benchmark switching behavior
- No reverse recovery/ No forward recovery
- Temperature independent switching behavior
- · High surge current capability
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1)</sup> for target applications
- Breakdown voltage tested at 35 mA<sup>2)</sup>
- Optimized for high temperature operation

#### **Benefits**

- System efficiency improvement over Si diodes
- System cost / size savings due to reduced cooling requirements
- Enabling higher frequency / increased power density solutions
- Higher system reliability due to lower operating temperatures
- Reduced EMI

#### **Applications**

- Switch mode power supply
- Power factor correction
- Solar inverter
- Uninterruptible power supply

#### Table 1 Key Performance Parameters

Parameter	Value	Unit
$V_{DC}$	650	V
Q <sub>C</sub> ; V <sub>R</sub> =400V	23	nC
$E_{C}$ ; $V_{R}$ =400V	5.3	μJ
$I_F @ T_C < 135^{\circ}C$	16	Α

#### Table 2 Pin Definition

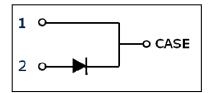
I able 2	i ili Dellilitioli						
Pin 1	Pin 2	Pin 3					
С	Α	n.a.					

Type / ordering Code	Package	Marking	Related links
IDH16G65C5	PG-TO220-2	D1665C5	www.infineon.com/sic

- 1) J-STD20 and JESD22
- 2) All devices tested under avalanche conditions for a time periode of 10ms

#### **IDH16G65C5**

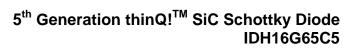














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**Maximum ratings** 

## 2 Maximum ratings

Table 3 Maximum ratings

Parameter	Symbol Values			Unit	Note/Test Condition	
		Min.	Тур.	Max.		
Continuous forward current	I <sub>F</sub>	_	_	16		<i>T</i> <sub>C</sub> < 135°C, D=1
Surge non-repetitive forward current, sine halfwave	I <sub>F,SM</sub>	_	_	124	1	$T_C = 25^{\circ}\text{C}, t_p = 10 \text{ ms}$
		_	_	105	A	$T_{\rm C}$ = 150°C, $t_{\rm p}$ =10 ms
Non-repetitive peak forward current	$I_{F,max}$	_	_	637		$T_{\rm C}$ = 25°C, $t_{\rm p}$ =10 µs
i²t value	∫ i²dt	_	_	76	A²s	$T_C = 25^{\circ}\text{C}, t_p = 10 \text{ ms}$
		_	_	55		$T_{\rm C}$ = 150°C, $t_{\rm p}$ =10 ms
Repetitive peak reverse voltage	$V_{RRM}$	_	_	650	V	$T_j = 25^{\circ} \text{C}$
Diode dv/dt ruggedness	dv/dt	_	_	100	V/ns	V <sub>R</sub> =0480 V
Power dissipation	P <sub>tot</sub>	_	_	129	W	T <sub>C</sub> = 25°C
Operating and storage temperature	$T_j;T_{stg}$	-55	_	175	°C	
Mounting torque		_	_	70	Ncm	M3 screws

## 3 Thermal characteristics

Table 4 Thermal characteristics TO-220-2

Parameter	Symbol	Values		Unit	Note/Test Condition	
		Min.	Тур.	Max.		
Thermal resistance, junction-case	$R_{thJC}$	-	0.7	1.2		
Thermal resistance, junction- ambient	$R_{thJA}$	_	_	62	K/W	leaded
Soldering temperature, wavesoldering only allowed at leads	$T_{sold}$	_	_	260	°C	1.6mm (0.063 in.) from case for 10 s



# 5th Generation thinQ!<sup>™</sup> SiC Schottky Diode IDH16G65C5

**Electrical characteristics** 

## 4 Electrical characteristics

Table 5 Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Condition
		Min.	Тур.	Max.		
DC blocking voltage	$V_{ m DC}$	650	_	_		$I_{R}$ = 0.2 mA, $T_{j}$ =25°C
Diode forward voltage	$V_{F}$	_	1.5	1.7	V	I <sub>F</sub> = 16 A, T <sub>j</sub> =25°C
		_	1.8	2.1		I <sub>F</sub> = 16 A, T <sub>j</sub> =150°C
Reverse current	I <sub>R</sub>	_	0.85	200		V <sub>R</sub> =650 V, T <sub>j</sub> =25°C
		_	0.2	71	μΑ	V <sub>R</sub> =600 V, T <sub>j</sub> =25°C
		_	3.2	1400		V <sub>R</sub> =650 V, T <sub>j</sub> =150°C

#### Table 6 AC characteristics

Parameter	Symbol	Values		Unit	Note/Test Condition	
		Min.	Тур.	Max.		
Total capacitive charge	Qc	_	23		nC	$V_R$ =400 V, <i>di/dt</i> =200A/µs, $I_F$ ≤ $I_{F,MAX}$ , $T_f$ =150°C
Total Capacitance	С	_	470	_		V <sub>R</sub> =1 V, <i>f</i> =1 MHz
		_	61	_	pF	V <sub>R</sub> =300 V, <i>f</i> =1 MHz
		_	60	_		V <sub>R</sub> =600 V, <i>f</i> =1 MHz



**Electrical characteristics diagrams** 

## 5 Electrical characteristics diagrams

#### Table 7

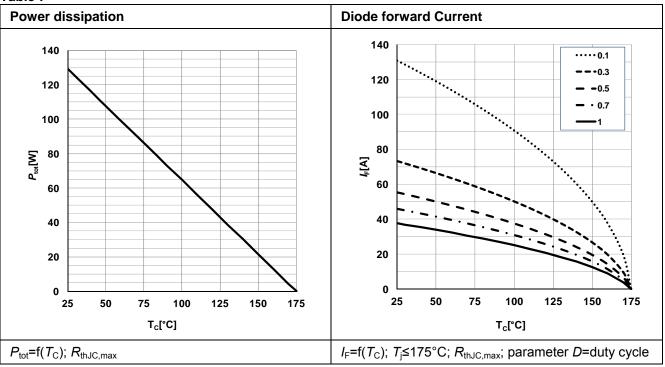
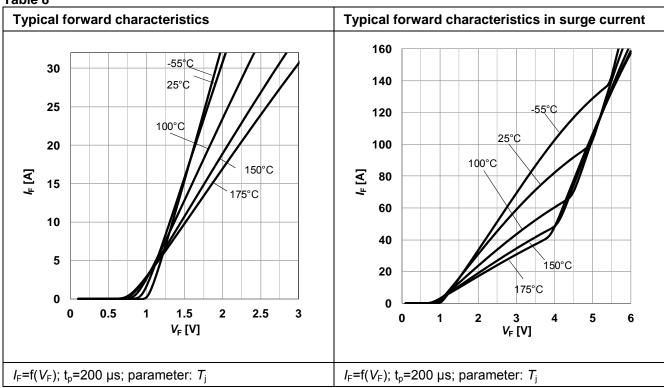


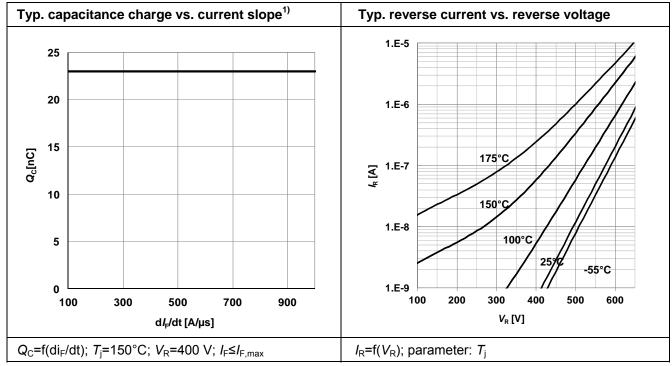
Table 8





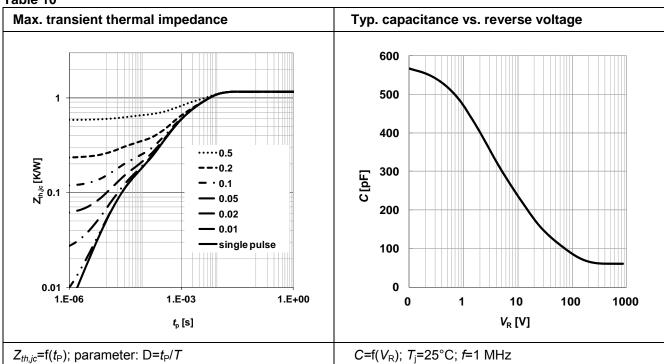
#### **Electrical characteristics diagrams**

Table 9



<sup>1)</sup> Only capacitive charge, guaranteed by design.

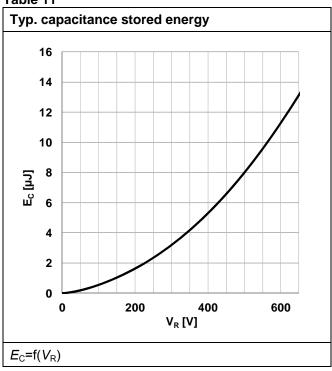
Table 10





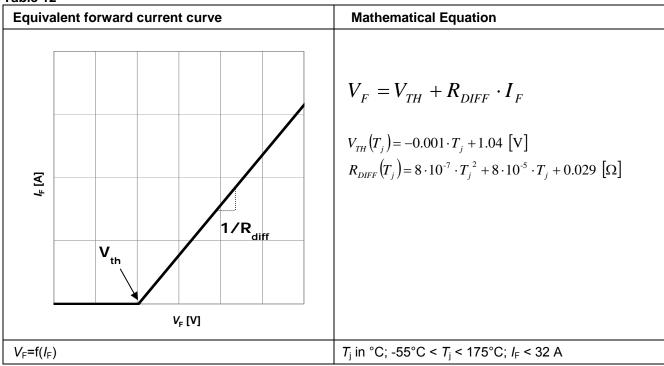
**Electrical characteristics diagrams** 

Table 11



## 6 Simplified Forward Characteristics Model

Table 12





Package outlines

## 7 Package outlines

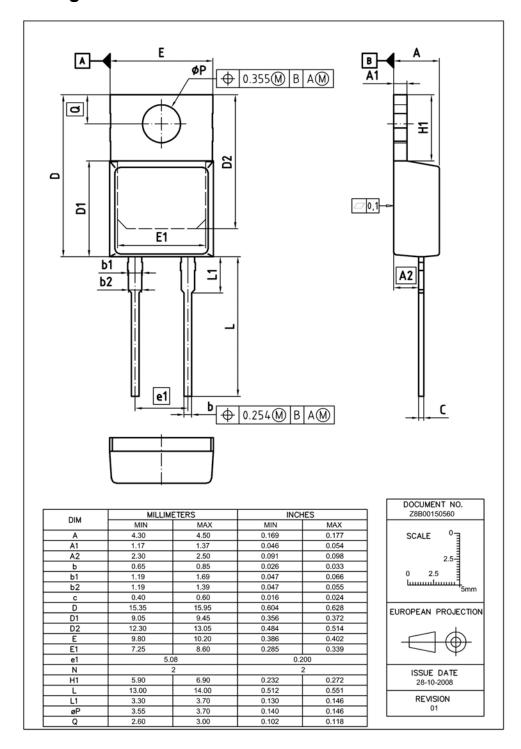


Figure 1 Outlines TO-220, dimensions in mm/inches

## 5<sup>th</sup> Generation thinQ!<sup>™</sup> SiC Schottky Diode IDH16G65C5

**Revision History** 

## 8 Revision History

5<sup>th</sup> Generation thinQ!<sup>™</sup> SiC Schottky Diode

Revision History: 2012-12-10, Rev. 2.2

Previous Revision:

Revision Subjects (major changes since last version)

2.0 Release of the final datasheet.

2.1 Reverse current values, maximum diode forward voltage.

2.2 Reverse current values, tested avalanche current, simplified calculation model

#### We Listen to Your Comments

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