ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Cathode to anode breakdown voltage	V_{BR}	I_R = 100 µA	1200	-	-	V
Maximum forward voltage	V_{FM}	I_F = 8.0 A	-	2.6	3.3	V
		I_F = 16 A	-	3.4	4.3	V
		I_F = 8.0 A, T_J = 125 °C	-	2.4	3.1	V
Maximum reverse leakage current	I_{RM}	V_R = V_R rated	-	0.31	10	µA
		T_J = 125 °C, V_R = 0.8 x V_R rated	-	135	1000	µA
Junction capacitance	C_T	V_R = 200 V	-	11	20	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNITS**
Reverse recovery time	t_{rr}	I_F = 1.0 A, dI_F/dt = 200 A/µs, V_R = 30 V		-	28	-	ns
	t_{rr1}	T_J = 25 °C	I_F = 8.0 A dI_F/dt = 200 A/µs V_R = 200 V	-	63	95	ns
	t_{rr2}	T_J = 125 °C		-	106	160	ns
Peak recovery current	I_{RRM1}	T_J = 25 °C		-	4.5	8.0	A
	I_{RRM2}	T_J = 125 °C		-	6.2	11	A
Reverse recovery charge	Q_{rr1}	T_J = 25 °C		-	140	380	nC
	Q_{rr2}	T_J = 125 °C		-	335	880	nC
Peak rate of recovery current during t_b	$dI_{(rec)M}/dt1$	T_J = 25 °C		-	133	-	A/µs
	$dI_{(rec)M}/dt2$	T_J = 125 °C		-	85	-	A/µs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Lead temperature	T_{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R_{thJC}		-	-	1.7	K/W
Thermal resistance, junction to ambient	R_{thJA}	Typical socket mount	-	-	40	K/W
Thermal resistance, case to heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.25	-	K/W
Weight			-	6.0	-	g
			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AC	HFA08TB120			

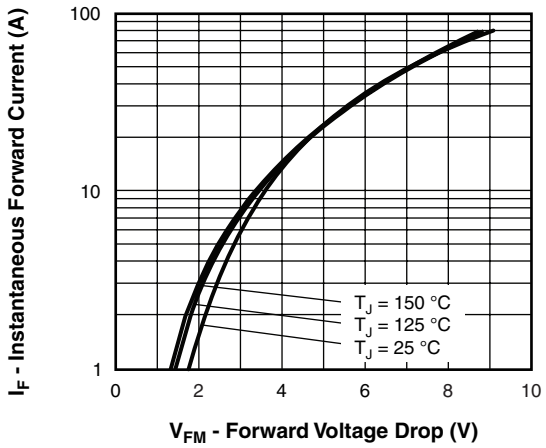


Fig. 1 - Maximum Forward Voltage Drop Characteristics

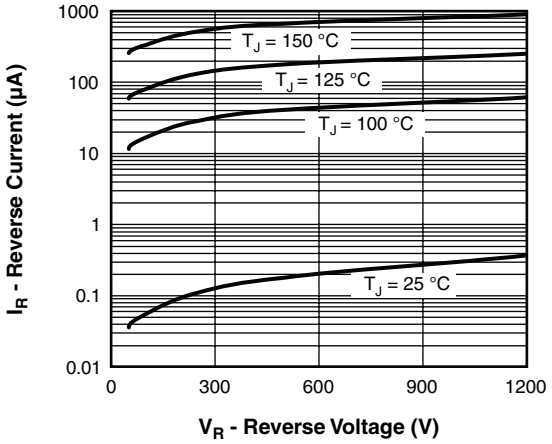


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

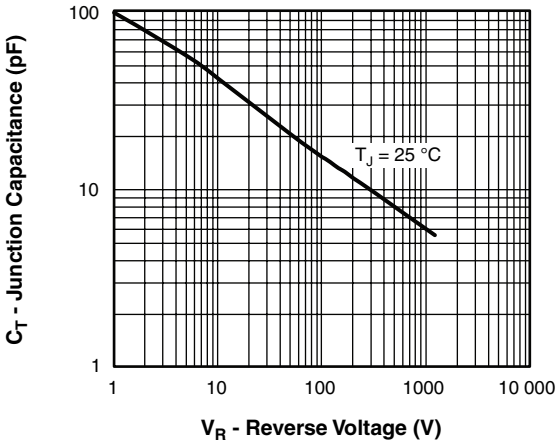


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

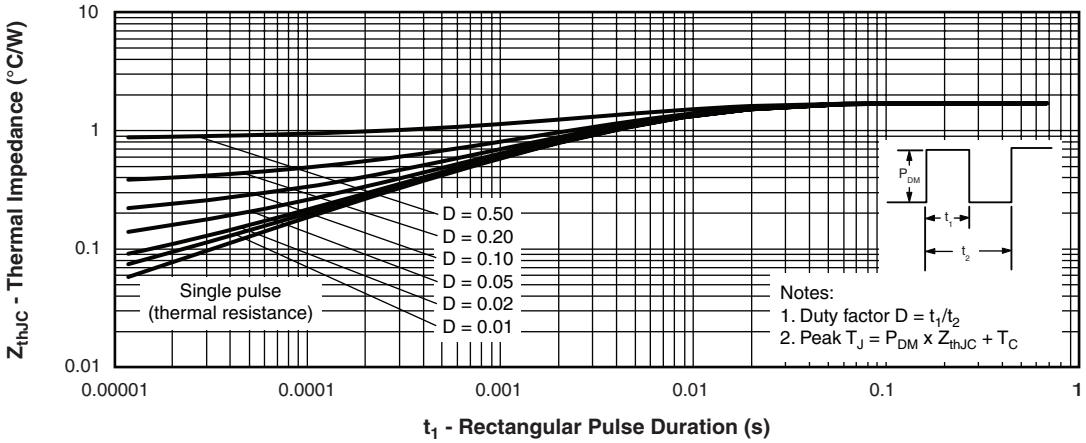


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

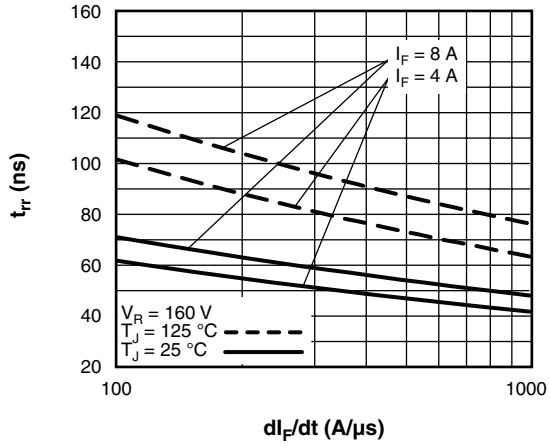


Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt

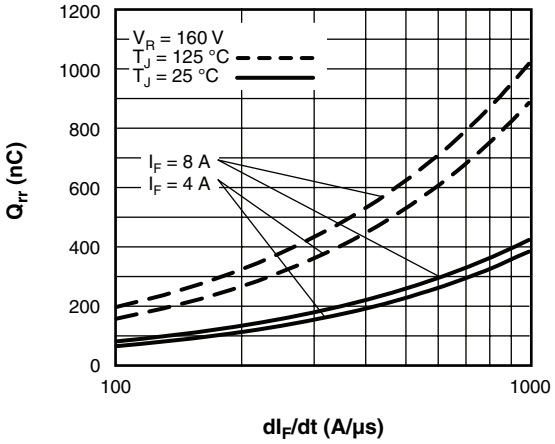


Fig. 7 - Typical Stored Charge vs. dI_F/dt

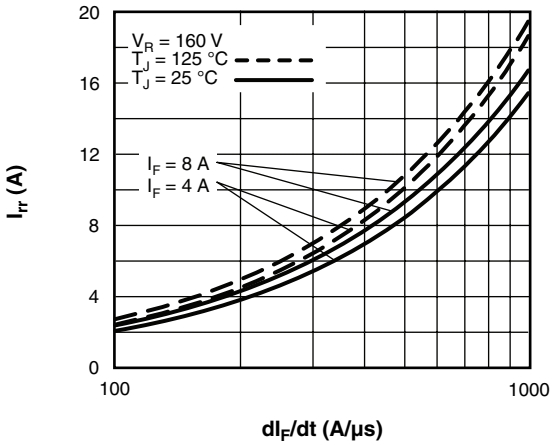


Fig. 6 - Typical Recovery Current vs. dI_F/dt

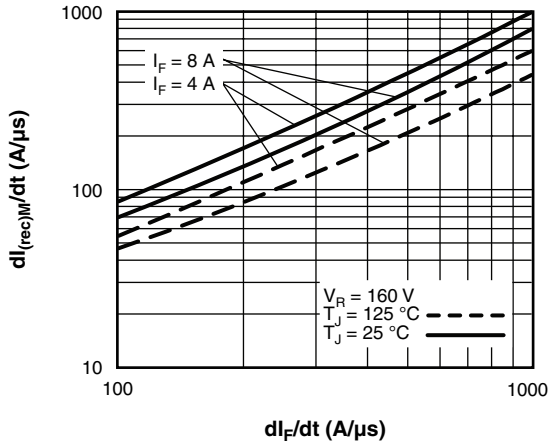


Fig. 8 - Typical $dI_{(rec)M}/dt$ vs. dI_F/dt

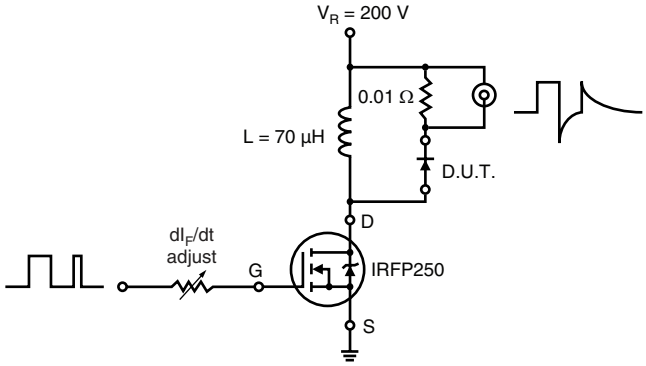
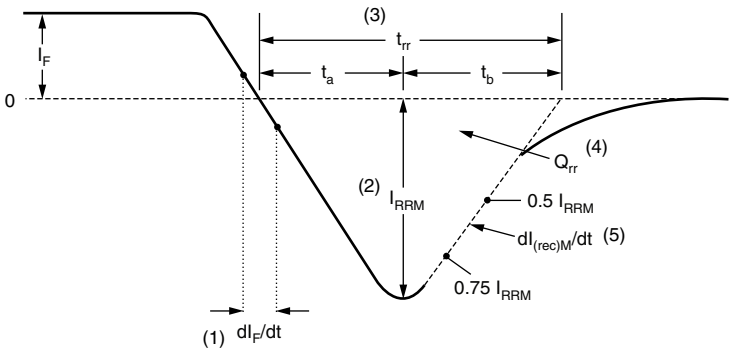


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dI_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

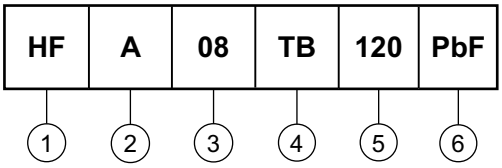
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



1 - HEXFRED® family

2 - Process designator A = subs. elec. irrad.
B = subs. platinum

3 - Current rating (08 = 8 A)

4 - Package outline (TB = TO-220, 2 leads)

5 - Voltage rating (120 = 1200 V)

6 - • None = Standard production
• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95221
Part marking information	http://www.vishay.com/doc?95224

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