

+1.2V, +1.8V and +2.5V CML logic by changing the voltage applied to the VCC_O pin. In general, DC-coupling to any termination voltage between +1.0V and +2.5V is supported.

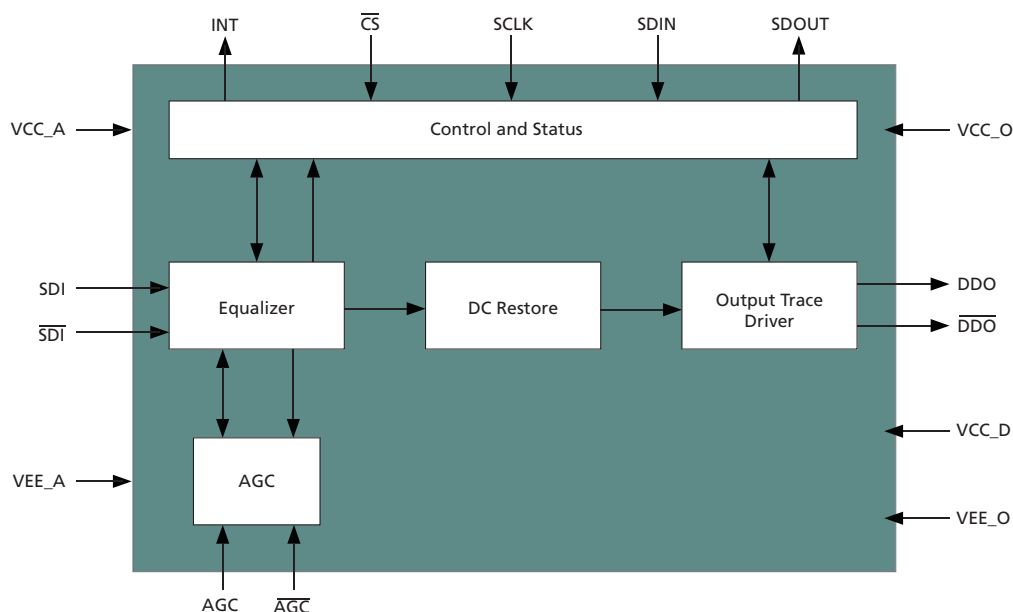
The GS6140 also features programmable output de-emphasis with eight user-selectable operating levels to support long PCB traces at the output of the device. The output swing can be programmed, via the user interface, from $250\text{mV}_{\text{ppd}}$ to 1V_{ppd} in 50mV steps.

The device comes in a 16-pin, 4mm x 4mm QFN-COL package.

Power consumption of the GS6140 is typically 83mW when DC-coupled to a +1.2V termination voltage.

The GS6140 is Pb-free, and the encapsulation compound does not contain halogenated flame retardant.

This component and all homogeneous subcomponents are RoHS compliant.



GS6140 Functional Block Diagram



**DOCUMENT IDENTIFICATION
PRODUCT BRIEF**

The product is in a development phase and specifications are subject to change without notice. Semtech reserves the right to remove the product at any time. Listing the product does not constitute an offer for sale.

CAUTION

ELECTROSTATIC SENSITIVE DEVICES

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