

Absolute Maximum Ratings (Ta=25°C)

	Parameter		Symbol	Rating	Unit
Input	Forward current		I _F	60	mA
	Reverse voltage		V_{R}	6	V
	Power dissipation		5	100	mW
	Derating factor (above	$T_a = 85^{\circ}C$)	P _D -	3.8	mW /°C
Output		EL303X		250	
	Off-state Output Terminal Voltage	EL304X	N/	400	_
		EL306X	– V _{DRM}	600	- V
		EL308X		800	
	Peak Repetitive Surge (pw=1ms,120pps)	Current	Ітѕм	1	А
	On-State RMS Current		I _{T(RMS)}	100	mA
	Power dissipation		D	300	mW
	Derating factor (above T _a = 85°C)		Pc -	7.6	mW/°C
Total pow	ver dissipation		P _{TOT}	330	mW
Isolation voltage *1			V _{ISO}	5000	Vrms
Operating temperature			T _{OPR}	-55 to 100	°C
Storage temperature			T _{STG}	STG -55 to 125	
Soldering	g Temperature* ²		T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= $40 \sim 60\%$ R.H. In this test, pins 1, 2& 3 are shorted together, and pins 4, 5 & 6 are shorted together.

^{*2} For 10 seconds



Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Typ.*1	Max.	Unit	Condition
Forward Voltage	VF	-	-	1.5	V	$I_F = 30 \text{mA}$
Reverse Leakage current	I_R	-	-	10	μΑ	$V_R = 6V$

Output

Parameter		Symbol	Min.	Тур.*	Max.	Unit	Condition	
Peak Blocking	EL303X EL304X	_			100	A	V _{DRM} = Rated V _{DRM}	
Current	EL306X EL308X	- I _{DRM1}	-	-	nA 500	I _F = 0 mA* ²		
Peak On-state Voltage		V_{TM}	-	-	3	V	I _{тм} =100 mA peak, I _F =Rated I _{FT}	
Critical Rate of Rise off-state	EL303X EL304X EL306X	dv/dt	1000	-	-	V/µs	V _{PEAK} =Rated V _{DRM} , I _F =0	
Voltage	EL308X		600	-			(Fig. 10)*3	
Inhibit Voltage (MT1-MT2 voltage above which device will not trigger)		Vinh	3		20	V	I _F = Rated I _{FT}	
Leakage in Inhibited State		I _{DRM2}	-	-	500	μΑ	eq:lemma	

Notes:

^{*1.}Typical values at T_a = 25°C

^{*2.} Test voltage must be applied within dv/dt rating.

^{*3.} This is static dv/dt. See Figure 10 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.



Transfer Characteristics

Parameter		Symbol	Min.	Тур.*	Max.	Unit	Condition
	EL3031 EL3041 EL3061 EL3081		-	-	15		
LED Trigger Current	EL3032 EL3042 EL3062 EL3082	l _{FT}	-	-	10	mA -	Main terminal Voltage=3V*4
	EL3033 EL3043 EL3063 EL3083		-	-	5		
Holding Currer	Holding Current		-	280	-	μΑ	

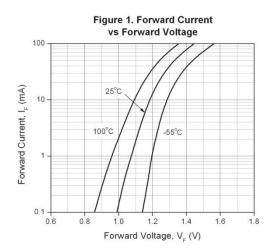
Notes:

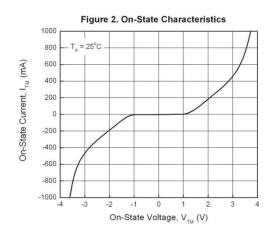
^{*4.} All devices are guaranteed to trigger at an I_F value less than or equal to max I_{FT}. Therefore, recommended operating I_F lies between max I_{FT} (15 mA for EL3031/EL3041/EL3061/EL3081,10 mA for EL3032/EL3042/EL3062/EL3082, 5 mA for EL3033/EL3043/EL3063/EL3083) and absolute maximum I_F (60 mA).

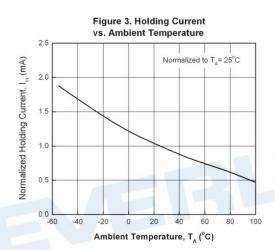


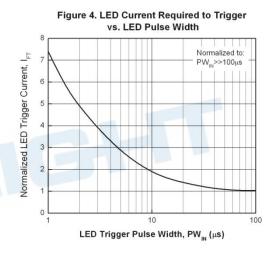


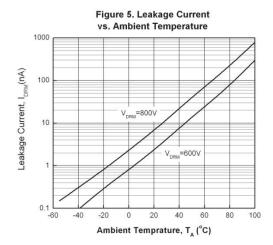
Typical Electro-Optical Characteristics Curves











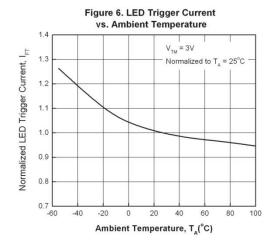
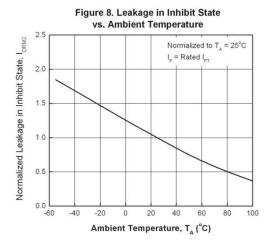


Figure 7. Off-State Output Terminal Voltage vs. Ambient Temperature Normalized to $T_A = 25^{\circ}C$ 1.3 Output Terminal Voltage, VDRM Normalized Off-State 1.1 1.0 0.9 0.7 L -60 -40 20 60 80 100 Ambient Temperature, T (°C)



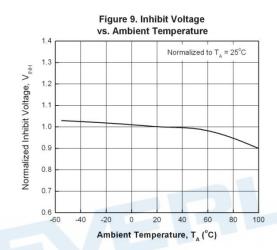
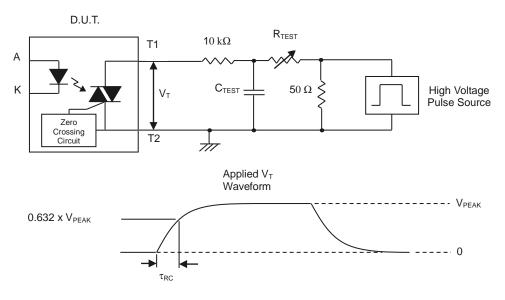




Figure 10. Static dv/dt Test Circuit & Waveform



Measurement Method

The high voltage pulse is set to the required V_{PEAK} value and applied to the D.U.T. output side through the RC circuit above. LED current is not applied. The waveform V_T is monitored using a x100 scope probe. By varying R_{TEST} , the dv/dt (slope) is increased, until the D.U.T. is observed to trigger (waveform collapses). The dv/dt is then decreased until the D.U.T. stops triggering. At this point, τ_{RC} is recorded and the dv/dt calculated.

$$dv/dt = \frac{0.632 \text{ x V}_{PEAK}}{\tau_{RC}}$$

For example, V_{PEAK} = 600V for EL306X series. The dv/dt value is calculated as follows:

$$dv/dt = \frac{0.632 \times 600}{\tau_{RC}} = \frac{379.2}{\tau_{RC}}$$



Order Information

Part Number

EL303XY(Z)-V or EL304XY(Z)-V or EL306XY(Z)-V

Note

X = Part No. (1, 2 or 3)

Y = Lead form option (S, S1, M or none)

Z = Tape and reel option (TA, TB or none)

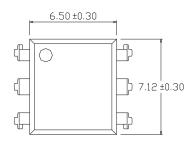
V = VDE safety approved option

Option	Description	Packing quantity
None	Standard DIP-6	65 units per tube
М	Wide lead bend (0.4 inch spacing)	65 units per tube
S (TA)	Surface mount lead form + TA tape & reel option	1000 units per reel
S (TB)	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel

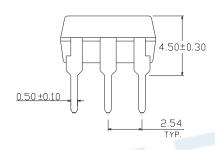


Package Dimension (Dimensions in mm)

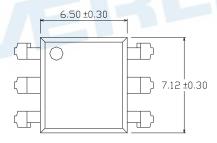
Standard DIP Type

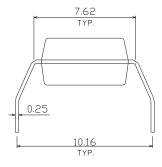


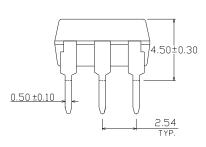




Option M Type

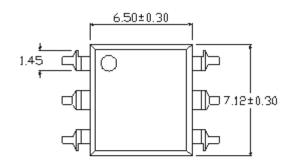


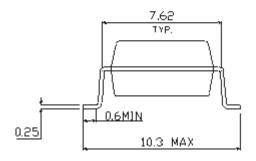


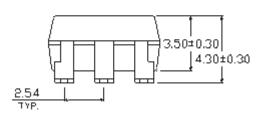




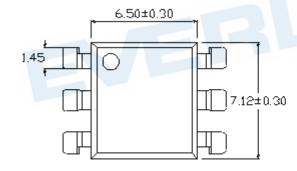
Option S Type

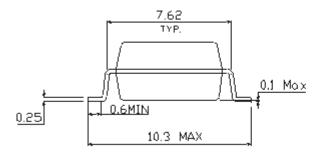


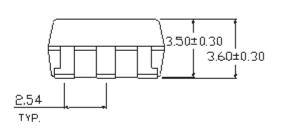




Option S1 Type

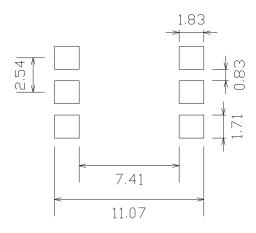








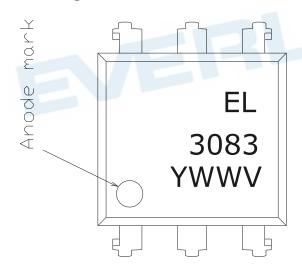
Recommended pad layout for surface mount leadform



Notes

Suggested pad dimension is just for reference only. Please modify the pad dimension based on individual need.

Device Marking



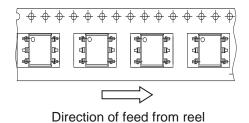
Notes

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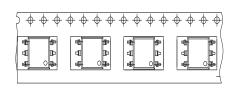


Tape & Reel Packing Specifications

Option TA

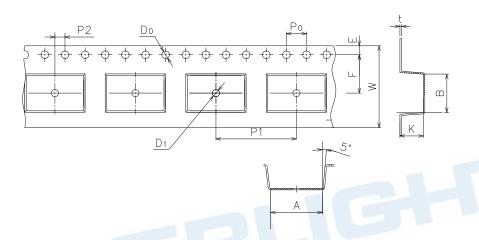


Option TB



Direction of feed from reel

Tape dimensions



Dimension No.	Α	В	Do	D1	E	F
Dimension (mm)	10.8±0.1	7.55±0.1	1.5±0.1	1.5±0.1	1.75±0.1	7.5±0.1

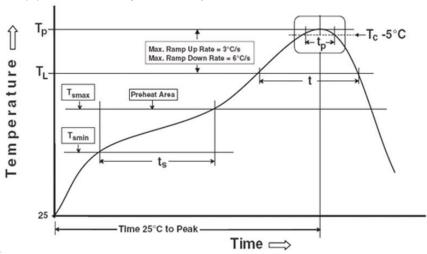
Dimension No.	Ро	P1	P2	t	w	K
Dimension (mm)	4.0±0.15	12±0.1	2.0±0.1	0.35±0.03	16.0±0.2	4.5±0.1



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin})

Temperature max (T_{smax})

Time $(T_{smin} \text{ to } T_{smax})$ (t_s)

Average ramp-up rate (T_{smax} to T_p)

Other

Liquidus Temperature (TL)

Time above Liquidus Temperature (t L)

Peak Temperature (T_P)

Time within 5 °C of Actual Peak Temperature: TP - 5°C

Ramp- Down Rate from Peak Temperature

Time 25°C to peak temperature

Reflow times

Reference: IPC/JEDEC J-STD-020D

150 °C

200°C

60-120 seconds

3 °C/second max

217 °C

60-100 sec

260°C

30 s

6°C /second max.

8 minutes max.

3 times

DATASHEET 6 PIN DIP ZERO-CROSS TRIAC DRIVER PHOTOCOUPLER EL303X, EL304X, EL306X, EL308X Series



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