

EFC4626R

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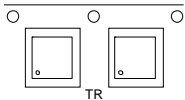
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Static Source to Source On-State Resistance	$R_{SS(on)1}$	$I_S=2A, V_{GS}=4.5V$ Test Circuit 5	29.2	37.5	46.2	$m\Omega$
	$R_{SS(on)2}$	$I_S=2A, V_{GS}=4.0V$ Test Circuit 5	30.8	39.5	48.6	$m\Omega$
	$R_{SS(on)3}$	$I_S=2A, V_{GS}=3.8V$ Test Circuit 5	32.0	41.0	50.5	$m\Omega$
	$R_{SS(on)4}$	$I_S=2A, V_{GS}=3.1V$ Test Circuit 5	35.5	45.5	58.3	$m\Omega$
	$R_{SS(on)5}$	$I_S=2A, V_{GS}=2.5V$ Test Circuit 5	42.6	54.0	72.4	$m\Omega$
Turn-ON Delay Time	$t_d(on)$	$V_{SS}=10V, V_{GS}=4.5V, I_S=2A$ Test Circuit 6		20		ns
Rise Time	t_r			350		ns
Turn-OFF Delay Time	$t_d(off)$			22000		ns
Fall Time	t_f			38400		ns
Total Gate Charge	Q_g	$V_{SS}=10V, V_{GS}=4.5V, I_S=5A$ Test Circuit 7		7.5		nC
Forward Source to Source Voltage	$V_{F(S-S)}$	$I_S=2A, V_{GS}=0V$ Test Circuit 8		0.81	1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

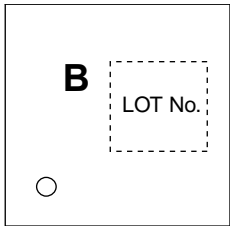
Ordering & Package Information

Device	Package	Shipping	note
EFC4626R-TR	EFCP	8,000 pcs. / reel	Pb-Free and Halogen Free

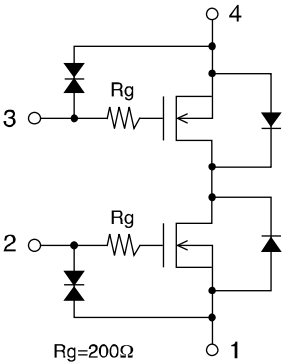
Packing Type: TR



Marking

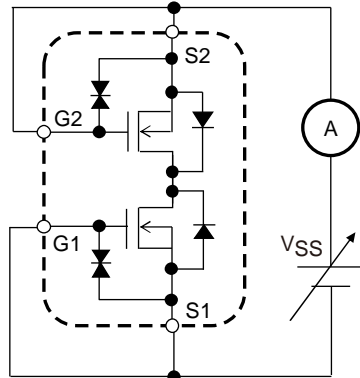


Electrical Connection

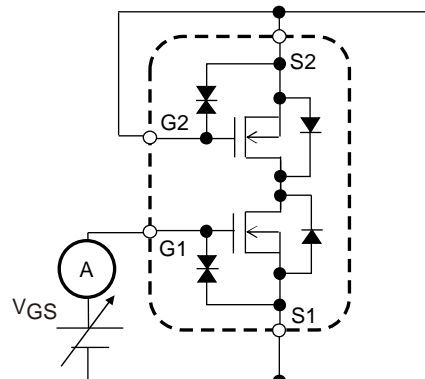


Test circuits are example of measuring FET1 side

Test Circuit 1
 I_{SSS}

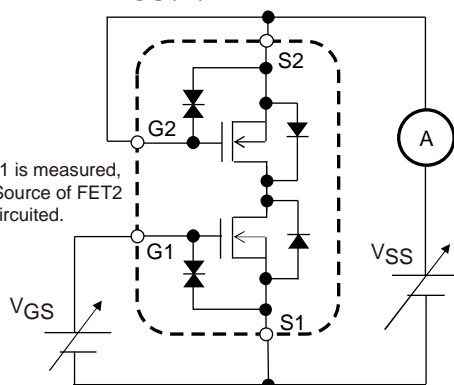


Test Circuit 2
 I_{GSS}



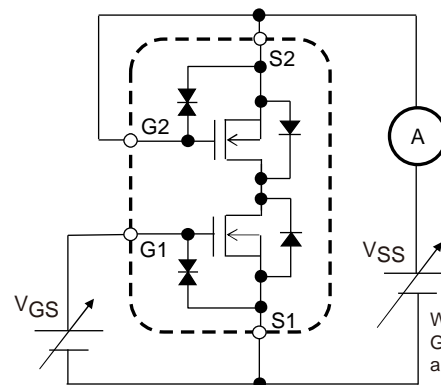
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 3
 $V_{GS(th)}$



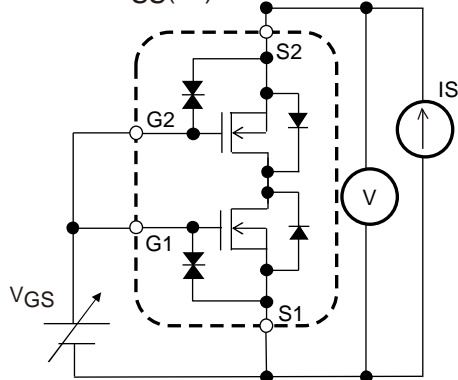
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 4
 g_{FS}

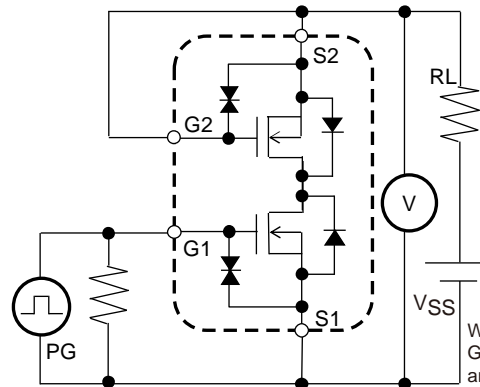


When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 5
 $R_{SS(on)}$

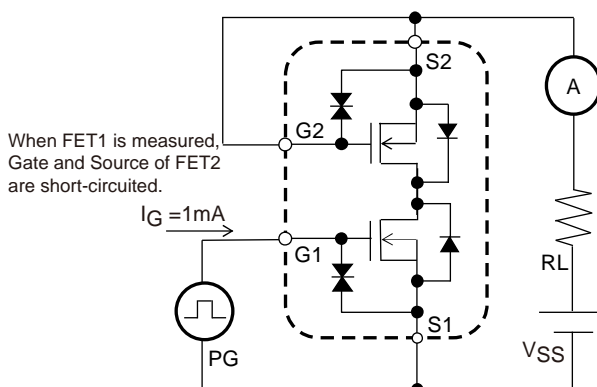


Test Circuit 6
 $t_{d(on)}$, t_r , $t_{d(off)}$, t_f



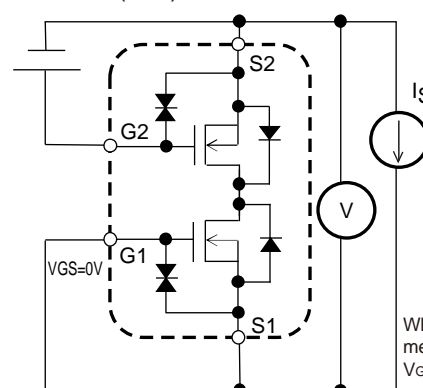
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 7
 Q_g



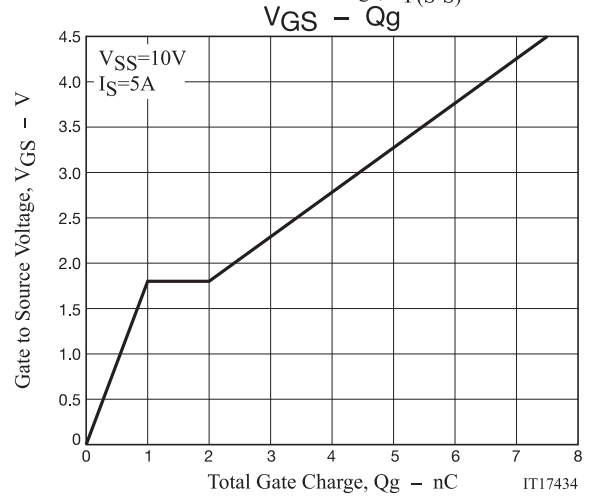
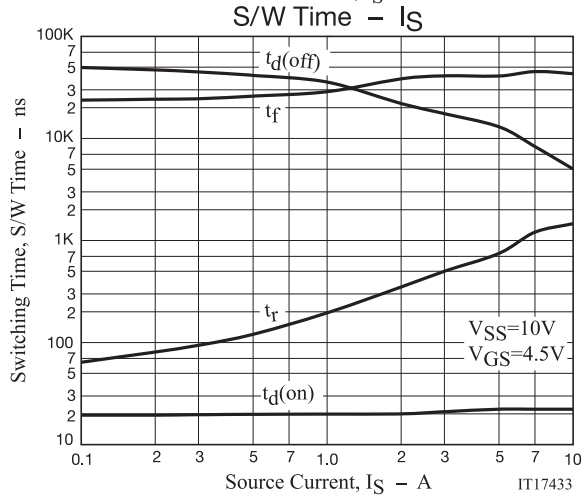
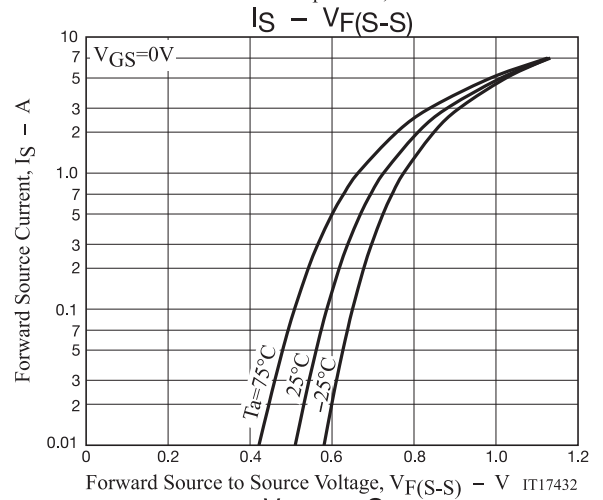
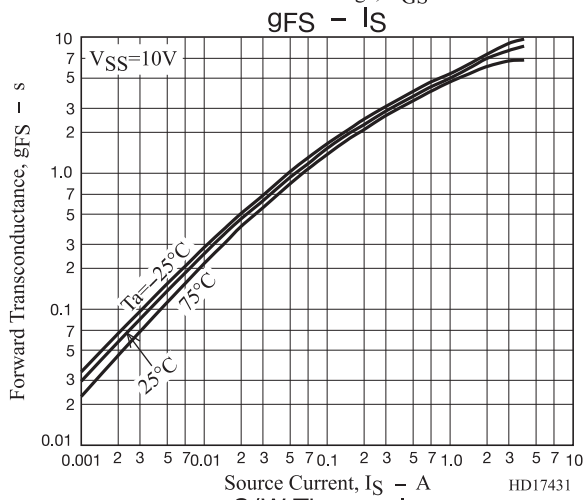
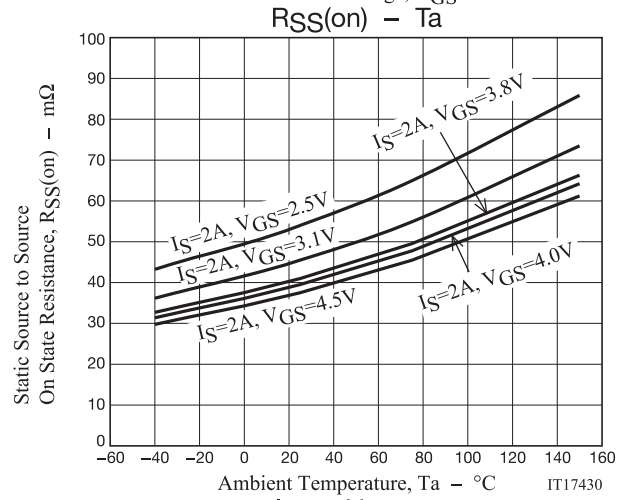
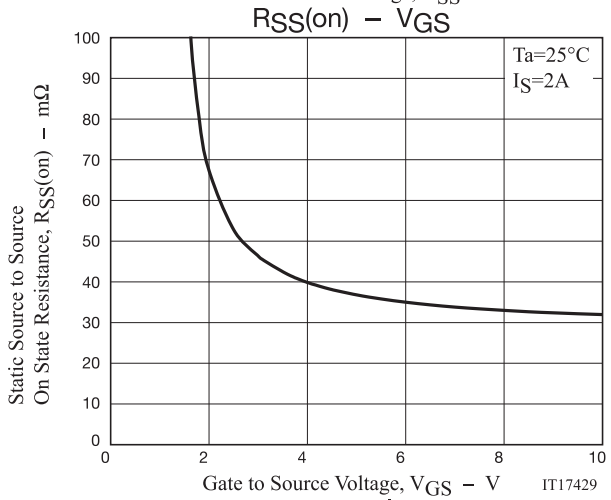
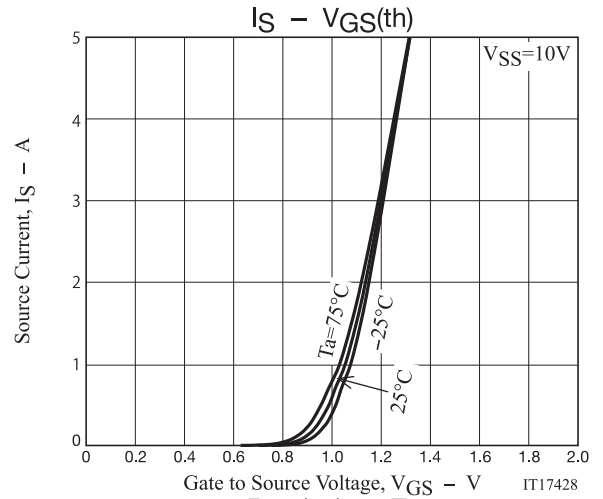
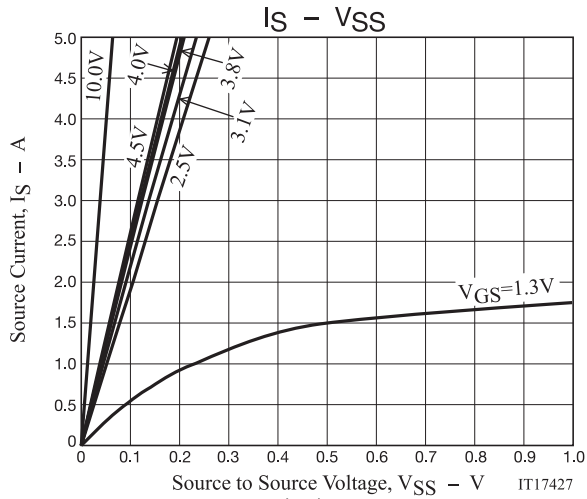
When FET1 is measured, Gate and Source of FET2 are short-circuited.

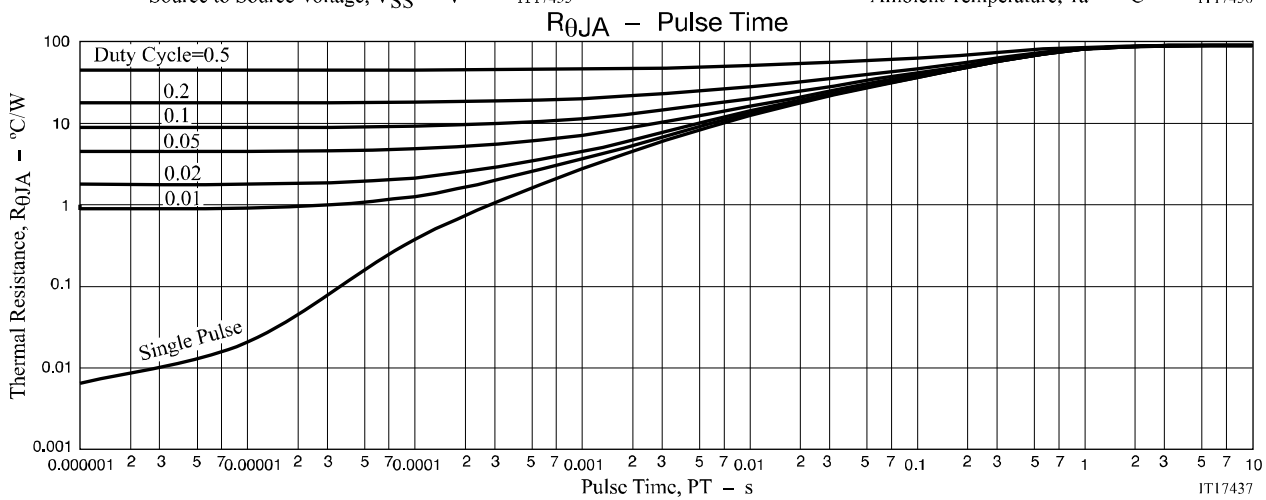
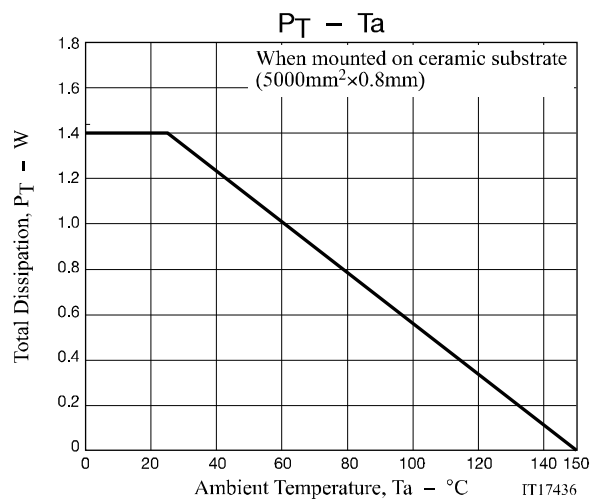
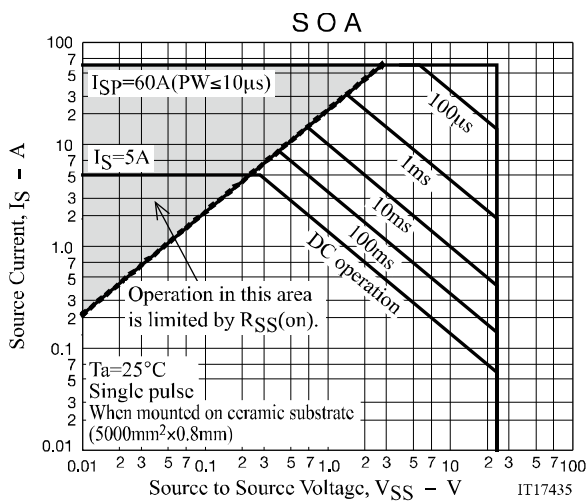
Test Circuit 8
 $V_{F(S-S)}$



When FET1 is measured, +4.5V is added to V_{GS} of FET2.

When FET2 is measured, the position of FET1 and FET2 is switched.





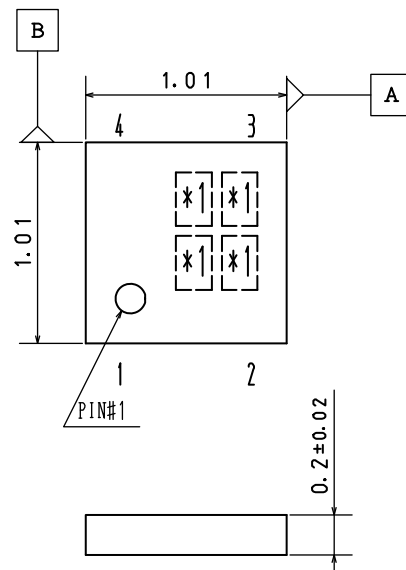
Package Dimensions

EFC4626R-TR

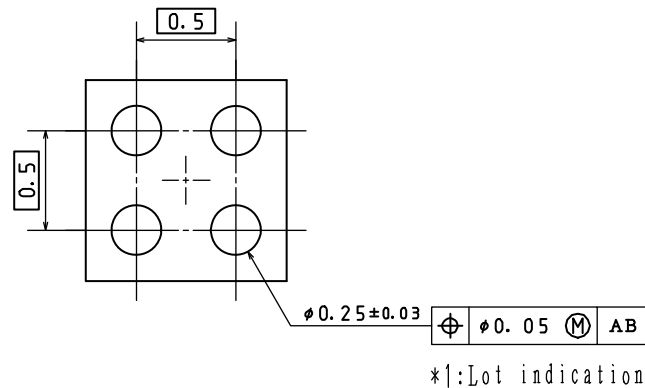
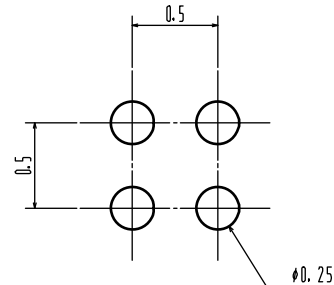
EFCP1010-4DG-020

unit : mm

- 1: Source1
- 2: Gate1
- 3: Gate2
- 4: Source2



Recommended Soldering Footprint



Note on usage : Since the EFC4626R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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