Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current, V_{GS} = 10V (Note 6)	T_C = +25°C T_C = +100°C	I_D	205 145	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	820	A
Continuous Body Diode Forward Current (Note 6)	T_C = +25°C	I_S	205	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	820	A
Avalanche Current, L = 3mH		I_{AS}	14	A
Avalanche Energy, L = 3mH		E_{AS}	294	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	3	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	50	°C/W
Total Power Dissipation (Note 6)	T_C = +25°C	P_D	167	W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	0.9	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	µA	V_{DS} = 48V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	—	3	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	1.7	2	mΩ	V_{GS} = 10V, I_D = 30A
		—	2	3		V_{GS} = 6V, I_D = 30A
		—	2.3	3.3		V_{GS} = 4.5V, I_D = 30A
Diode Forward Voltage	V_{SD}	—	—	1.2	V	V_{GS} = 0V, I_S = 50A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	6555	—	pF	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz
Output Capacitance	C_{oss}	—	2264	—		
Reverse Transfer Capacitance	C_{rss}	—	187	—		
Gate Resistance	R_g	—	0.7	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 10V)	Q_g	—	130.8	—	nC	V_{DS} = 30V, I_D = 50A
Total Gate Charge (V_{GS} = 4.5V)	Q_g	—	63.6	—		
Gate-Source Charge	Q_{gs}	—	20.8	—		
Gate-Drain Charge	Q_{gd}	—	29.4	—		
Turn-On Delay Time	$t_{D(ON)}$	—	11.2	—	ns	V_{DD} = 20V, V_{GS} = 10V, I_D = 50A, R_g = 2.5Ω
Turn-On Rise Time	t_R	—	10.8	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	44	—		
Turn-Off Fall Time	t_F	—	19.5	—		
Reverse Recovery Time	t_{RR}	—	61.8	—	ns	I_F = 50A, di/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	123	—	nC	

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
6. Thermal resistance from junction to soldering point (on the exposed drain pad).
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

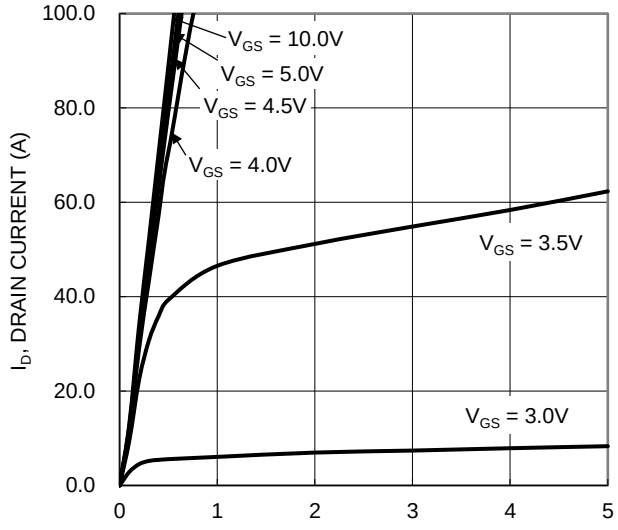


Figure 1. Typical Output Characteristic

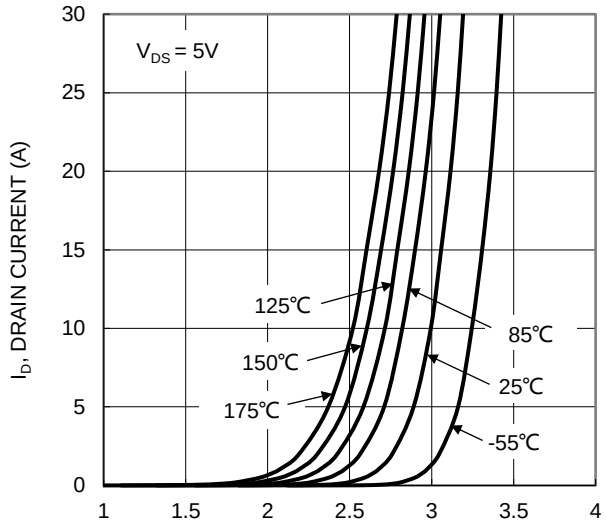


Figure 2. Typical Transfer Characteristic

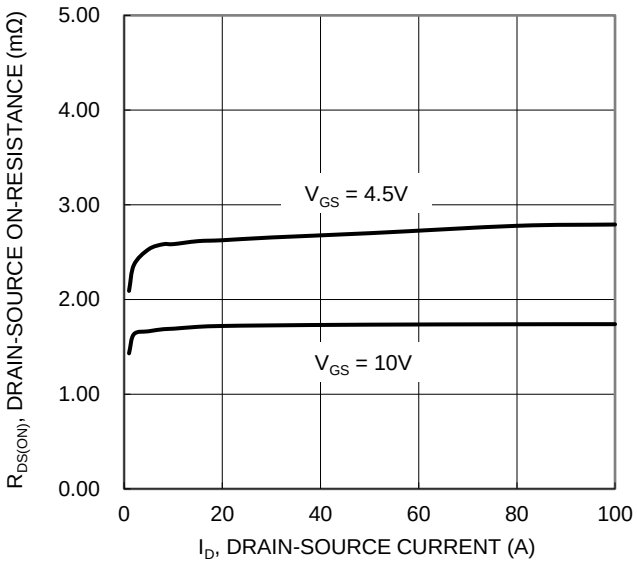


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

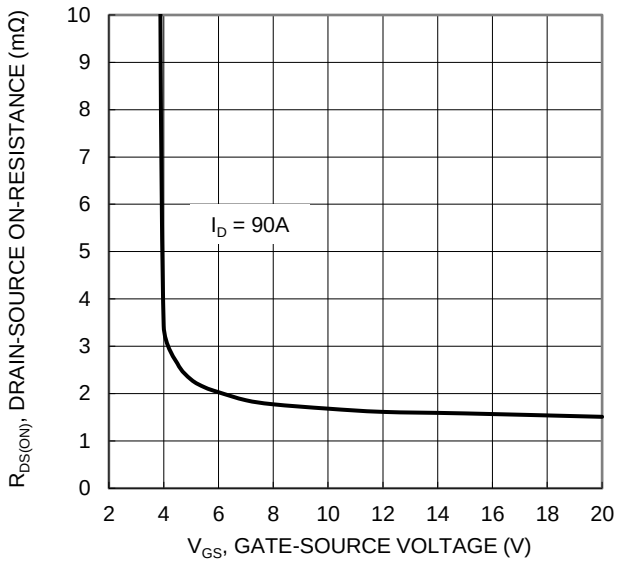


Figure 4. Typical Transfer Characteristic

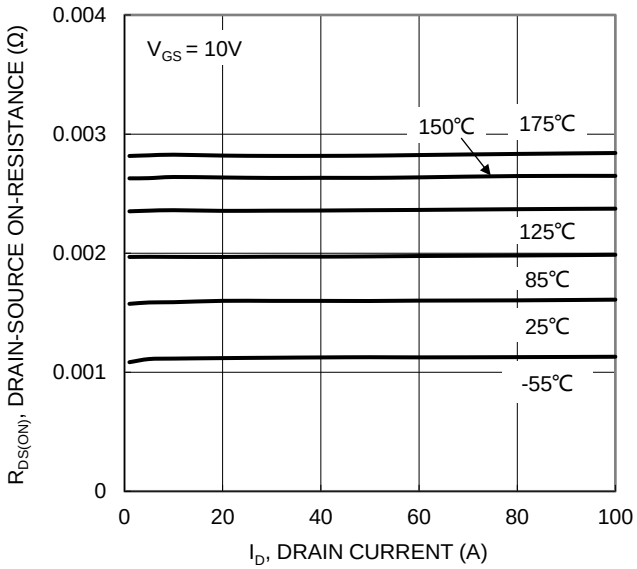


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

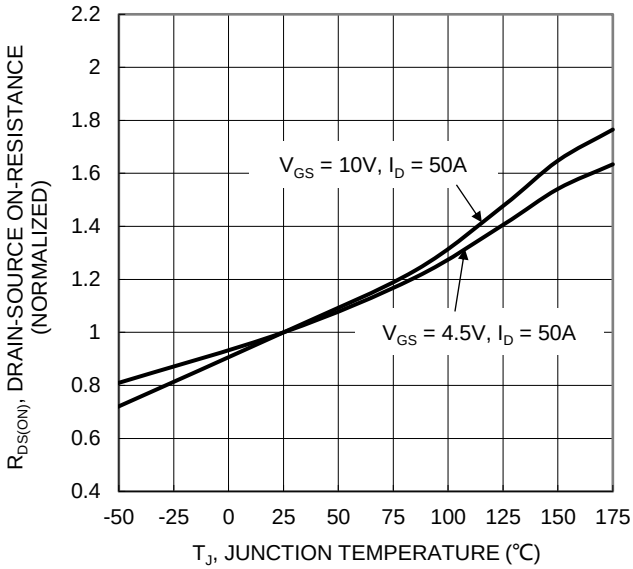


Figure 6. On-Resistance Variation with Temperature

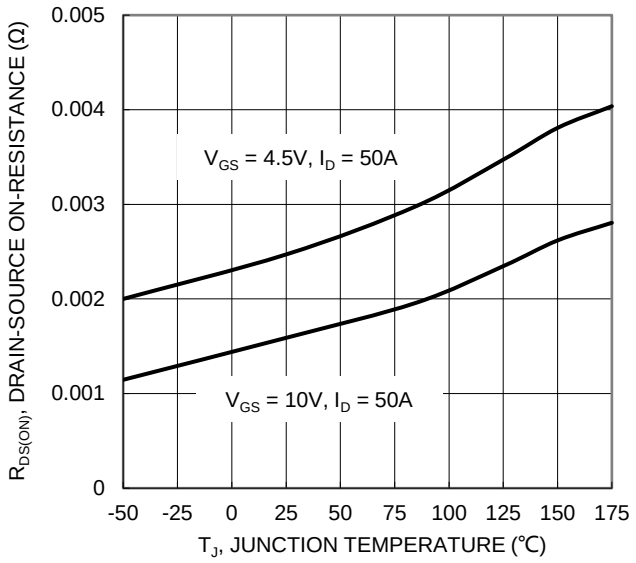


Figure 7. On-Resistance Variation with Temperature

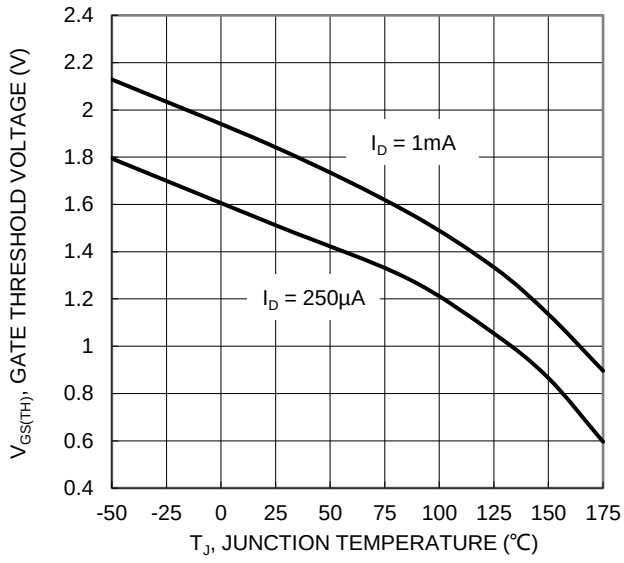


Figure 8. Gate Threshold Variation vs. JunctionTemperature

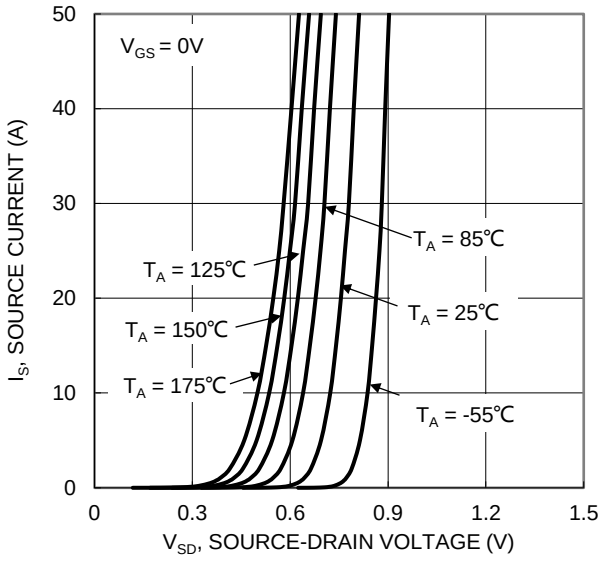


Figure 9. Diode Forward Voltage vs. Current

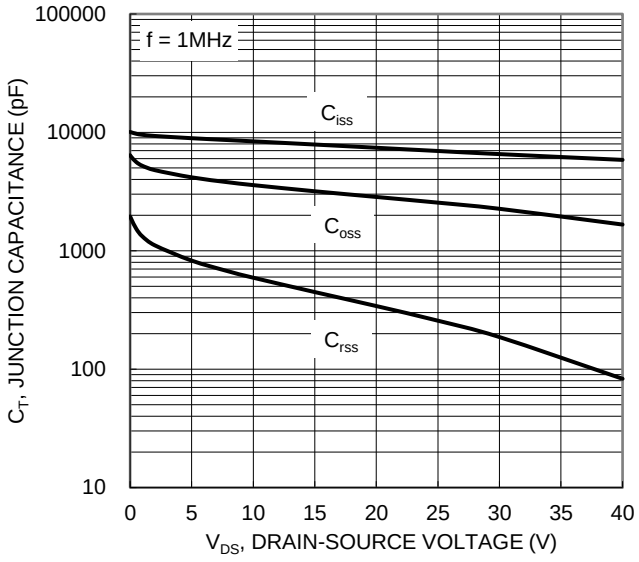


Figure 10. Typical Junction Capacitance

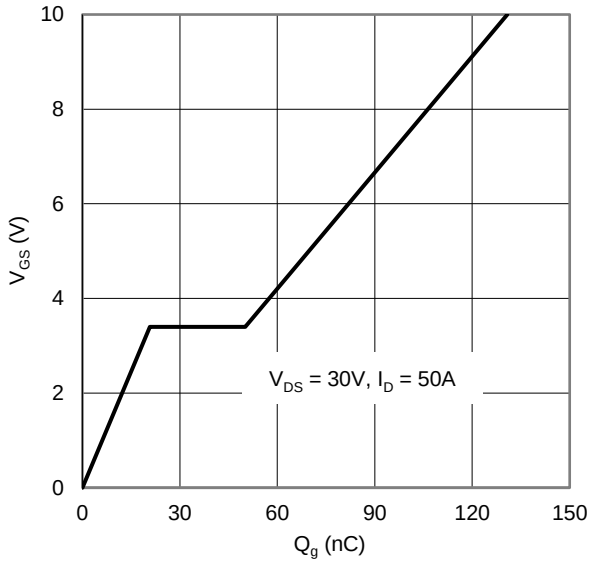


Figure 11. Gate Charge

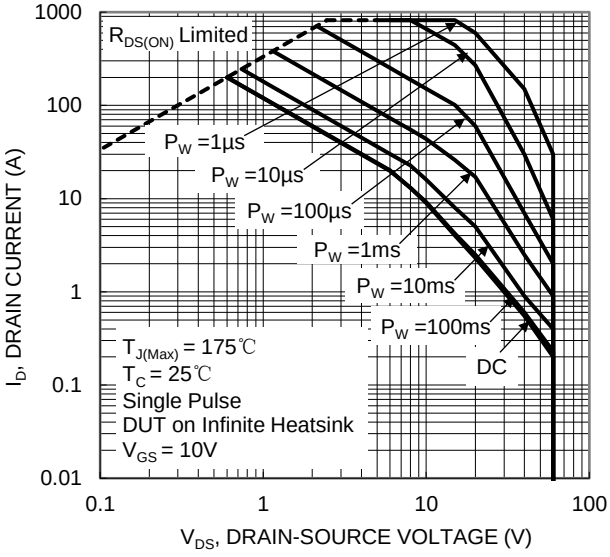


Figure 12. SOA, Safe Operation Area

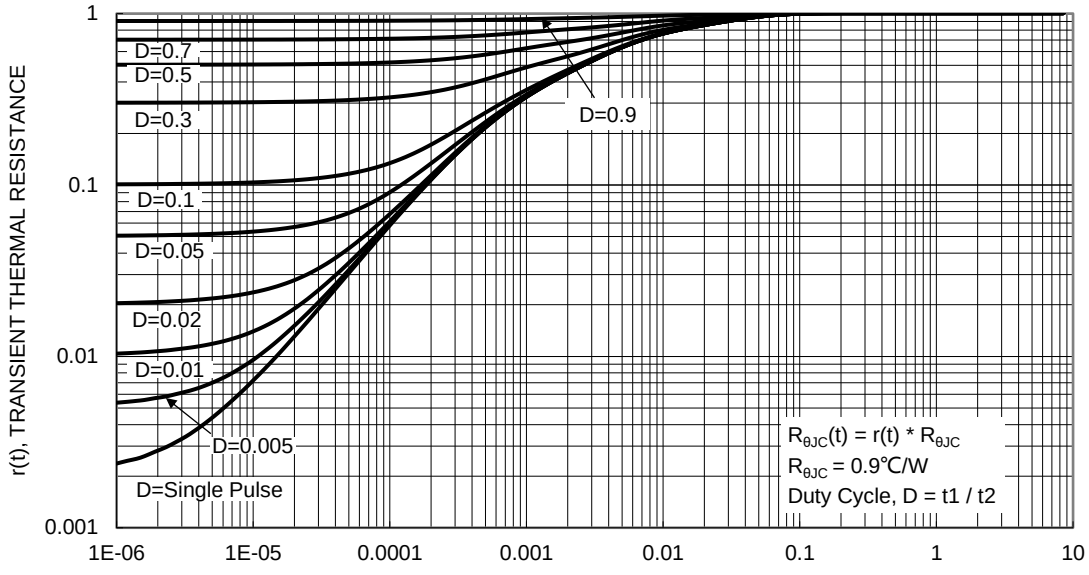
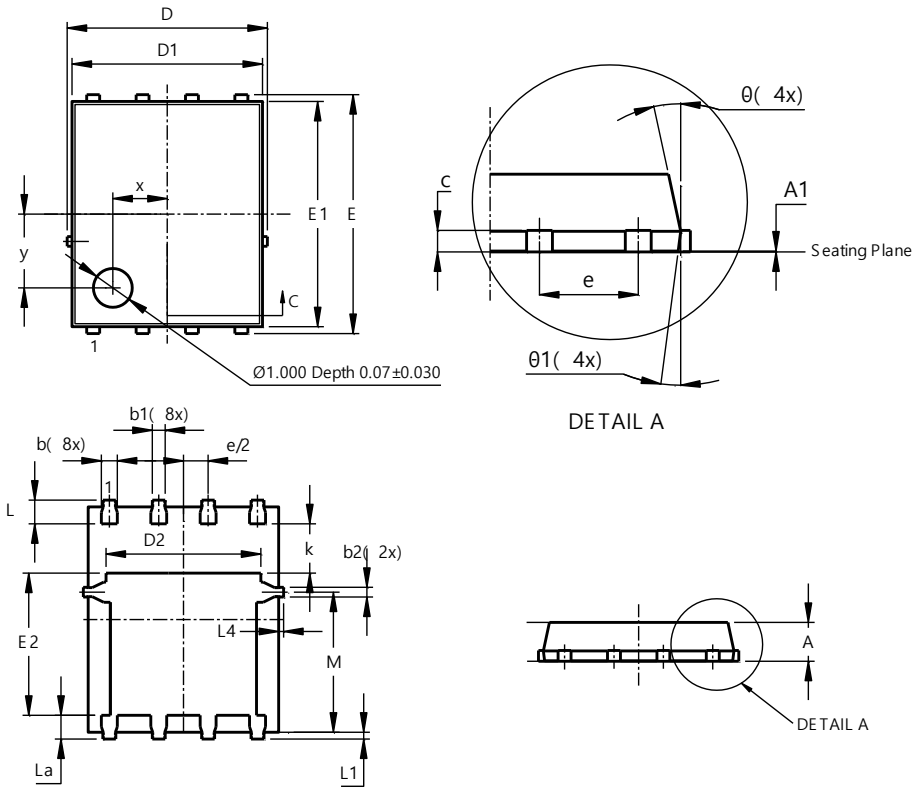


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)

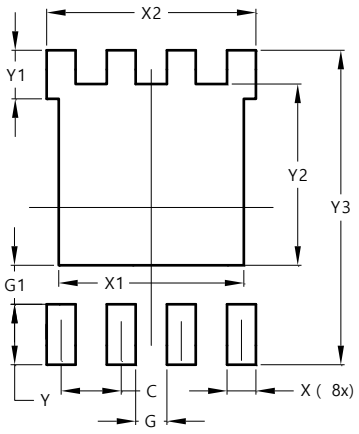


PowerDI5060-8 (Type K)			
Dim	**Min**	**Max**	**Typ**
A	0.90	1.10	1.00
A1	0	0.05	0.02
b	0.33	0.51	0.41
b1	0.300	0.366	0.333
b2	0.20	0.35	0.25
c	0.23	0.33	0.277
D	5.15 BSC		
D1	4.85	4.95	4.90
D2	-	-	3.98
E	6.15 BSC		
E1	5.75	5.85	5.80
E2	3.56	3.725	3.66
e	1.27BSC		
k	-	-	1.27
L	0.51	0.71	0.61
La	0.51	0.675	0.61
L1	0.05	0.20	0.175
L4	-	-	0.125
M	3.50	3.71	3.605
x	-	-	1.400
y	-	-	1.900
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	3.910
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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