

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	60	V	
Gate-Source Voltage	V _{GSS}	±16	V	
Continuous Drain Current (Note 6) // 10\/	$T_A = +25$ °C $T_A = +70$ °C	I _D	10.6 8.5	А
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_C = +25$ °C $T_C = +70$ °C	I _D	31 25	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	60	А	
Maximum Continuous Body Diode Forward Current (Note 6)		Is	2	Α
Avalanche Current (Note 7) L=0.1mH	I _{AS}	10	Α	
Avalanche Energy (Note 7) L=0.1mH		E _{AS}	5	mJ
V _{DS} Spike t=10μs		V _{SPIKE}	75	V

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	1.16	W
The word Desistance I have time to Ambient (Nets 5)	Steady state	-	108	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	56	
Total Power Dissipation (Note 6)		PD	2.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	-	46	°C/W
	t<10s	$R_{\theta JA}$	24	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	4.4	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

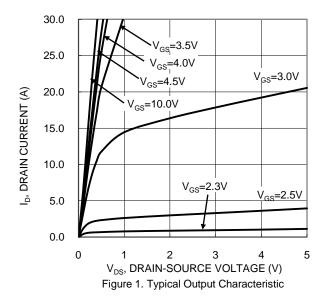
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	0.5	1	2.5	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	14.2	16	mΩ	$V_{GS} = 10V, I_D = 10A$	
	R _{DS(ON)}	_	18	24		$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	1	1103	_		$V_{DS} = 30V, V_{GS} = 0V,$ $f = 1MHz$	
Output Capacitance	Coss	_	251.3	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	19.7	_		1 = 1101112	
Gate Resistance	Rg	_	1.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	8.9	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	18.9	_		$V_{DS} = 30V, I_{D} = 10A$	
Gate-Source Charge	Qgs	_	3.0	_	nC		
Gate-Drain Charge	Q _{qd}	_	2.8	_			
Turn-On Delay Time	t _{D(ON)}	_	4.1	_		$V_{GS} = 10V, V_{DS} = 30V,$ $R_G = 6\Omega, I_D = 10A$	
Turn-On Rise Time	t _R	_	7.1	_			
Turn-Off Delay Time	t _{D(OFF)}	_	19.5	_	ns		
Turn-Off Fall Time	t _F	_	8.6	_			
Reverse Recovery Time	t _{RR}		21.2	_	ns	100 11/11 1000/	
Reverse Recovery Charge	Q _{RR}	_	13.2	_	nC	$I_F = 10A$, di/dt = 100A/ μ s	

Notes:

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.





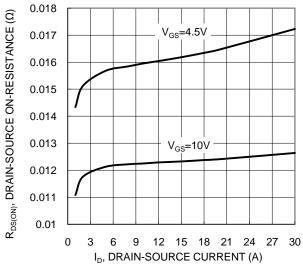


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

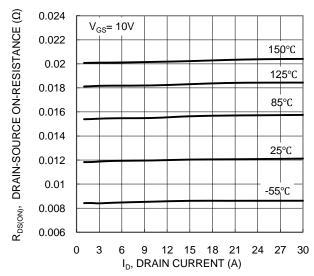
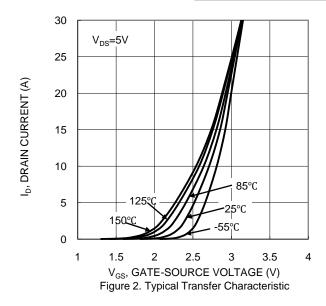
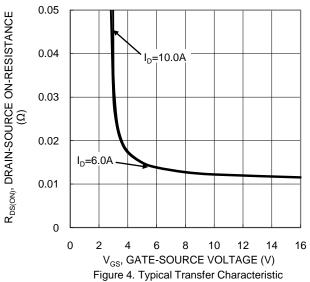


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





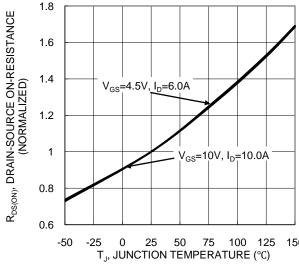


Figure 6. On-Resistance Variation with Temperature



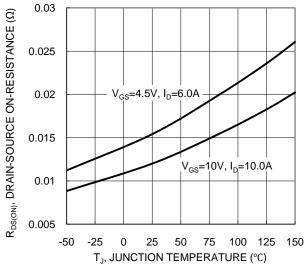
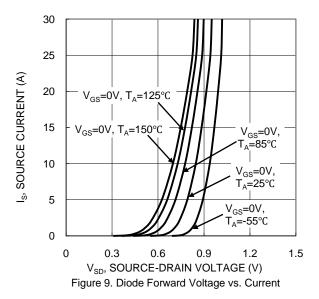
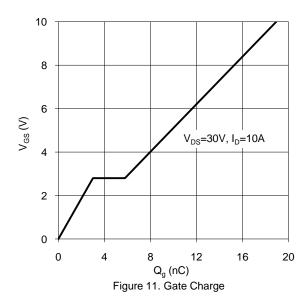


Figure 7. On-Resistance Variation with Temperature





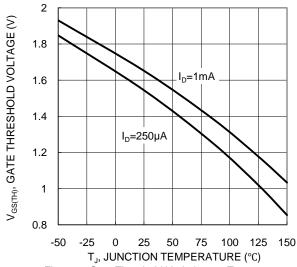
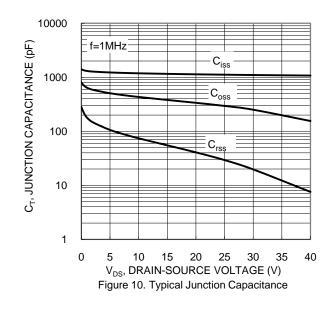
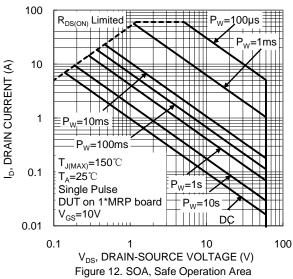
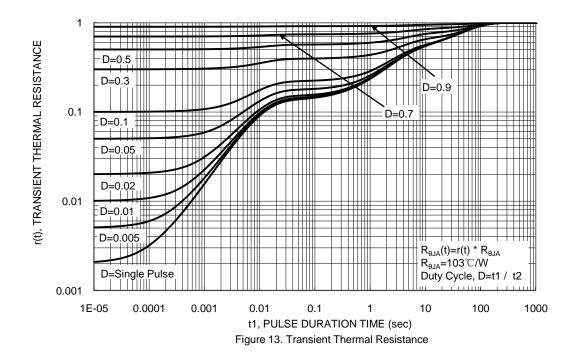


Figure 8. Gate Threshold Variation vs. Temperature



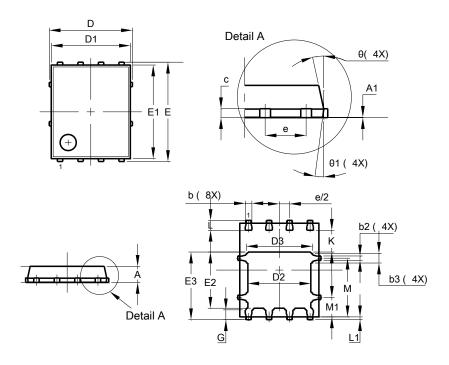






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

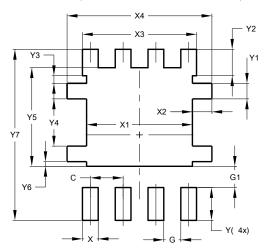


POWERDI®5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
C	0.230	0.330	0.277		
D	5.15 BSC				
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	6.15 BSC				
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10°	12º	11º		
θ1	6º	8º	7º		
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			

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