

DMS3016SSSA

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	8.0 5.8	А
Pulsed Drain Current (Note 6)			I _{DM}	90	Α
Avalanche Current (Notes 6 & 7)			I _{AR}	13	А
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.3mH			E _{AR}	25.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.54	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	$R_{\theta JA}$	81	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

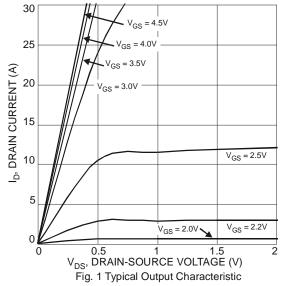
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Syllibol	IAITH	ТУР	IVIAA	Unit	rest condition	
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	K-/	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	7		1.0	mA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_		±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	.000	•				1.00 =:=1, 1.00 =:	
Gate Threshold Voltage	V _{GS(TH)}	1.0	-	2.3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Otatia Pasia Ossana Os Pasiatasa		.—	9	13		V _{GS} = 10V, I _D = 9.8A	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	11	16	mΩ	$V_{GS} = 4.5V, I_D = 9.8A$	
Forward Transfer Admittance	Y _{fs}	1	11	-	s	$V_{DS} = 5V, I_{D} = 9.8A$	
Diode Forward Voltage	V _{SD}	7	0.35	0.6	V	$V_{GS} = 0V, I_{S} = 1A$	
Maximum Body-Diode + Schottky Continuous Current	Is	_	_	5	Α	-	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	1849	_	pF	\\ 45\\\\\ 0\\	
Output Capacitance	Coss	_	158	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	l	123	_	pF	1 = 1.0IVII IZ	
Gate Resistance	R_g	0.53	2.68	4.82	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Q_g		18.5		nC		
Total Gate Charge V _{GS} = 10V	Q_g		43	_	nC	$V_{DS} = 15V, V_{GS} = 10V,$ $I_{D} = 9.8A$	
Gate-Source Charge	Qgs	_	4.7	_	nC		
Gate-Drain Charge	Q_{gd}	_	4.0	_	nC		
Turn-On Delay Time	t _{D(on)}	_	6.62	_	ns		
Turn-On Rise Time	t _r	_	8.73	_	ns	$V_{GS} = 10V, V_{DS} = 10V,$ $R_g = 3\Omega, R_L = 1.2\Omega$	
Turn-Off Delay Time	t _{D(off)}	_	36.41	_	ns		
Turn-Off Fall Time	t _f	_	4.69		ns		

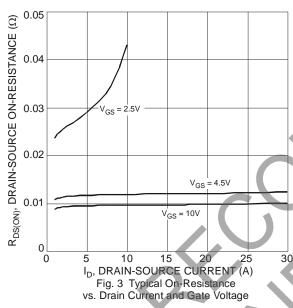
Notes:

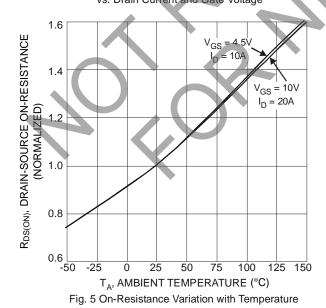
- 5. Device mounted on minimum recommended layout. The value in any given application depends on the user's specific board design. 6. Repetitive rating, pulse width limited by junction temperature.
- 7. I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.

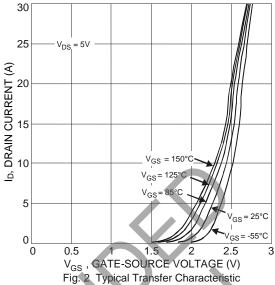


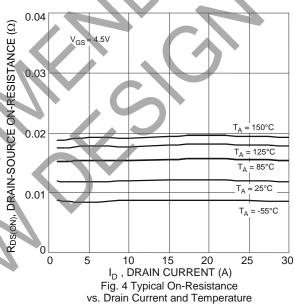
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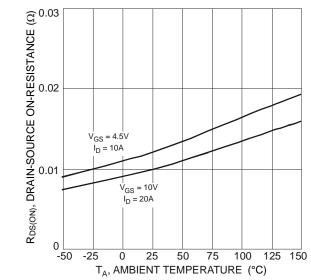


Fig. 6 On-Resistance Variation with Temperature



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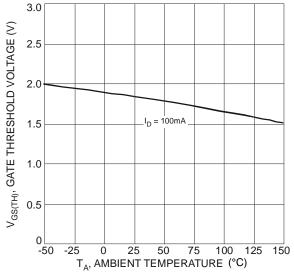
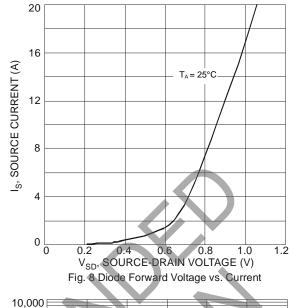
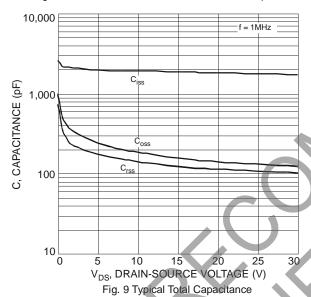
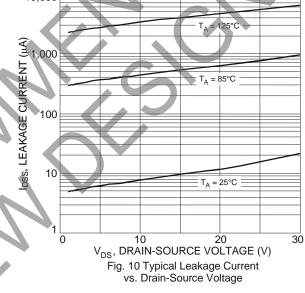
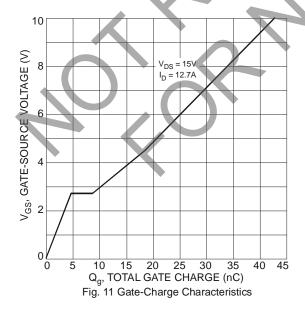


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





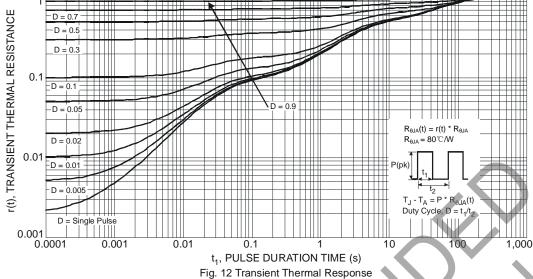






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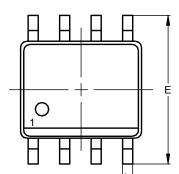


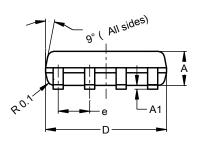
SO-8

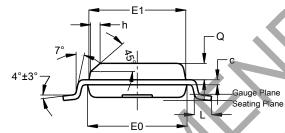


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



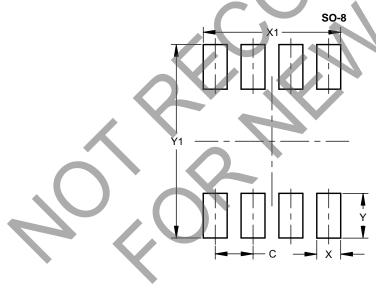




SO-8						
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
С	0.15	0.25	0.20			
D	4.85	4.95	4.90			
E	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е	1		1.27			
h	-		0.35			
L	0.62	0.82	0.72			
Q	0.60	0.70	0.65			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Υ	1.505
V1	6.50



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