

NOT RECOMMENDED FOR NEW DESIGN **USE DMP3068L**

DMP3130L

Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 5) // 45/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-3.0 -2.6	А
Continuous Drain Current (Note 5) V _{GS} = -4.5V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-4.1 -3.2	А
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-1.6	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-20	А

Thermal Characteristics

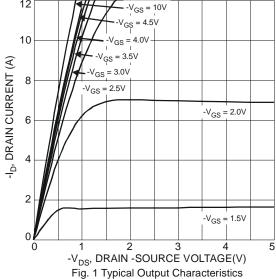
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P _D	0.7	W
	$T_A = +70$ °C	- PD	0.4	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	184	°CW
Thermal Nesistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	115	C/VV
Total Power Dissipation (Note 6)	$T_A = +25$ °C	Pn	1.3	W
Total Fower Dissipation (Note 0)	$T_A = +70^{\circ}C$	PD	0.8	VV
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	94	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	61	°C/W
Thermal Resistance, Junction to Case		$R_{ heta JC}$	25	
Operating and Storage Temperature Range		T _J , T _{ST}	-55 to +150	°C

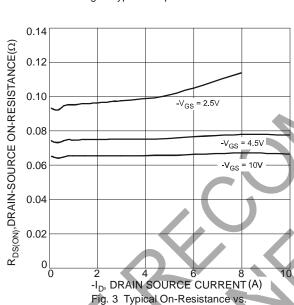
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	1		V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	-	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}		1-7	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.6		-1.3	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
			5 9	77		$V_{GS} = -10V, I_D = -4.2A$
Static Drain-Source On-Resistance	R _{DS(ON)}		73	95	mΩ	$V_{GS} = -4.5V, I_{D} = -4A$
		_	115	150		$V_{GS} = -2.5V, I_D = -3A$
Forward Transconductance	G fs	_	8	_	S	$V_{DS} = -5V, I_{D} = -4A$
Source-Drain Diode Forward Voltage	V _{SD}	_	-0.8	-1.25	V	$V_{GS} = 0V, I_{S} = -3.0A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	432	864	pF	\/ 45\/\/ 0\/
Output Capacitance	Coss		87	174	pF	$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss		62	124	pF	1 = 1.000112
Gate Resistance	R _G	_	4.04	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Q_{G}	_	5.9	11.8	nC	$V_{DS} = -15V$, $V_{GS} = -4.5V$, $I_{D} = -4.0A$
Total Gate Gliarge			12	24		$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -4.0A$
Gate-Source Charge	Q_{GS}		1.0	2.0	110	Vps = -15V. Vgs = -4.5V. Ip = -4.0A
Gate-Drain Charge	Q_{GD}	_	3.1	6.2		VDS = -15V, VGS = -4.5V, ID = -4.0A
Turn-On Delay Time	t _{D(ON)}	_	4.6	9.2		
Rise Time	t _R		6.5	13.0	ns	$V_{DS} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	27.8	55.6	115	$I_D = -1A, R_G = 6.0\Omega$
Fall Time	t _F	_	15.0	30.0		

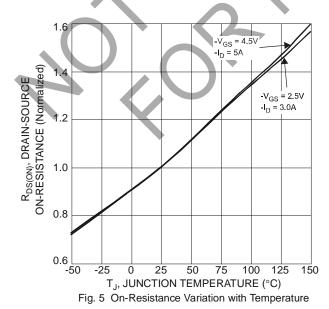
Notes:

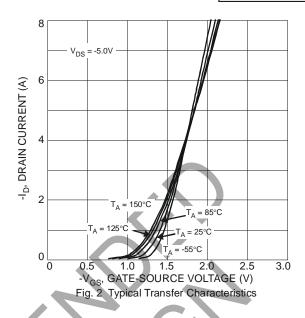
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

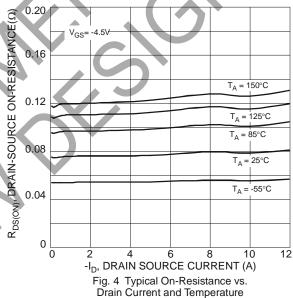


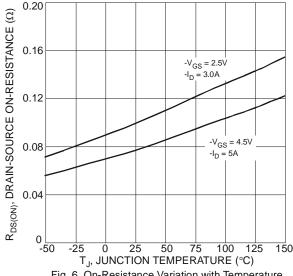


Drain Current and Gate Voltage











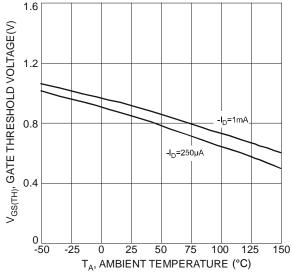
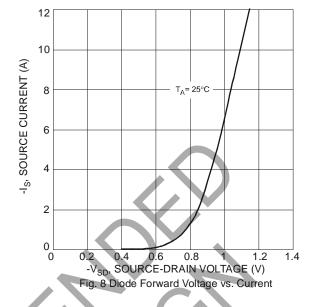
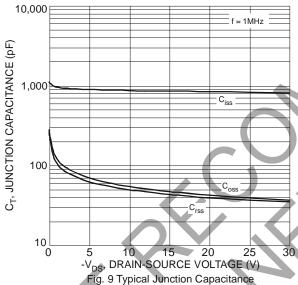
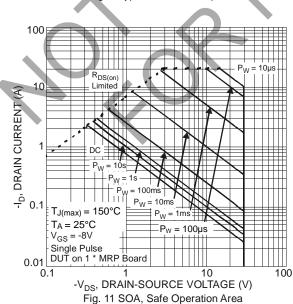
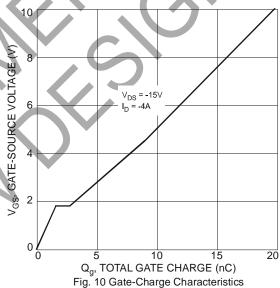


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

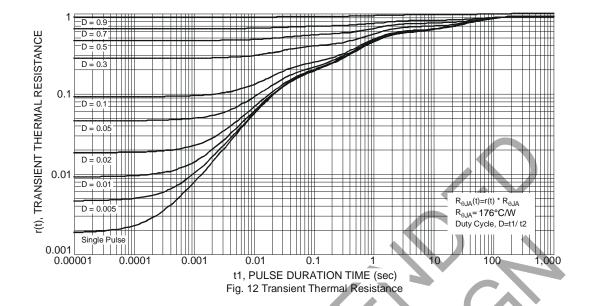










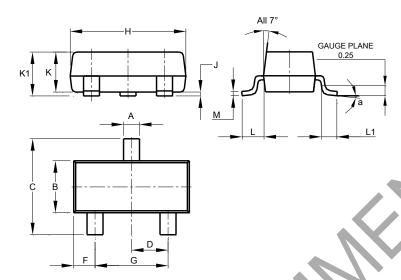




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

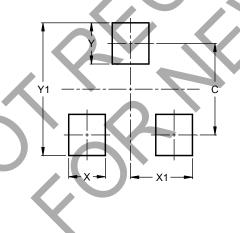


SOT23						
Dim	Min	Max	Тур			
A	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Η	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
C	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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DMP3130L

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