

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 6) V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	l n	-3.8 -2.9	А
Pulsed Drain Current (Note 7)			I _{DM}	-11	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P_D	1.08	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	$R_{ heta JA}$	115	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

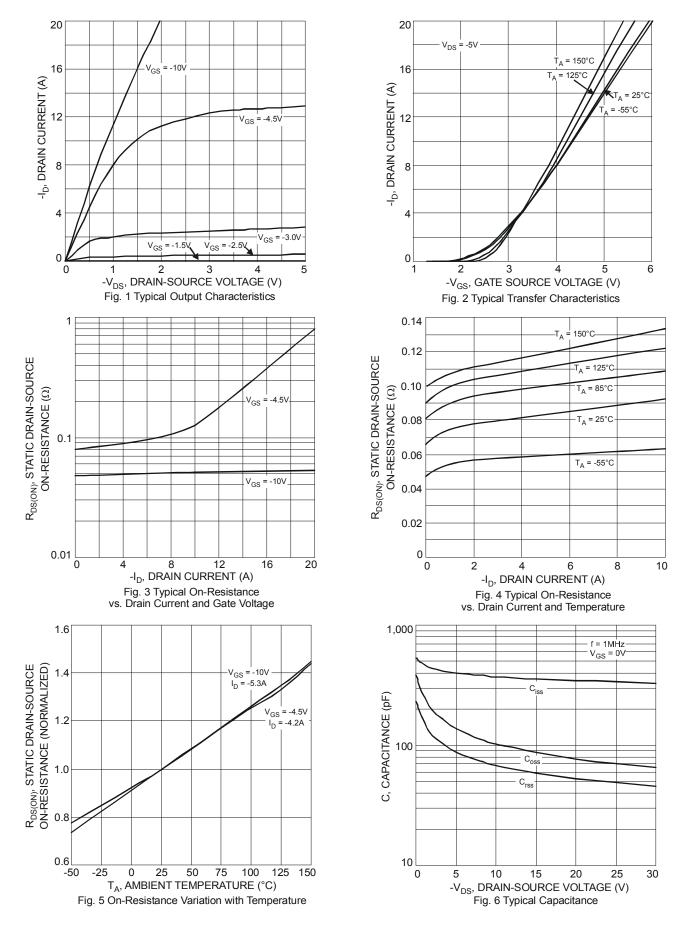
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-800	nA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-1.0	-1.8	-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	J		56 98	70 120	mΩ	$V_{GS} = -10V$, $I_D = -3.8A$
Static Dialii-Source Oil-Resistance	R _{DS(ON)}					$V_{GS} = -4.5V$, $I_D = -3.0A$
Forward Transfer Admittance	Y _{fs}	_	3.6	_	S	V _{DS} = -5V, I _D = -2.7A
Diode Forward Voltage (Note 7)	V _{SD}	_	_	-1.26	V	V _{GS} = 0V, I _S = -2.7A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	336	1008	pF	
Output Capacitance	Coss		70	210	pF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	49	147	pF	1
Gate Resistance	R_{G}	_	4.6	_	Ω	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz
SWITCHING CHARACTERISTICS (Note 9)						
Total Gate Charge	Qg		4.0	8.0	nC	$V_{DS} = -15V$, $V_{GS} = -4.5V$, $I_{D} = -3.8A$
		_	7.8	_		V _{DS} = -15V, V _{GS} = -10V, I _D = -3.8A
Gate-Source Charge	Qgs	_	1.0	_		
Gate-Drain Charge	Q _{gd}	_	2.5	_		
Turn-On Delay Time	t _{d(on)}	_	6.0	12.0	ns	V_{DS} = -15V, V_{GS} = -10V, I_{D} = -1A, R_{G} = 6.0 Ω
Rise Time	tr	_	5.0	10.0		
Turn-Off Delay Time	t _{d(off)}	_	17.6	35.2		
Fall Time	t _f	_	9.5	19.0		

Notes:

- 6. Device mounted on FR-4 PCB on 2 oz., 0.5 in. 2 copper pads and t \leq 5 sec. 7. Pulse width \leq 10 μ S, Duty Cycle \leq 1%. 8. Short duration pulse test used to minimize self-heating effect.

- 9. Guaranteed by design. Not subject to production testing.







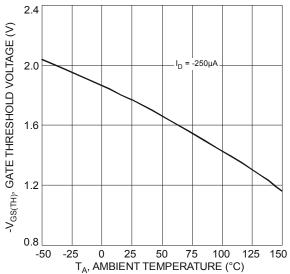
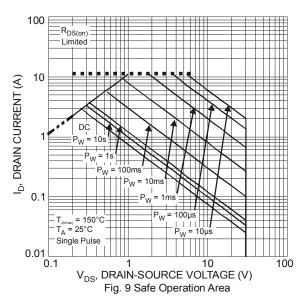
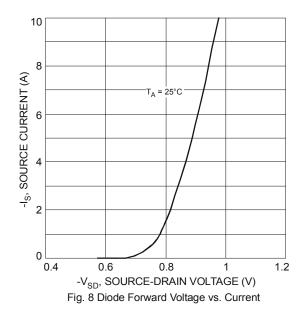


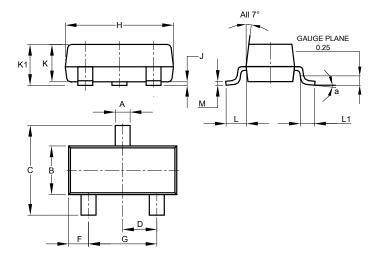
Fig. 7 Gate Threshold Variation vs. Ambient Temperature





Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

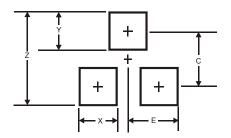


SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
7	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
α	8°				
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Υ	0.9
С	2.0
E	1.35

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