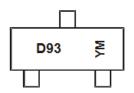


Marking Information



D93= Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2016		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	D			J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	60	V	
Gate-Source Voltage		Vgss	±20	V	
Continuous Drain Current (Note C) Vac. 45V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	340 270	mA
Continuous Drain Current (Note 6) Vgs = 4.5V	t<5s	$T_A = +25$ °C $T_A = +70$ °C	lo	400 300	mA
Maximum Continuous Body Diode Forward Curren	Is	340	mA		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	IDM	1.2	Α		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Davi	398	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	RөJA	306	C/VV
Total Power Dissipation (Note 6)		PD	470	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	273	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	RθJA	235	C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	_	_	>	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.0	V	$V_{DS} = 10V, I_{D} = 250\mu A$
			1.2	2.0		$V_{GS} = 4.5V, I_{D} = 0.1A$
Static Drain-Source On-Resistance	RDS(ON)	_	1.4	2.5	Ω	$V_{GS} = 2.5V, I_{D} = 0.05A$
			1.8	3.0		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	_	1.8	_	S	V _{DS} =10V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	_	0.8	1.3	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)		•			•	
Input Capacitance	Ciss	_	32	_	pF	.,
Output Capacitance	Coss	_	3.9	_	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.4	_	pF	1 = 1.0lvin2
Gate Resistance	Rg	_	101	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.5	_	nC	4.577.77
Gate-Source Charge	Qgs	_	0.09	_	nC	V _G S = 4.5V, V _D S = 10V,
Gate-Drain Charge	Q_{gd}	_	0.09	_	nC	I _D = 250mA
Turn-On Delay Time	tD(ON)	_	2.4	_	ns	
Turn-On Rise Time	tR	_	2.5	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	_	22.6	_	ns	$R_G = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	tr	_	12.5	_	ns	

7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing. Notes:



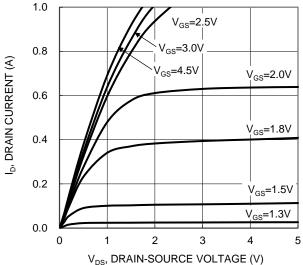


Figure 1. Typical Output Characteristic

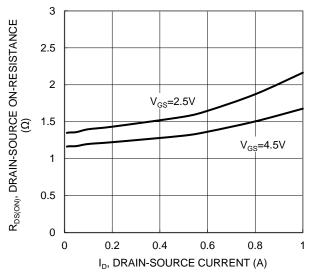


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

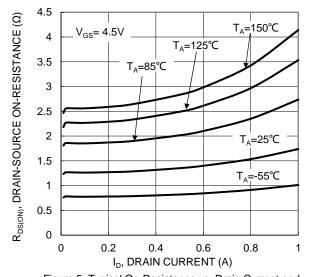
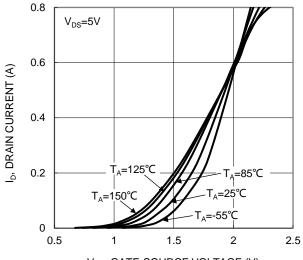


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

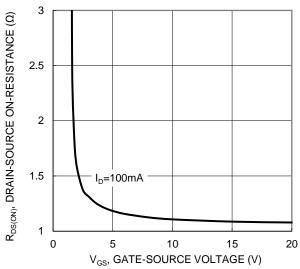


Figure 4. Typical Transfer Characteristic

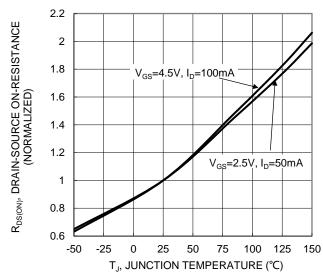


Figure 6. On-Resistance Variation with Junction Temperature



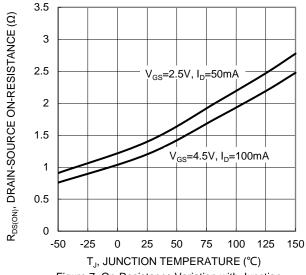


Figure 7. On-Resistance Variation with Junction Temperature

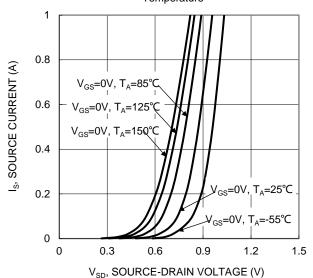


Figure 9. Diode Forward Voltage vs. Current

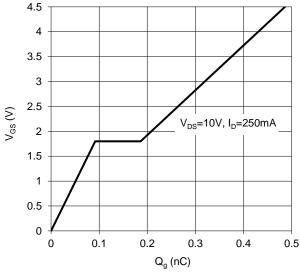


Figure 11. Gate Charge

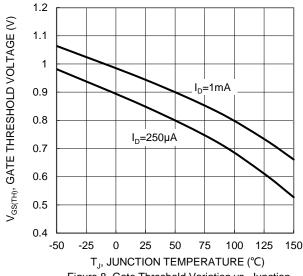


Figure 8. Gate Threshold Variation vs. Junction Temperature

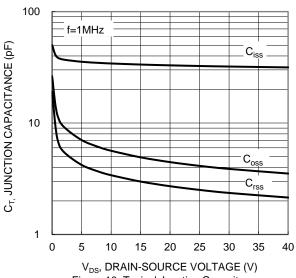


Figure 10. Typical Junction Capacitance

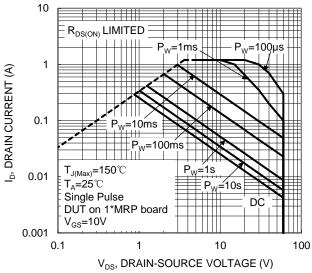


Figure 12. SOA, Safe Operation Area



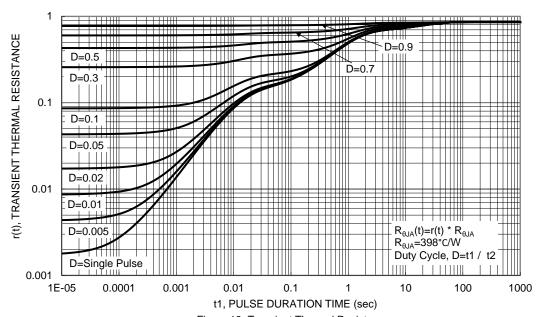


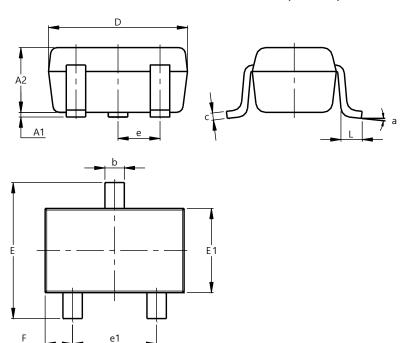
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323 (Standard)

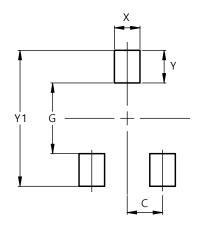


COT222 (Standard)								
SOT323 (Standard)								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.80	1.00	0.90					
b	0.20	0.40	0.30					
С	0.08	0.18	0.13					
D	1.80	2.20	2.00					
Е	2.00	2.45	2.225					
E1	1.15	1.35	1.25					
е			0.65					
e1	1.20	1.40	1.30					
F	0.25	0.475	0.3625					
L	0.25	0.46	0.355					
а	0°	8°						
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323 (Standard)



Dimensions	Value
Dimensions	(in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
V1	2 500



IMPORTANT NOTICE

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com