

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	550 450	mA
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	1.2	Α

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	450	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	283	°C/W
Total Power Dissipation (Note 6)		P_{D}	940	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	133	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

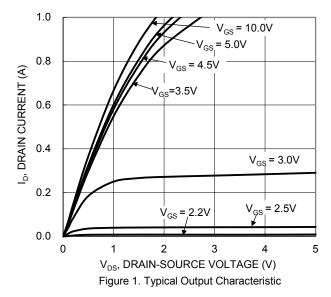
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	1.0	1.6	2.5	V	V_{DS} = 10V, I_D = 1mA	
Static Drain-Source On-Resistance			_ 2	2.0	2.0 3.0 Ω	$V_{GS} = 10V, I_D = 0.5A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}			3.0		$V_{GS} = 5V, I_D = 0.05A$	
Forward Transfer Admittance	Y _{fs}	80			mS	$V_{DS} = 10V, I_{D} = 0.2A$	
Diode Forward Voltage	V_{SD}	_	0.75	1.2	V	$V_{GS} = 0V, I_S = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	30	_	pF		
Output Capacitance	Coss		4.2		рF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	2.9	_	pF	11 - 1.0WIHZ	
Gate Resistance	Rg	_	133	_	Ω	$f = 1MHz, V_{GS} = 0V, V_{DS} = 0V$	
Total Gate Charge	Q_g	_	392	_	рC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA	
Gate-Source Charge	Qgs	_	157	_	рC		
Gate-Drain Charge	Q_{gd}	_	92	_	рC		
Turn-On Delay Time	t _{D(ON)}	_	3.9	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$ $R_{G} = 25\Omega, I_{D} = 200\text{mA}$	
Turn-On Rise Time	t _R	_	3.4	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	15.7	_	ns		
Turn-Off Fall Time	t _F	_	9.9	_	ns		

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.

 8. Guaranteed by design. Not subject to product testing.





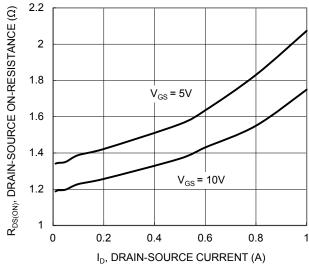
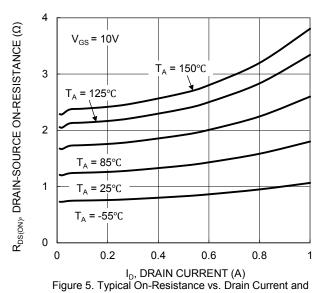
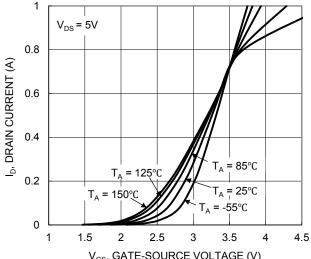


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage



Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

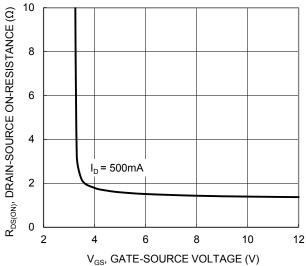


Figure 4. Typical Transfer Characteristic

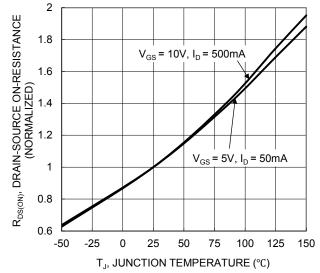


Figure 6. On-Resistance Variation with Junction Temperature



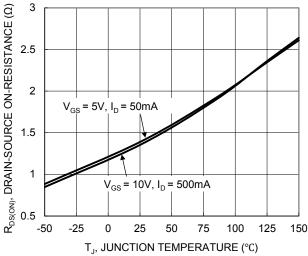
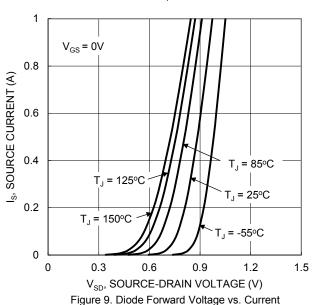


Figure 7. On-Resistance Variation with Junction Temperature



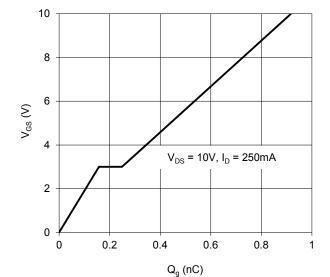


Figure 11. Gate Charge

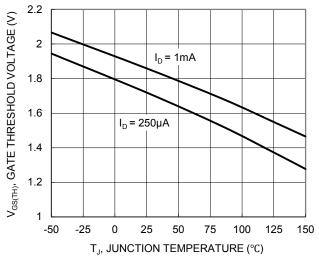


Figure 8. Gate Threshold Variation vs. Junction Temperature

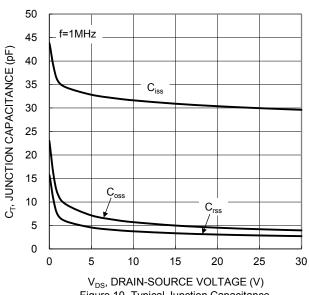
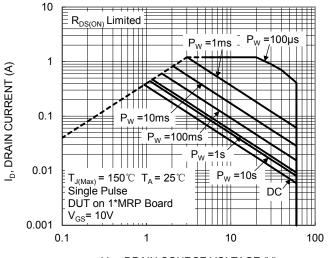


Figure 10. Typical Junction Capacitance



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



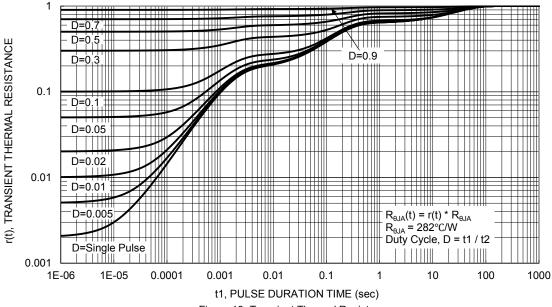
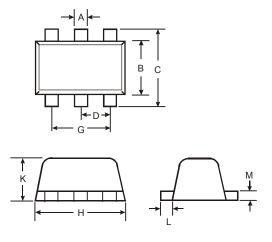


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

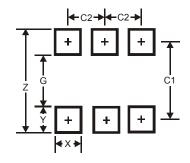
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT563						
Dim	Min	Max	Тур			
Α	0.15	0.30	0.20			
В	1.10	1.25	1.20			
С	1.55	1.70	1.60			
D	-	-	0.50			
G	0.90	1.10	1.00			
Н	1.50	1.70	1.60			
K	0.55	0.60	0.60			
L	0.10	0.30	0.20			
М	0.10	0.18	0.11			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Υ	0.5
C1	1.7
C2	0.5



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