

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±12	V
Drain Current (Note 5)	I _D	4	Α
Body-Diode Continuous Current (Note 5)	Is	1	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	770	mW
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{ heta JA}$	162	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C

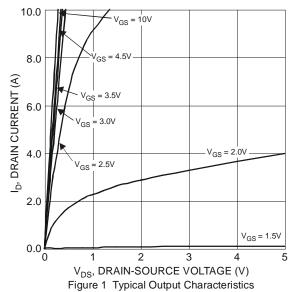
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

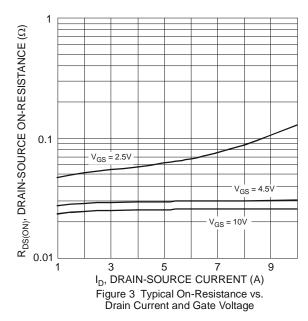
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	٧	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 30V$, $V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	0.5		1.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
				52		V _{GS} = 10V, I _D = 4A
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	65	mΩ	$V_{GS} = 4.5V, I_D = 3A$
			_	85		$V_{GS} = 2.5V, I_D = 2A$
Source-Drain Diode Forward Voltage	V_{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 2.0A$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	_	465	_	pF	
Output Capacitance	Coss	_	49.5	_	pF	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}	_	43.8	_	pF	
Gate Resistance	R_g	_	2.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 10V)	Q_g	_	11.7	_	nC	V _{DS} = 15V, I _D = 4A
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.5	_	nC	
Gate-Source Charge	Q_{gs}	_	1.1	_	nC	$V_{DS} = 15V, I_{D} = 4A$
Gate-Drain Charge	Q_{gd}	_	1.8	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	1.9	_	ns	
Turn-On Rise Time	t _R	_	1.6	_	ns	V _{DD} = 15V, V _{GEN} = 10V,
Turn-Off Delay Time	t _{D(OFF)}	_	10.3	_	ns	$R_{GEN} = 3\Omega$, $R_L = 3.75\Omega$
Turn-Off Fall Time	t _F	_	2.0	_	ns	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.6. Short duration pulse test used to minimize self-heating effect.7. Guaranteed by design. Not subject to production testing.









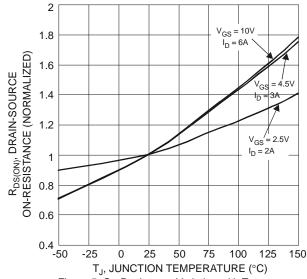


Figure 5 On-Resistance Variation with Temperature

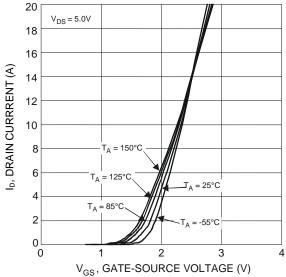


Figure 2 Typical Transfer Characteristics

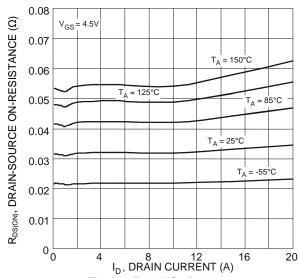


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

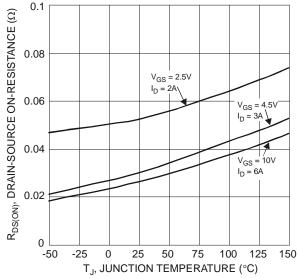
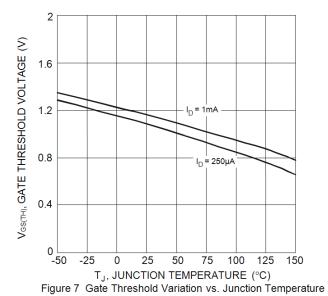
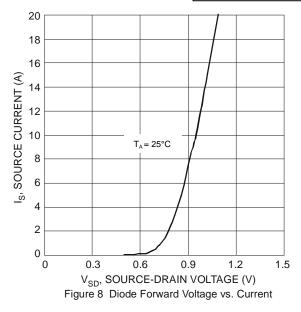


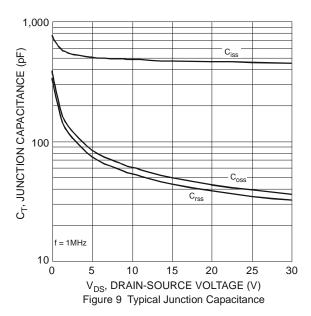
Figure 6 On-Resistance Variation with Temperature

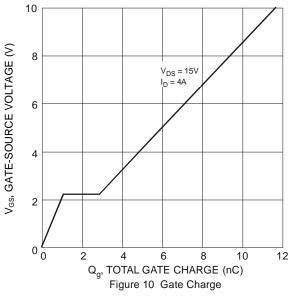












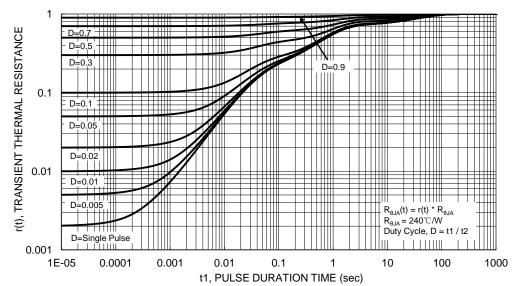


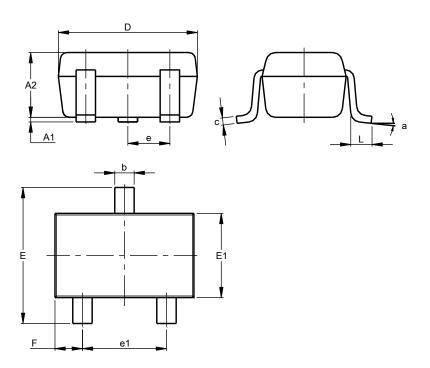
Figure 11. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

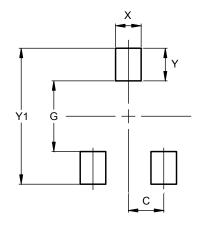


SOT323				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.25	0.40	0.30	
С	0.10	0.18	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
e1	1.20	1.40	1.30	
F	0.375	0.475	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500



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