

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_C = +25$ °C $T_C = +70$ °C	ΙD	30 25	А
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	ΙD	8.5 6.8	А
	t<10s	$T_A = +25$ °C $T_A = +70$ °C	ΙD	11.3 9.1	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	6.6 5.3	А
	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	8.7 7.0	А
Maximum Continuous Body Diode Forward Current (Note 5)			Is	2.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	60	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	18	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	16	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)		P_{D}	1.0	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	126	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	R _{0JA}	71]	
Total Power Dissipation (Note 6)		P _D	2.2	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	56	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	31		
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	7.0		
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to 150	°C	

Electrical Characteristics (@T_A = ±25°C, unless otherwise specified.)

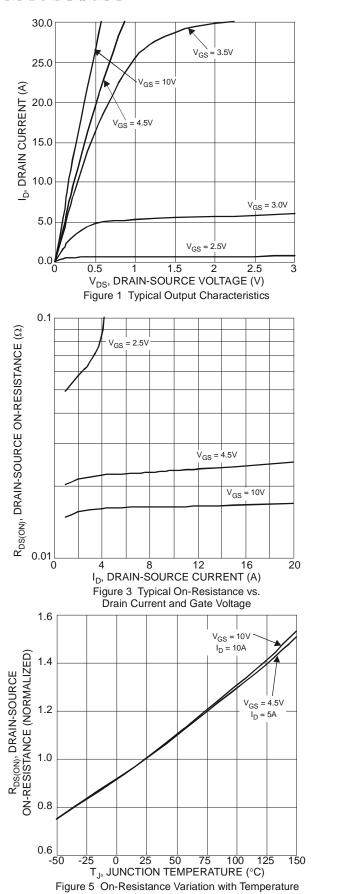
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	1.7	2.1	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance			16	21	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	21	35		$V_{GS} = 4.5V, I_D = 8.5A$	
Diode Forward Voltage	V_{SD}	0.5	_	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{ISS}	-	697	_	pF	1/ 15// // 0)/	
Output Capacitance	Coss	1	97	_	рF	$V_{DS} = 15V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{RSS}		67	_	рF	1 = 1.000112	
Gate resistance	R _G	1	1.47	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_{G}	1	6.0	_	nC		
Total Gate Charge (V _{GS} = 10V)	Q_G	1	13.2	_	nC	$V_{GS} = 10V, V_{DS} = 15V,$ $I_{D} = 9A$	
Gate-Source Charge	Q_{GS}	_	2.2	_	nC		
Gate-Drain Charge	Q_{GD}	_	1.8	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	4.3	_	ns		
Turn-On Rise Time	t _R	_	4.4	_	ns	$V_{DD} = 15V, V_{GS} = 10V,$ $R_{L} = 15\Omega, I_{D} = 1A, R_{G} = 6\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	_	20.1	_	ns		
Turn-Off Fall Time	t _F	_	4.1	_	ns		
Reverse Recovery Time	T_RR	_	7.3	_	ns	I _F = 9A, di/dt = 500A/μs	
Reverse Recovery Charge	Q _{RR}		7.9	_	nC		

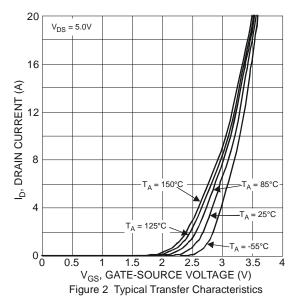
^{5.} Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

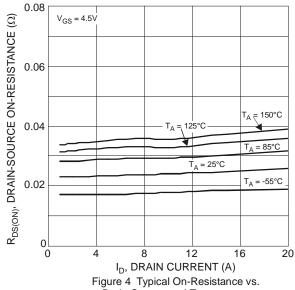
^{7.} I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.

^{8.} Short duration pulse test used to minimize self-heating effect.









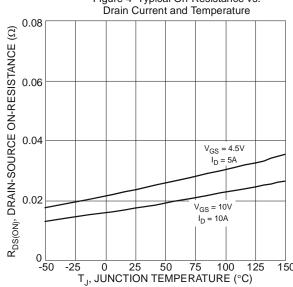
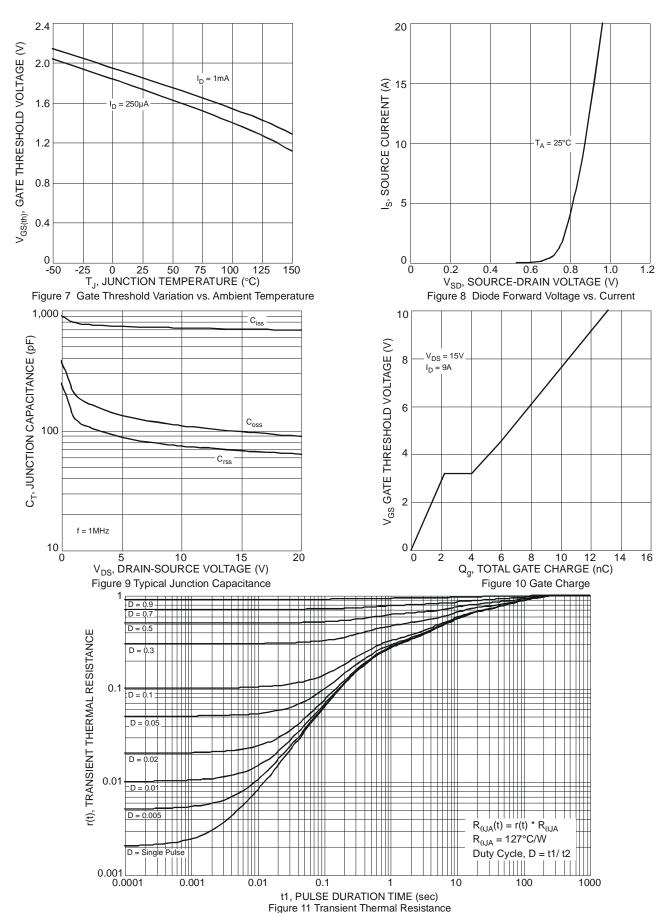


Figure 6 On-Resistance Variation with Temperature



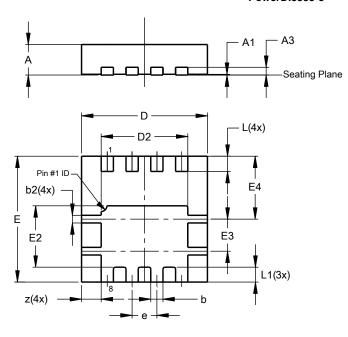




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

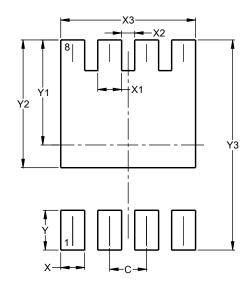


PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	_	_	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
E	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	-	-	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
Z	_	_	0.515			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
X	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700



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