

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteri	Symbol	Q1 Value	Q2 Value	Units		
Drain-Source Voltage	V _{DSS}	12	-12	V		
Gate-Source Voltage				±8	±8	V
Continuous Desir Courset (Nata 5) V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	9.5 7.6	-6.9 -5.5	А
Continuous Drain Current (Note 5) V _{GS} = 4.5V	t<10s	T _A = +25°C T _A = +70°C	I _D	13.0 10.4	-9.4 -7.5	А
Maximum Body Diode Forward Current	Is	2	-2	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1	I _{DM}	50	-35	Α		
Avalanche Current (Note 6) L = 0.1mH				9.7	-9.2	Α
Avalanche Energy (Note 6) L = 0.1mH				4.7	4.3	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	D-	2.3	- W
Total Fower Dissipation (Note 3)	T _A = +70°C	P_{D}	1.5	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	54	°C/W
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{ heta JA}$	29	
Thermal Resistance, Junction to Case (Note 5)		$R_{ heta JC}$	4.1	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics Q1 N-Channel (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	12	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 12V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	0.6	_	1.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	9.6	17	mΩ	$V_{GS} = 4.5V, I_D = 11.8A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	11	25	11122	$V_{GS} = 2.5V, I_D = 9.8A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 2.9A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	1787	_		V _{DS} = 6V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	297	_	pF		
Reverse Transfer Capacitance	Crss	_	265	_			
Gate Resistance	R_{G}	_	1.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	18.6	_		V _{DS} = 6V, I _D = 11.8A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	35.4	_	nC		
Gate-Source Charge	Qgs		2.7	_	nc nc		
Gate-Drain Charge	Q_{gd}		3.8	_			
Turn-On Delay Time	t _{D(on)}	_	6.9	_		$V_{DD}=6V,\ R_L=6\Omega$ $V_{GS}=4.5V,\ R_G=6\Omega,\ I_D=1A$	
Turn-On Rise Time	t _r		10.9	_	nS		
Turn-Off Delay Time	t _{D(off)}	_	70.3	_	113		
Turn-Off Fall Time	t _f	_	31.8	_			
Body Diode Reverse Recovery Time	t _{rr}	_	13.1	_	nS	I _F = 11.8A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Qrr		2.2	_	nC	I _F = 11.8A, di/dt = 100A/µs	

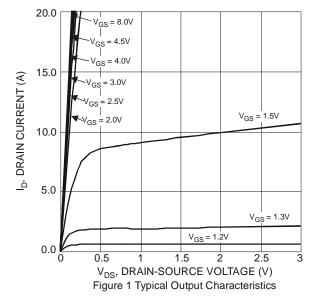
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

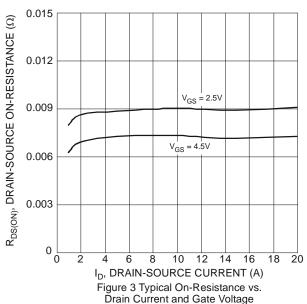
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 ^{6.} I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = 25°C.
7. Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.







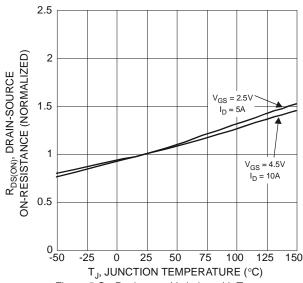
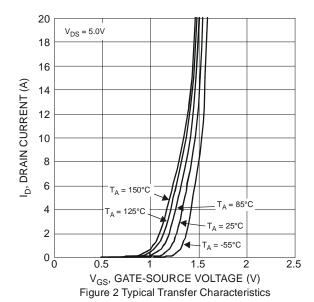
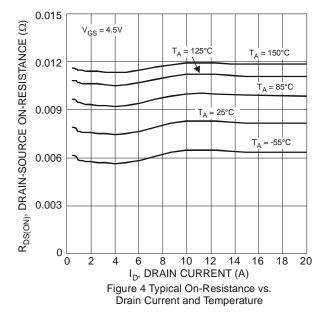


Figure 5 On-Resistance Variation with Temperature





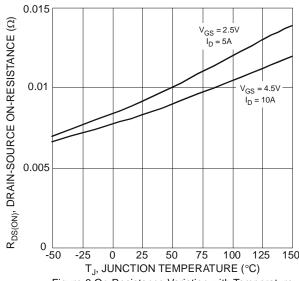


Figure 6 On-Resistance Variation with Temperature



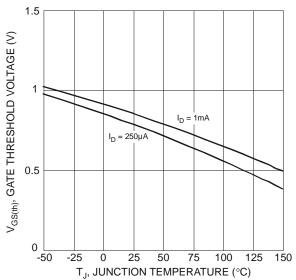
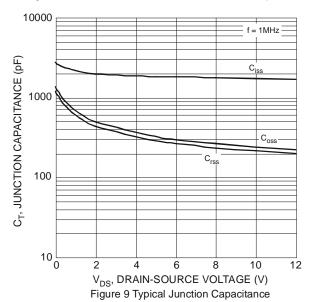
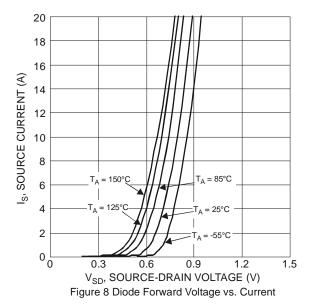
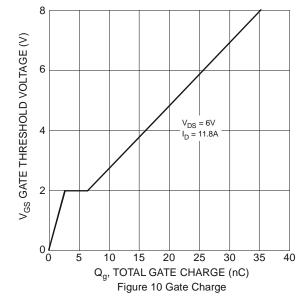


Figure 7 Gate Threshold Variation vs. Ambient Temperature







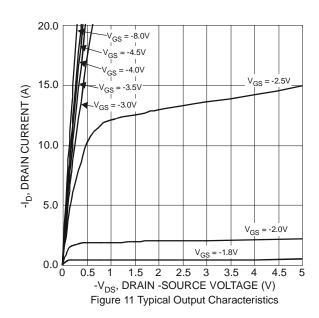


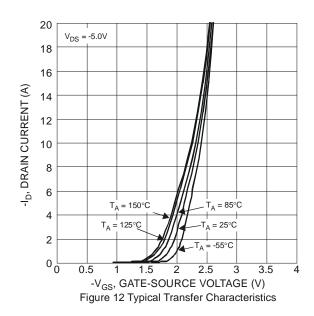
Electrical Characteristics Q2 P-Channel (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-12	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -12V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.6	_	-1.5	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	 	_	21	32	mΩ	$V_{GS} = -4.5V, I_D = -8.9A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	41	53	11177	$V_{GS} = -2.5V, I_D = -6.9A$	
Diode Forward Voltage	V_{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_S = -2.9A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	_	2100	_		$V_{DS} = -6V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	Coss	_	872	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	626	_			
Gate Resistance	R_{G}	_	23.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_g	_	23.7	_		$V_{DS} = -6V$, $I_{D} = -8.9A$	
Total Gate Charge (V _{GS} = -8V)	Qg	_	38.8	_	nC		
Gate-Source Charge	Qgs	_	5.3	_	IIC		
Gate-Drain Charge	Q _{gd}	_	9.8	_			
Turn-On Delay Time	t _{D(on)}	_	10.6	_		V_{DD} = -6V, R_L = 6 Ω V_{GS} = -4.5V, R_G = 6 Ω , I_D = -1A	
Turn-On Rise Time	t _r	_	25.5	_	nS		
Turn-Off Delay Time	t _{D(off)}	_	144	_	113		
Turn-Off Fall Time	t _f	_	129	_			
Body Diode Reverse Recovery Time	t _{rr}	_	48.9	_	nS	I _F = -8.9A, di/dt = -100A/μs	
Body Diode Reverse Recovery Charge	Q _{rr}	_	15.3	_	nC	$I_F = -8.9A$, $di/dt = -100A/\mu s$	

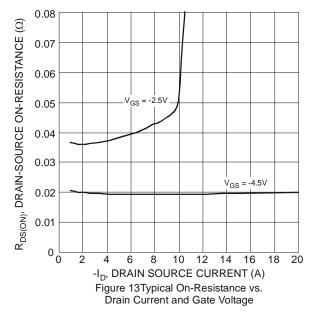
Notes:

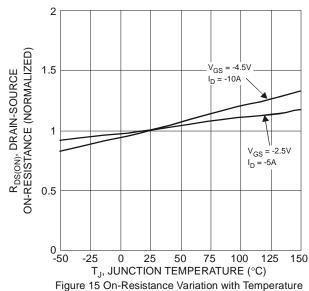
- 6. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J=25^{\circ}C$. 7. Short duration pulse test used to minimize self-heating effect.











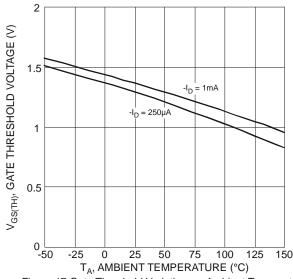
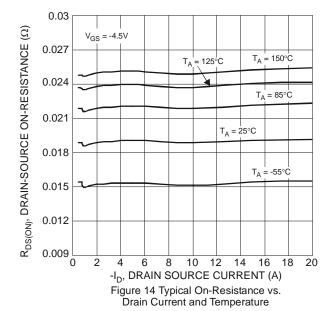


Figure 17 Gate Threshold Variation vs. Ambient Temperature



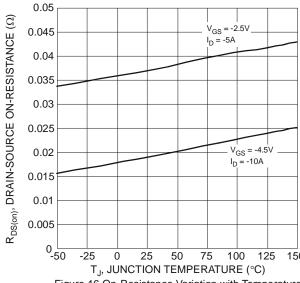


Figure 16 On-Resistance Variation with Temperature

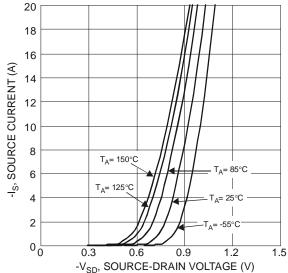
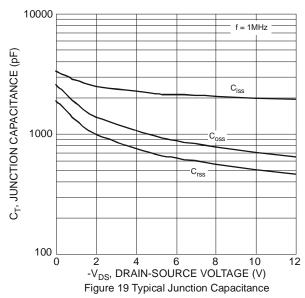
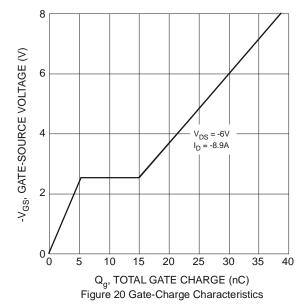
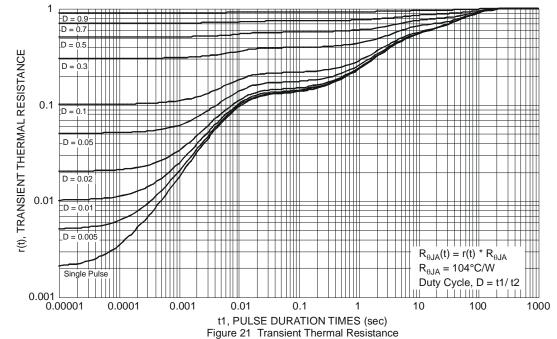


Figure 18 Diode Forward Voltage vs. Current





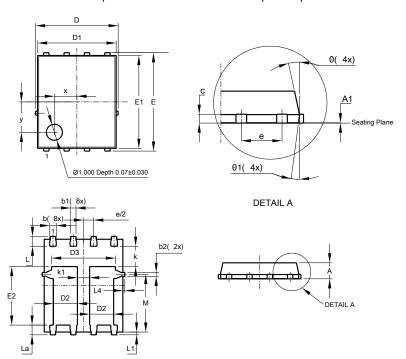






Package Outline Dimensions

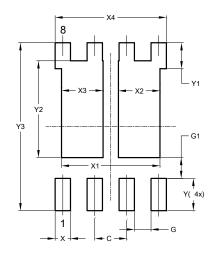
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0	0.05	0.02		
b	0.33	0.51	0.41		
b1	0.300	0.366	0.333		
b2	0.20	0.35	0.25		
С	0.23	0.33	0.277		
D	5	.15 BS0	\circ		
D1	4.85	4.95	4.90		
D2	1.40	1.60	1.50		
D3	-	-	3.98		
Е	6.15 BSC				
E1	5.75	5.85	5.80		
E2	3.56	3.76	3.66		
е	1.27BSC				
k	-	-	1.27		
k1	0.56	-	-		
L	0.51	0.71	0.61		
La	0.51	0.71	0.61		
L1	0.05	0.20	0.175		
L4	-	-	0.125		
М	3.50	3.71	3.605		
Х	-	-	1.400		
у	-	-	1.900		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value		
	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	3.910		
X2	1.650		
Х3	1.650		
X4	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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