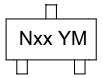


#### **Marking Information**



Nxx = Product Type Marking Code (See Table in Features) YM = Date Code Marking Y or  $\underline{Y}$  = Year (ex: I = 2021)

M or M = Month (ex: 9 = September)

Date Code Key												
Year	2002		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	0			J	K	L	М	N	0	Р	R	S
Month	lan	Feb	Mar	Anr	May	lun	Jul	Aug	Sep	Oct	Nov	Dec
Monun	Jan	гер	IVIdi	Apr	iviay	Jun	Jui	Aug	Sep	001	NOV	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

#### Absolute Maximum Ratings (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Collector Current	I <sub>C(MAX)</sub>	100	mA

# Thermal Characteristics (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 5)	PD	150	mW	
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ ext{ heta}JA}$	833	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

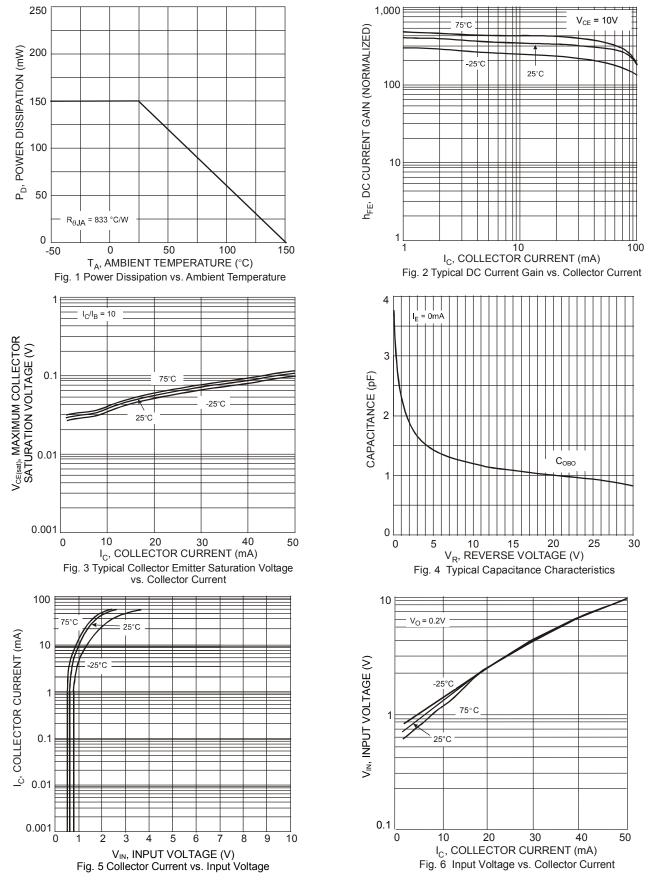
#### **Electrical Characteristics** (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50		_	V	I <sub>C</sub> = 50μA
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	50		_	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage	$BV_{EBO}$	5		_	V	I <sub>E</sub> = 50μA
Collector Cutoff Current	I <sub>CBO</sub>	_		0.5	μA	V <sub>CB</sub> = 50V
Emitter Cutoff Current	I <sub>EBO</sub>	_		0.5	μA	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	_		0.3	V	$\begin{split} &  _{C/I_B} = 10 \text{mA} / 1 \text{mA} & \text{DDTC113TE} \\ &  _{C/I_B} = 5 \text{mA} / 0.5 \text{mA} & \text{DDTC123TE} \\ &  _{C/I_B} = 2.5 \text{mA} / 0.25 \text{mA} & \text{DDTC143TE} \\ &  _{C/I_B} = 1 \text{mA} / 0.1 \text{mA} & \text{DDTC114TE} \\ &  _{C/I_B} = 5 \text{mA} / 0.5 \text{mA} & \text{DDTC124TE} \\ &  _{C/I_B} = 2.5 \text{mA} / 0.25 \text{mA} & \text{DDTC144TE} \\ &  _{C/I_B} = 1 \text{mA} / 0.1 \text{mA} & \text{DDTC115TE} \\ &  _{C/I_B} = 0.5 \text{mA} / 0.05 \text{mA} & \text{DDTC125TE} \end{split}$
DC Current Transfer Ratio	h <sub>FE</sub>	100	250	600	_	I <sub>C</sub> = 1mA, V <sub>CE</sub> = 5V
Input Resistor (R1) Tolerance	$\Delta R_1$	-30		+30	%	—
Gain-Bandwidth Product (Note 6)	f⊤		250	_	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz

Notes: 5. Mounted on FR-4 PC Board with minimum recommended pad layout. 6. Transistor only.



## **Typical Curves – DDTC114TE**

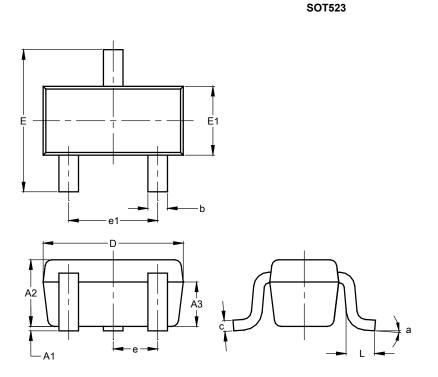


DDTC (R1-ONLY SERIES) E Document number: DS30315 Rev. 11 - 2 Downloaded from Arrow.com.



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

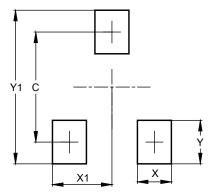


SOT523						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.60	0.80	0.75			
A3	0.45	0.65	0.50			
b	0.15	0.30	0.22			
С	0.10	0.20	0.12			
D	1.50	1.70	1.60			
Е	1.45	1.75	1.60			
E1	0.75	0.85	0.80			
e	e 0.50 BSC					
e1	0.90	1.10	1.00			
L	0.20	0.40	0.33			
а	0°		8°			
All Dimensions in mm						

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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