

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	I <sub>PP</sub>	5	A	8/20μs, Per Figure 3
ESD Protection – Contact Discharge	V <sub>ESD_Contact</sub>	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V <sub>ESD_Air</sub>	±15	kV	Standard IEC 61000-4-2

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P <sub>D</sub>	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	500	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse working voltage	V <sub>RWM</sub>	—	—	3.3	V	—
Reverse current (Note 6)	I <sub>R</sub>	—	0.1	1.0	μA	V <sub>R</sub> = V <sub>RWM</sub> = 3.3V
Reverse breakdown voltage	V <sub>BR</sub>	6.0	—	—	V	I <sub>R</sub> = 1mA
Forward voltage	V <sub>F</sub>	0.6	0.8	0.95	V	I <sub>F</sub> = 8mA
Reverse clamping voltage, Positive Transients	V <sub>CL1</sub>	—	10.0	—	V	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20μs
Reverse clamping voltage, Negative Transients	V <sub>CL2</sub>	—	-1.7	—	V	I <sub>PP</sub> = -1A, t <sub>p</sub> = 8/20μs
Dynamic resistance	R <sub>DYN</sub>	—	0.9	—	Ω	I <sub>R</sub> = 1A, t <sub>p</sub> = 8/20μs
Capacitance	C <sub>T</sub>	—	0.85	1.2	pF	V <sub>R</sub> = 1.65V, f = 1MHz

- Notes:
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>.
  6. Short duration pulse test used to minimize self-heating effect.
  7. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: [http://www.diodes.com/destdtools/appnote\\_dnote.html](http://www.diodes.com/destdtools/appnote_dnote.html).

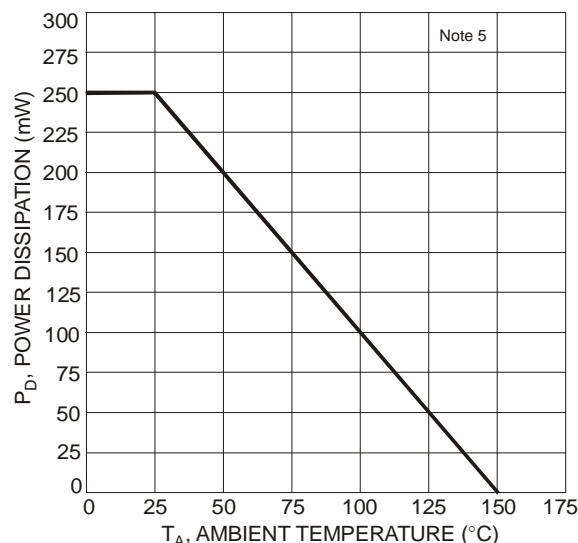


Figure 1 Power Derating Curve

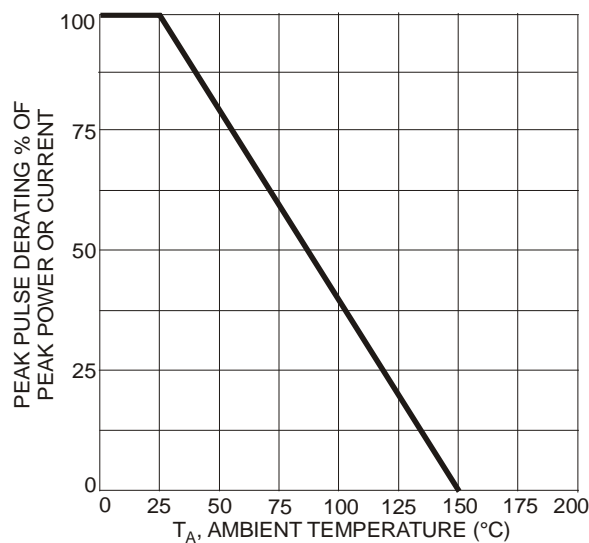


Figure 2 Pulse Derating Curve

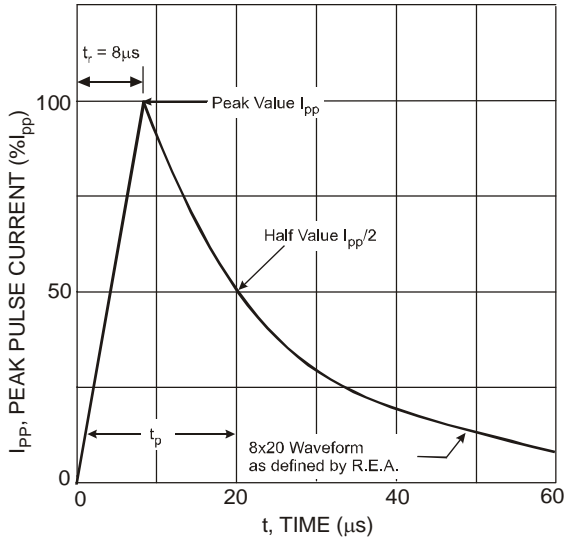


Figure 3 Pulse Waveform

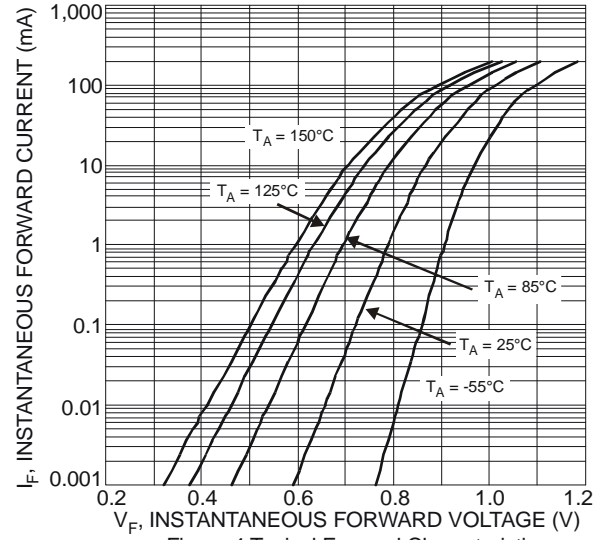


Figure 4 Typical Forward Characteristics

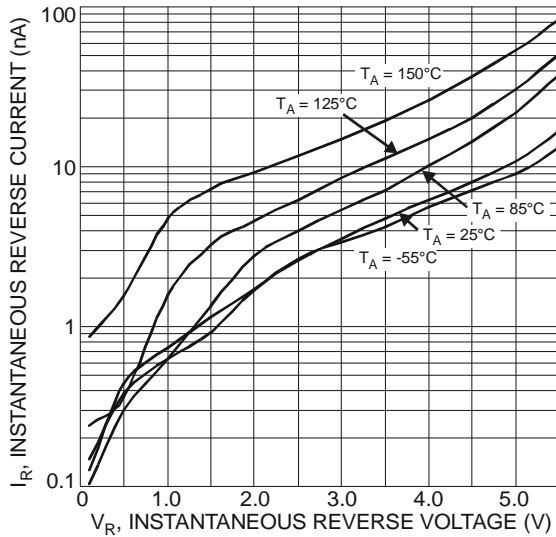


Figure 5 Typical Reverse Characteristics

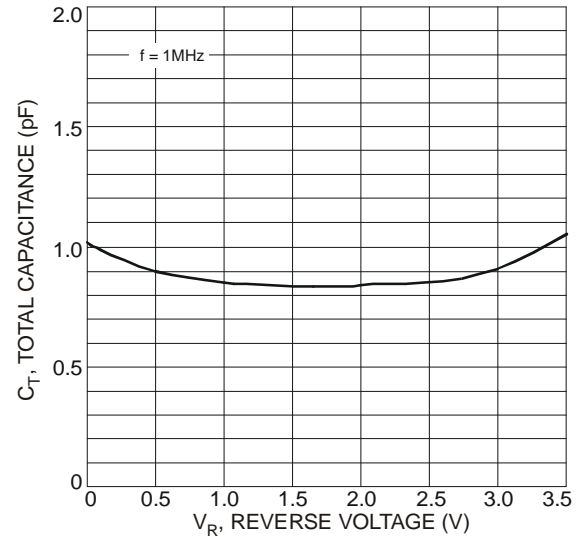
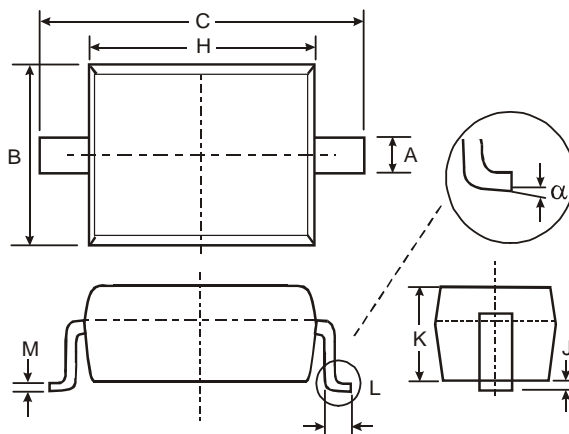


Figure 6 Typical Total Capacitance vs. Reverse Voltage

## Package Outline Dimensions

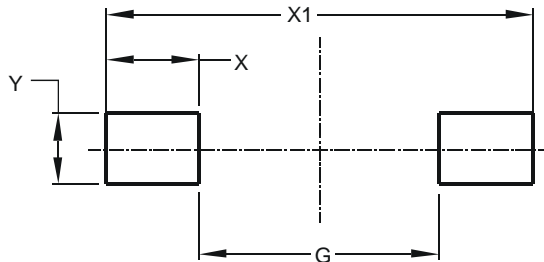
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOD323		
Dim	Min	Max
A	0.25	0.35
B	1.20	1.40
C	2.30	2.70
H	1.60	1.80
J	0.00	0.10
K	1.0	1.1
L	0.20	0.40
M	0.10	0.15
α	0°	8°
All Dimensions in mm		

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
G	1.520
X	0.590
X1	2.700
Y	0.450

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