

**CMLDM7002A
CMLDM7002AG*
CMLDM7002AJ**

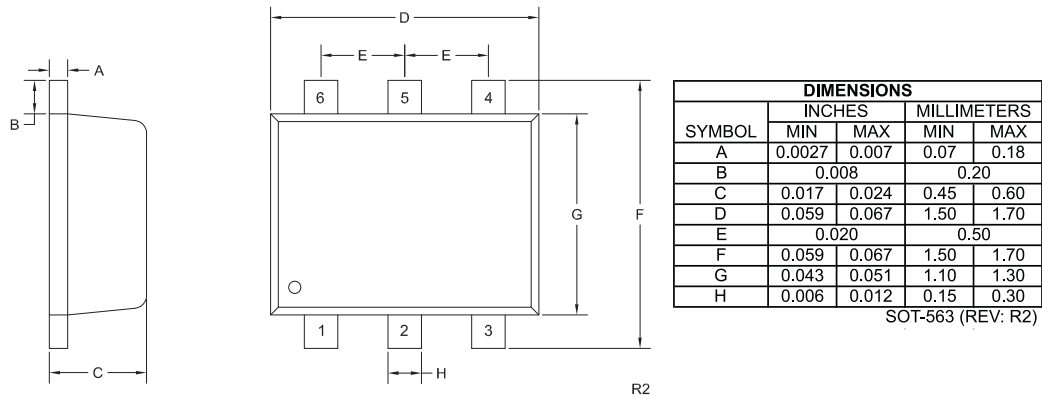
**SURFACE MOUNT SILICON
DUAL N-CHANNEL
ENHANCEMENT-MODE
MOSFETS**



ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued: ($T_A=25^\circ\text{C}$ unless otherwise noted)

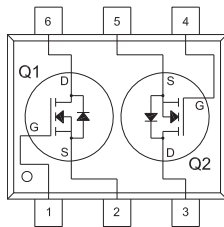
SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
C_{rss}	$V_{DS}=25\text{V}$, $V_{GS}=0$, $f=1.0\text{MHz}$		5.0	pF
C_{iss}	$V_{DS}=25\text{V}$, $V_{GS}=0$, $f=1.0\text{MHz}$		50	pF
C_{oss}	$V_{DS}=25\text{V}$, $V_{GS}=0$, $f=1.0\text{MHz}$		25	pF
$Q_{g(\text{tot})}$	$V_{DS}=30\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=100\text{mA}$	0.592		nC
Q_{gs}	$V_{DS}=30\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=100\text{mA}$	0.196		nC
Q_{gd}	$V_{DS}=30\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=100\text{mA}$	0.148		nC
t_{on} , t_{off}	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=200\text{mA}$ $R_G=25\Omega$, $R_L=150\Omega$		20	ns

SOT-563 CASE - MECHANICAL OUTLINE



PIN CONFIGURATIONS

**CMLDM7002A (USA Pinout)
CMLDM7002AG***

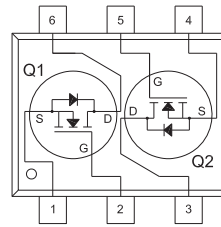


LEAD CODE:

- 1) Gate Q1
- 2) Source Q1
- 3) Drain Q2
- 4) Gate Q2
- 5) Source Q2
- 6) Drain Q1

MARKING CODES:
CMLDM7002A: L02
CMLDM7002AG*: C2G

CMLDM7002AJ (Japanese Pinout)



LEAD CODE:

- 1) Source Q1
- 2) Gate Q1
- 3) Drain Q2
- 4) Source Q2
- 5) Gate Q2
- 6) Drain Q1

MARKING CODE: 02J

* Device is **Halogen Free** by design

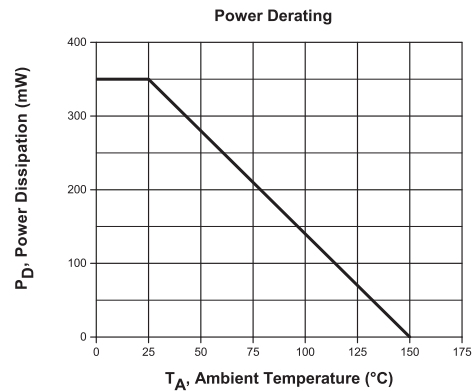
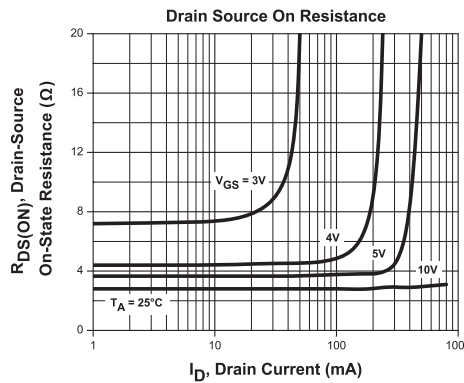
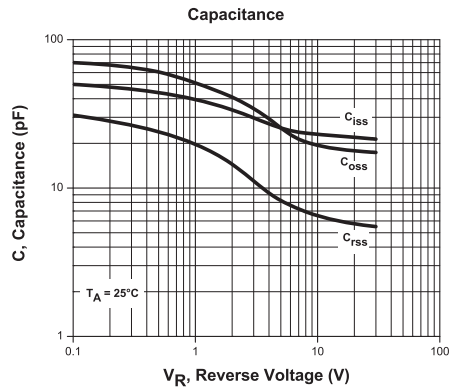
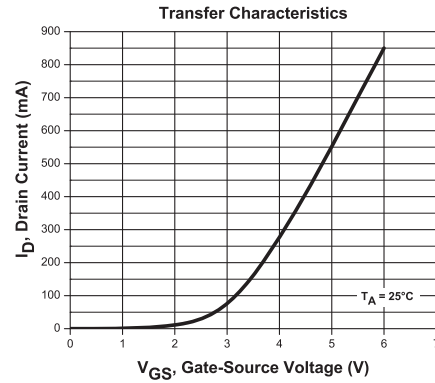
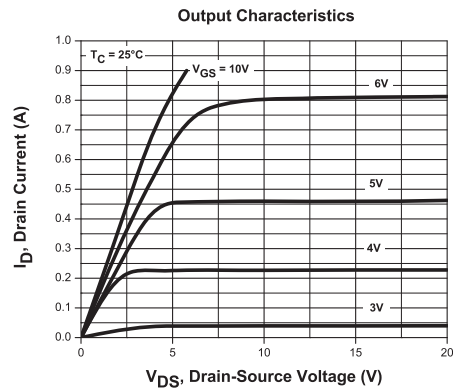
R8 (8-June 2015)

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SURFACE MOUNT SILICON
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TYPICAL ELECTRICAL CHARACTERISTICS



R8 (8-June 2015)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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