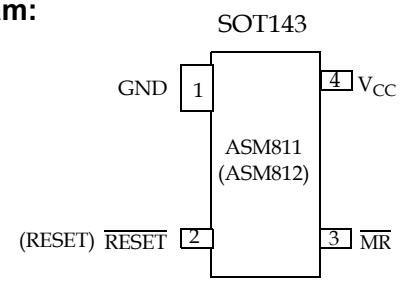
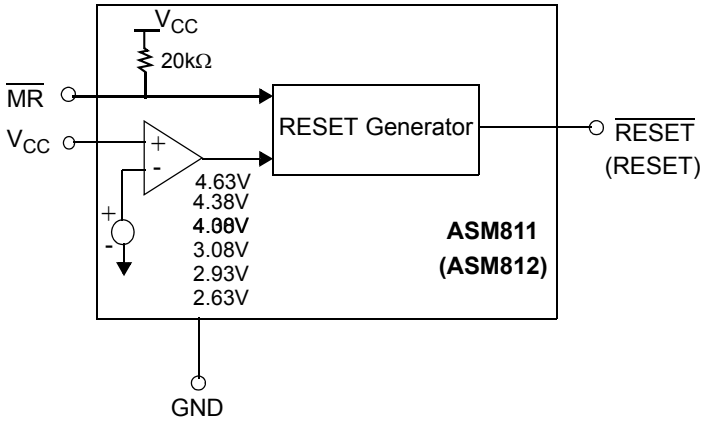
rev 1.5

Pin Diagram:



Block Diagram



Pin Description

Pin #		Pin Name	Function
ASM811	ASM812		
1	1	GND	Ground.
2	-	$\overline{RESET}$	$\overline{RESET}$ is asserted LOW if V_{CC} falls below V_{TH}. $\overline{RESET}$ remains LOW for atleast 140ms (T_{RST}) once V_{CC} exceeds the Threshold. In addition, $\overline{RESET}$ is active LOW as long as the manual reset ($\overline{MR}$) is low.
-	2	RESET	RESET is asserted HIGH if V_{CC} falls below V_{TH}. RESET remains HIGH for atleast 140ms (T_{RST}) once V_{CC} exceeds the threshold. In addition, RESET is active HIGH as long as the manual reset ($\overline{MR}$) is low.
3	3	$\overline{MR}$	Manual Reset Input. A logic LOW on $\overline{MR}$ asserts reset. Reset remains active as long as $\overline{MR}$ is LOW and for atleast 180ms (T_{MRST}) once $\overline{MR}$ returns HIGH. The active low input has an internal 20kΩ pull-up resistor. The input should be left open if not used. It can be driven by TTL or CMOS logic or shorted to ground by a switch.
4	4	V_{CC}	Power supply input voltage (3.0V, 3.3V, 5.0V)

Detailed Description

A proper reset input enables a microprocessor / microcontroller to start in a known state. ASM811/812 assert reset to prevent code execution errors during power-up, power-down and brown-out conditions.

Reset Timing

The reset signal is asserted- LOW for the ASM811 and HIGH for the ASM812- when the V_{CC} supply voltage falls below the threshold trip voltage and remains asserted for 140ms minimum after the V_{CC} has risen above the threshold.

Manual Reset ($\overline{MR}$) Input

A logic low on $\overline{MR}$ assserts $\overline{RESET}$ LOW on the ASM811 and RESET HIGH on the ASM812. $\overline{MR}$ is internally pulled high through a 20kΩ resistor and can be driven by TTL/CMOS gates or with open collector/drain outputs. $\overline{MR}$ can be left open if not used. $\overline{MR}$ may be connected to ground through a normally-open momentary switch without an external debounce circuit.

A 0.1µF capacitor from $\overline{MR}$ to ground can be added for additional noise immunity.

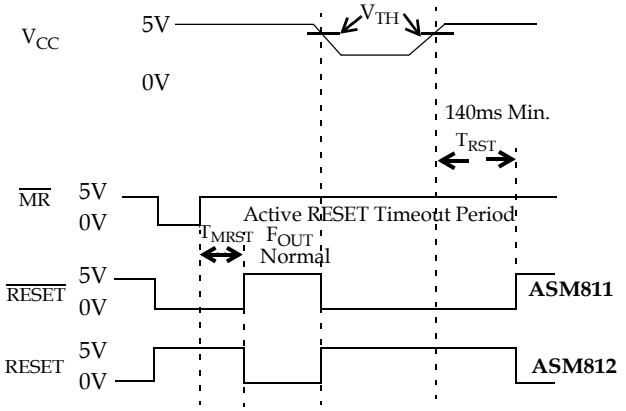


Figure 1: Reset Timing and Manual Reset ($\overline{MR}$)

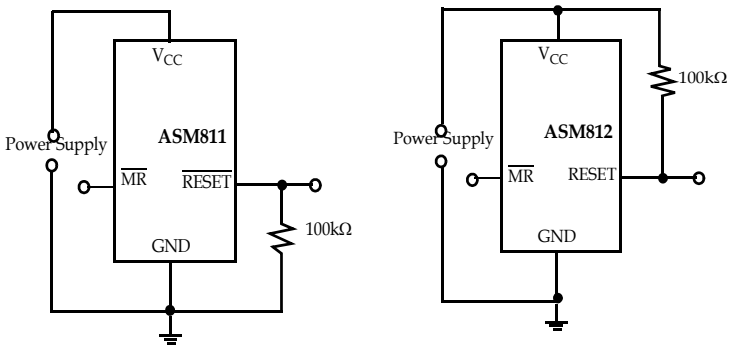
Reset Output Operation

In µP / µC systems it is important to have the processor and the system begin operation from a known state. A reset output to a processor is provided to prevent improper operation during power supply sequencing or low voltage brown-out conditions.

The ASM811/812 are designed to monitor the system power supply voltages and issue a reset signal when the levels are out of range. RESET outputs are guaranteed to be active for V_{CC} above 1.1V. When V_{CC} exceeds the reset threshold, an internal timer keeps RESET active for the reset timeout period, after which RESET becomes inactive (HIGH for the ASM811 and LOW for the ASM812). If V_{CC} drops below the reset threshold, RESET automatically becomes active. Alternatively, external circuitry or an operator can initiate this condition using the Manual Reset ($\overline{MR}$) pin. $\overline{MR}$ can be left open if it is not used. $\overline{MR}$ can be driven by TTL/CMOS logic or even an external switch.

Valid Reset with V_{CC} under 1.1V

To ensure logic inputs connected to the ASM811 $\overline{RESET}$ pin are in a known state when V_{CC} is under 1.1V, a 100kΩ pull-down resistor at $\overline{RESET}$ is needed. The value is not critical. A 100kΩ pull-up resistor to V_{CC} at RESET is needed with the ASM812.



Figures 2 & 3: RESET valid with V_{CC} under 1.1V

Application Information

Negative VCC Transients

Typically short duration transients of 100mV amplitude and 60µs duration do not cause a false RESET. A 0.1µF capacitor at V_{CC} increases transient immunity.

Bidirectional Reset Pin Interfacing

The ASM811/812 can interface with µP / µC bi-directional reset pins by connecting a 4.7kΩ resistor in series with the ASM811/812 reset output and the µP/µC bi-directional reset input pin.

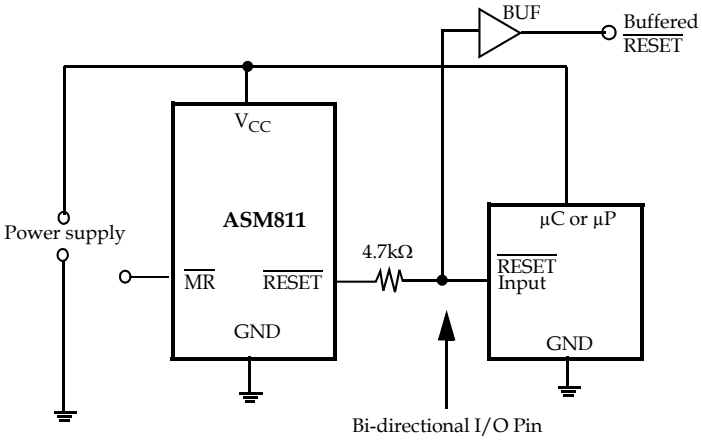


Figure 4: Bi-directional Reset Pin Interface

Absolute Maximum Ratings, Table 1:

Parameter	Min	Max	Units
Pin Terminal Voltage With Respect To Ground			
V_{CC}	-0.3	6.0	V
RESET, $\overline{\text{RESET}}$ and $\overline{\text{MR}}$	-0.3	V_{CC} + 0.3	V
Input current at V_{CC} and $\overline{\text{MR}}$		20	mA
Output current: RESET, $\overline{\text{RESET}}$		20	mA
Rate of Rise at V_{CC}		100	V/µs
ESD rating HBM MM		 2 200	 KV V
Note: These are stress ratings only and the functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability.			

Absolute Maximum Ratings, Table 2:

Parameter	Min	Max	Units
Power Dissipation (T_A = 70°C) Derate SOT-143 4mW/°C above 70°C		320	mW
Operating temperature range	-40	105	°C
Storage temperature range	-65	160	°C
Lead temperature (Soldering, 10 sec)		300	°C
Note: These are stress ratings only and the functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability.			

Electrical Characteristics:

Unless otherwise noted, V_{CC} is over the full voltage range, T_A = -40°C to 105°C.
Typical values at T_A = 25°C, V_{CC} = 5V for L/M/J devices, V_{CC} = 3.3V for T/S devices and V_{CC} = 3V for R devices.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CC}	Input Voltage Range	T_A = 0°C to 70°C T_A = -40°C to 105°C		1.1 1.2		5.5 5.5	V V
I_{CC}	Supply Current (Unloaded)	T_A= -40°C to 85°C T_A = -40°C to 85°C T_A = 85°C to 105°C T_A = 85°C to 105°C	V_{CC} < 5.5V, L/M/J V_{CC} < 3.6V, R/S/T V_{CC} < 5.5V, L/M/J V_{CC} < 3.6V, R/S/T		6 5	15 10 25 20	µA
V_{TH}	Reset Threshold	L devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	4.56 4.50 4.40	4.63	4.70 4.75 4.86	V
		M devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	4.31 4.25 4.16	4.38	4.45 4.50 4.56	
		J devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	3.93 3.89 3.80	4.00	4.06 4.10 4.20	
		T devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	3.04 3.00 2.92	3.08	3.11 3.15 3.23	
		S devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	2.89 2.85 2.78	2.93	2.96 3.00 3.08	
		R devices	T_A = 25°C T_A = -40°C to 85°C T_A = 85°C to 105°C	2.59 2.55 2.50	2.63	2.66 2.70 2.76	
TC_{VTH}	Reset Threshold Temp. Coefficient				30		ppm/°C
	V_{CC} to Reset Delay	V_{CC} = V_{TH} to (V_{TH} - 125mV),			60		µs
T_{RST}	Reset Active Timeout Period	T_A = 0°C to 70°C		140	240	560	ms
		T_A = -40°C to 105°C		100		840	
t_{MR}	$\overline{MR}$ Minimum Pulse Width			10			µs
	$\overline{MR}$ Glitch Immunity	Note 3			100		ns

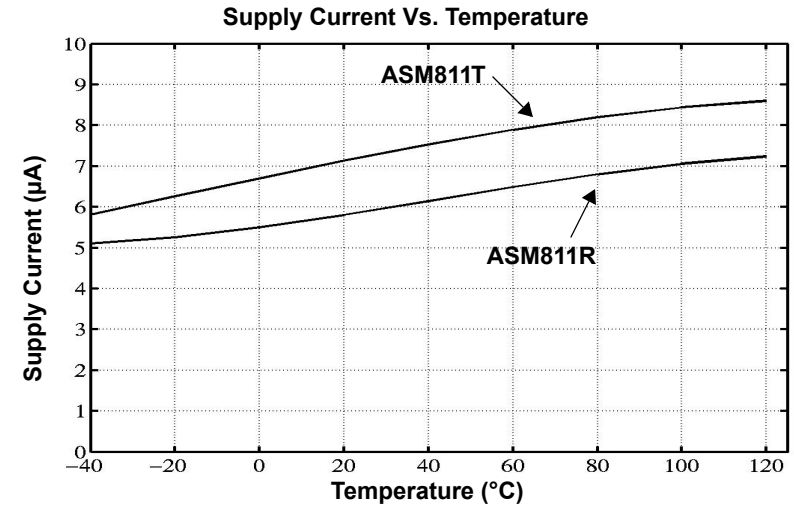
Notes:
1. Production testing done at TA = 25°C. Over-temperature specifications guaranteed by design only using six sigma design limits.
2. $\overline{RESET}$ output is active LOW for the ASM811 and RESET output is active HIGH for the ASM812.
3. Glitches of 100ns or less typically will not generate a reset pulse.

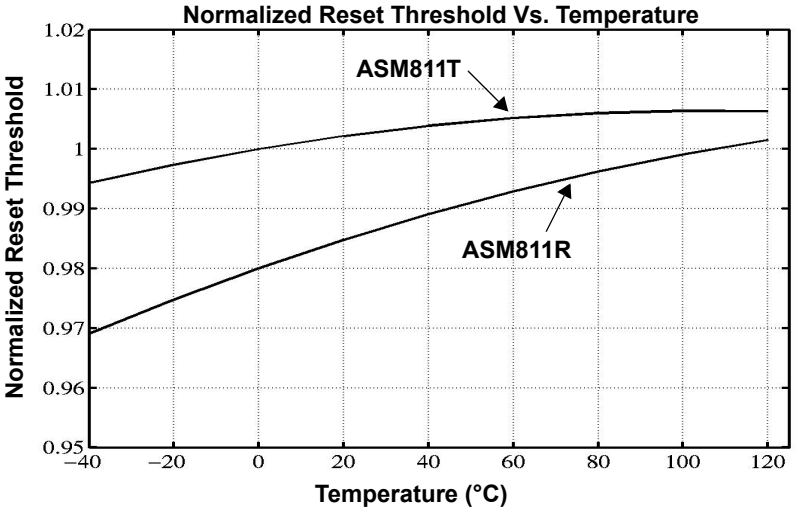
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_{MD}	$\overline{MR}$ to RESET Propagation Delay	Note 2		0.5		µs
V_{IH}	$\overline{MR}$ Input Threshold	$V_{CC} > V_{TH}$ (MAX), ASM811/812L/M/J	2.3			V
V_{IL}					0.8	
V_{IH}	$\overline{MR}$ Input Threshold	$V_{CC} > V_{TH}$ (MAX), ASM811/812R/S/T	$0.7V_{CC}$			V
V_{IL}					$0.25V_{CC}$	
	$\overline{MR}$ Pullup Resistance		10	20	30	kΩ
V_{OL}	Low $\overline{RESET}$ Output Voltage (ASM811)	$V_{CC}= V_{TH}$ min., I_{SINK} = 1.2mA, ASM811R/S/T			0.3	V
		$V_{CC}= V_{TH}$ min., I_{SINK} = 3.2mA, ASM811L/M/J			0.4	
		V_{CC} > 1.1V, I_{SINK} = 50µA			0.3	
V_{OH}	High $\overline{RESET}$ Output Voltage (ASM811)	$V_{CC} > V_{TH}$ max., I_{SOURCE} = 500µA, ASM811R/S/T	$0.8V_{CC}$			V
		$V_{CC} > V_{TH}$ max., I_{SOURCE} = 800µA, ASM811L/M/J	V_{CC} - 1.5			
V_{OL}	Low RESET Output Voltage (ASM812)	$V_{CC}= V_{TH}$ max., I_{SINK} = 1.2mA, ASM812R/S/T			0.3	V
		$V_{CC}= V_{TH}$ max., I_{SINK} = 3.2mA, ASM812L/M/J			0.4	
V_{OH}	High RESET Output Voltage (ASM812)	1.8V < V_{CC} < V_{TH} min., I_{SOURCE} = 150µA	$0.8V_{CC}$			V

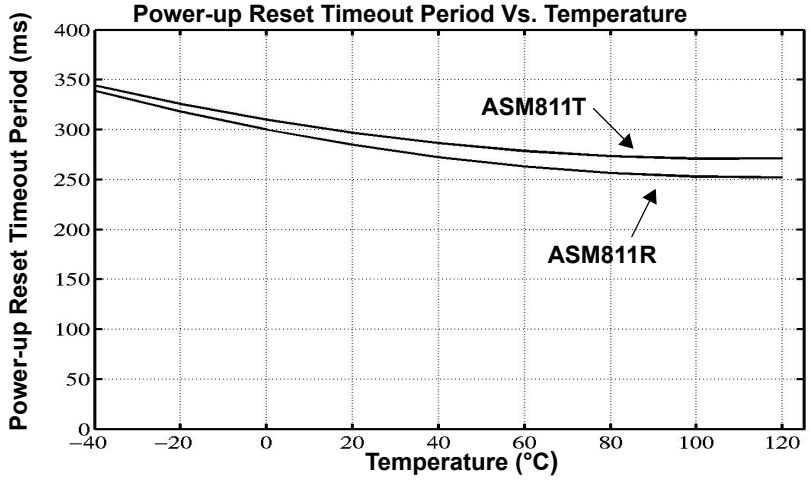
Notes:
1. Production testing done at TA = 25°C. Over-temperature specifications guaranteed by design only using six sigma design limits.
2. $\overline{RESET}$ output is active LOW for the ASM811 and RESET output is active HIGH for the ASM812.
3. Glitches of 100ns or less typically will not generate a reset pulse.

Typical Operating Characteristics

Unless otherwise noted, V_{CC} is over the full voltage range, T_A = -40°C to 105°C. Typical values at T_A = 25°C, V_{CC} = 5V for L/M/J devices, V_{CC} = 3.3V for T/S devices and V_{CC} = 3V for R devices.



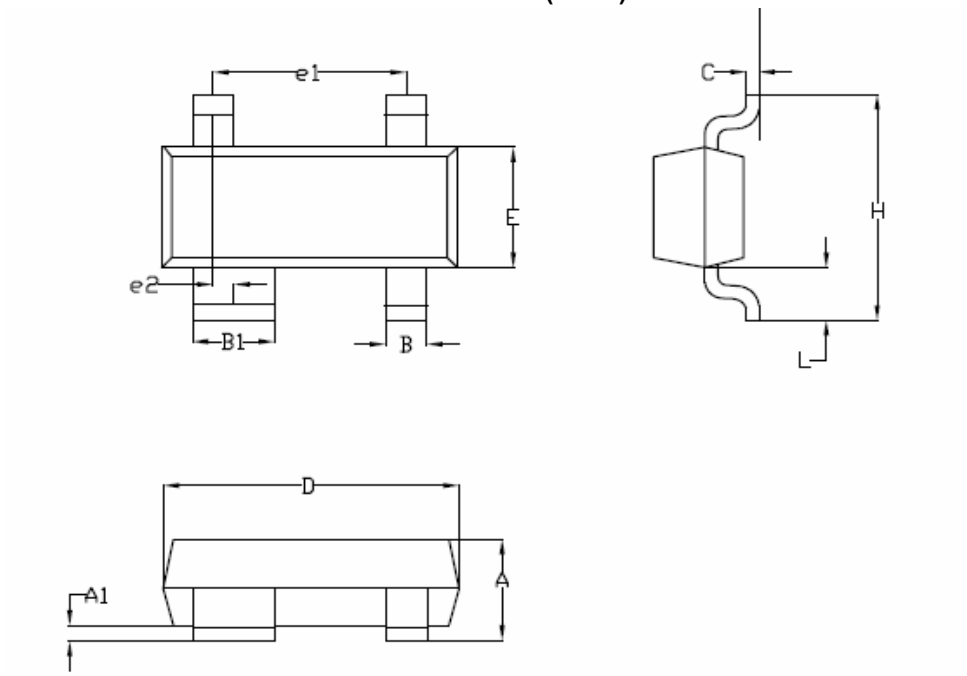




rev 1.5

Package Dimensions:

Plastic SOT-143 (4-Pin)



Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.031	0.048	0.80	1.22
A1	0.002	0.006	0.05	0.15
B	0.012	0.020	0.30	0.50
B1	0.030	0.035	0.76	0.89
C	0.003	0.008	0.08	0.20
D	0.110	0.120	2.80	3.04
E	0.047	0.055	1.20	1.40
e1	0.075BSC		1.92 BSC	
e2	0.181 BSC		4.60 BSC	
H	0.083	0.104	2.10	2.64
L	0.016	0.024	0.400	0.600

Ordering Information

Part Number	Reset Threshold (V)	Temperature Range	Pin-Package	Package Marking (LL Lot Code)
ASM811 ACTIVE LOW RESET, TIN-LEAD PLATED DEVICES				
ASM811LEUS	4.63	-40°C to +105°C	4-SOT143	SMLL
ASM811MEUS	4.38	-40°C to +105°C	4-SOT143	SNLL
ASM811JEUS	4.00	-40°C to +105°C	4-SOT143	SOLL
ASM811TEUS	3.08	-40°C to +105°C	4-SOT143	SPLL
ASM811SEUS	2.93	-40°C to +105°C	4-SOT143	SQLL
ASM811REUS	2.63	-40°C to +105°C	4-SOT143	SRLL
ASM812 ACTIVE HIGH RESET, TIN-LEAD PLATED DEVICES				
ASM812LEUS	4.63	-40°C to +105°C	4-SOT143	SSLL
ASM812MEUS	4.38	-40°C to +105°C	4-SOT143	STLL
ASM812JEUS	4.00	-40°C to +105°C	4-SOT143	SULL
ASM812TEUS	3.08	-40°C to +105°C	4-SOT143	SVLL
ASM812SEUS	2.93	-40°C to +105°C	4-SOT143	SWLL
ASM812REUS	2.63	-40°C to +105°C	4-SOT143	SXLL
ASM811 ACTIVE LOW RESET, LEAD FREE DEVICES				
ASM811LEUSF	4.63	-40°C to +105°C	4-SOT143	NMLL
ASM811MEUSF	4.38	-40°C to +105°C	4-SOT143	NNLL
ASM811JEUSF	4.00	-40°C to +105°C	4-SOT143	NOLL
ASM811TEUSF	3.08	-40°C to +105°C	4-SOT143	NPLL
ASM811SEUSF	2.93	-40°C to +105°C	4-SOT143	NQLL
ASM811REUSF	2.63	-40°C to +105°C	4-SOT143	NRLL
ASM812 ACTIVE HIGH RESET, LEAD FREE DEVICES				
ASM812LEUSF	4.63	-40°C to +105°C	4-SOT143	NSLL
ASM812MEUSF	4.38	-40°C to +105°C	4-SOT143	NTLL
ASM812JEUSF	4.00	-40°C to +105°C	4-SOT143	NULL
ASM812TEUSF	3.08	-40°C to +105°C	4-SOT143	NVLL
ASM812SEUSF	2.93	-40°C to +105°C	4-SOT143	NWLL
ASM812REUS	2.63	-40°C to +105°C	4-SOT143	NXLL

Notes:
- For parts to be packed in Tape and Reel, add “-T” at the end of the part number.
- Alliance Semiconductor's lead free parts are RoHS compliant. All parts are Lead Free by default. Contact factory for Non Lead Free devices

Related Products:

	ASM809	ASM810	ASM811	ASM812
Max Supply Current	15µA	15µA	15µA	15µA
Package Pins	3	3	4	4
Manual RESET input			■	■
Package Type	SOT - 23	SOT - 23	SOT - 143	SOT - 143
Active-HIGH RESET Output		■		■
Active-LOW $\overline{\text{RESET}}$ Output	■		■	

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Document Version: v 1.5