Typical Applications Circuit
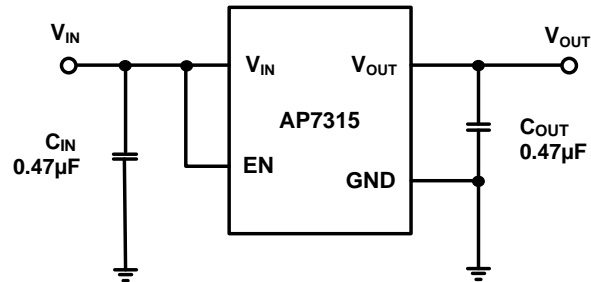
Pin Descriptions

Pin Name	Pin Number				Function
	X2-DFN1010-4	SOT25	SOT23 (SA)	SOT23 (SR)	
V$_{OUT}$	1	5	2	2	Power output pin
GND	2	2	3	1	Ground
EN	3	3	—	—	Enable Pin This pin should be driven either high or low and must not be floating. Driving this pin high enables the regulator, while pulling it low puts the regulator into shutdown mode
V$_{IN}$	4	1	1	3	Power input pin
—	Thermal PAD	—	—	—	In PCB layout, prefer to use large copper area to cover this pad for better thermal dissipation, then connect this area to GND or leave it open. However, do not use it as GND electrode function alone.
NC	—	4	—	—	Not Connected internally. Recommend connection to GND to maximize PCB copper for thermal dissipation.

Functional Block Diagram

AP7315 (Non Discharge)

AP7315D (With Discharge)

AP7315
Document number: DS38352 Rev. 2 - 2

2 of 23
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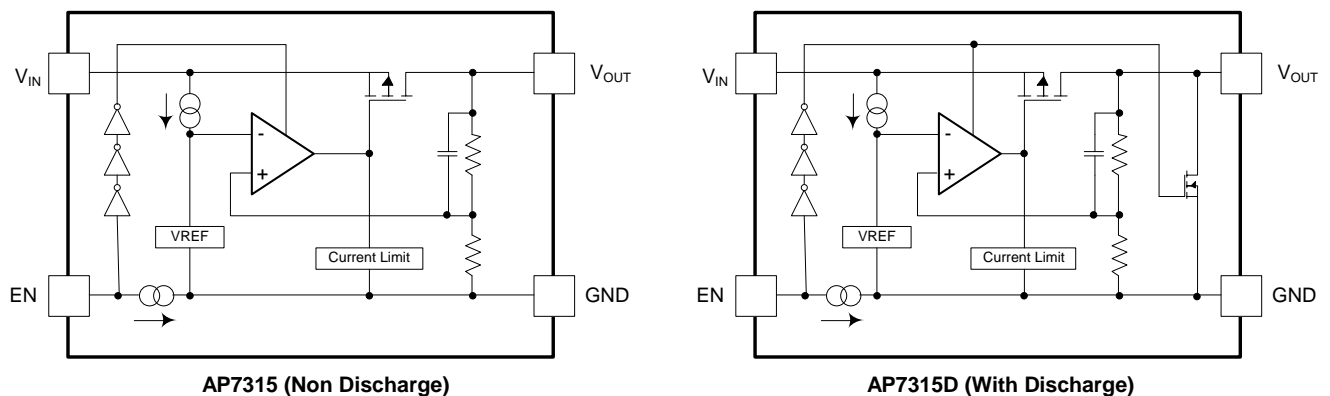
Absolute Maximum Ratings (Note 4) (@ T$_A$ = +25°C, unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
V$_{IN}$	Input Voltage	6.0	V
V$_{EN}$	Input Voltage EN	6.0	V
V$_{OUT}$	Output Voltage	-0.3 to V$_{IN}$ +0.3	V
I$_{OUT}$	Output Current	300	mA
P$_D$	Power Dissipation	400	mW
T$_A$	Operating Ambient Temperature	-40 to +85	°C
T$_{STG}$	Storage Temperature	-55 to +125	°C

Note: 4. a). Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
b). Ratings apply to ambient temperature at +25°C. The JEDEC High-K board design used to derive this data is a 2in. x 2in. multi-layer board with 1oz. internal power and ground planes and 2oz. copper traces on the top and bottom of the board.

Recommended Operating Conditions (@ T$_A$ = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V$_{IN}$	Input Voltage	1.7	5.25	V
I$_{OUT}$	Output Current	0	150	mA
T$_A$	Operating Ambient Temperature	-40	+85	°C

AP7315
Document number: DS38352 Rev. 2 - 2
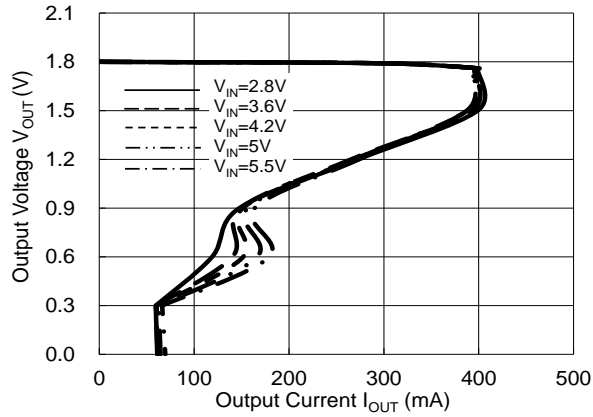
3 of 23
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October 2021
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Electrical Characteristics $(V_{EN} = V_{IN} = V_{OUT} +1.0V, C_{IN} = C_{OUT} = 0.47\mu F, I_{OUT} = 1.0mA @ T_A = +25°C$, unless otherwise specified.)

| Parameter	Conditions		Min	Typ	Max	Units
Input Voltage	T_A = -40°C to +85°C		1.7	—	5.25	V
Output Voltage Accuracy (Note 11)	$V_{OUT} \geq 2.0V$, $V_{IN} = V_{OUT}+1V$, $T_A = +25°C$		V_{OUT} *0.99	V_{OUT}	V_{OUT} *1.01	V
	$V_{OUT} < 2.0V$, $V_{IN} = V_{OUT}+1V$, $T_A = +25°C$		V_{OUT} - 20mV	V_{OUT}	V_{OUT} + 20mV	V
Line Regulation ($dV_{OUT}/dV_{IN}/V_{OUT}$)	$V_{IN} = (V_{OUT – Nom} + 1.0V)$ to 5.25V, $I_{OUT} = 1.0mA$		—	0.02	0.1	%/V
Load Regulation ($dV_{OUT}/V_{OUT}/dI_{OUT}$)	$V_{IN} = V_{OUT - Nom} + 1.0V$, $I_{OUT} = 1mA$ to 150mA		—	0.5	1.0	%/A
Quiescent Current (Note 6)	$I_{OUT} = 0mA$		20	35	60	μA
	$I_{OUT} = 150mA$		30	60	100	
$I_{STANDBY}$	$V_{EN} = 0V$ (Disabled)		—	0.01	1.0	μA
Output Current	—		150	—	—	mA
Fold-back Short Current (Note 7)	V_{OUT} Short to Ground		—	55	—	mA
PSRR (Note 8)	$V_{IN} = (V_{OUT}+1V)V_{DC} + 0.2Vp-pAC$, $V_{OUT} \geq 1.8V$, $I_{OUT} = 30mA$	f = 1kHz	—	75	—	dB
Output Noise Voltage (Notes 8, 9)	BW = 10Hz to 100kHz, $I_{OUT} = 30mA$		—	60	—	μVrms
Dropout Voltage (Note 5)	$I_{OUT} = 150mA$	$1.1V \leq V_{OUT} < 1.5V$	—	0.50	0.64	V
		$1.5V \leq V_{OUT} < 1.7V$	—	0.38	0.49	
		$1.7V \leq V_{OUT} < 2.0V$	—	0.34	0.44	
		$2.0V \leq V_{OUT} < 2.5V$	—	0.28	0.38	
		$2.5V \leq V_{OUT} < 2.8V$	—	0.22	0.32	
		$2.8V \leq V_{OUT} \leq 3.3V$	—	0.21	0.29	
Output Voltage Temperature Coefficient	$I_{OUT} = 30mA$, T_A = -40°C to +85°C		—	±30	—	ppm/°C
EN Input Low Voltage	—		0	—	0.5	V
EN Input High Voltage	—		1.3	—	5.25	V
EN Input Leakage	$V_{EN} = 0$, $V_{IN} = 5.0V$ or $V_{EN} = 5.0V$, $V_{IN} = 0V$		-1.0	—	+1.0	μA
On Resistance of N-channel for Auto-Discharge (Note 10)	$V_{IN} = 4.0V$ $V_{EN} = 0V$ (Disabled)		—	30	—	Ω

Notes: 5. Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.
6. Quiescent current is defined here is the difference in current between the input and the output.
7. Short circuit current is measured with V_{OUT} pulled to GND.
8. This specification is guaranteed by design.
9. To make sure lowest environment noise minimizes the influence on noise measurement.
10. AP7315 has 2 options for output, built-in discharge and non-discharge.
11. Potential multiple grades based on following output voltage accuracy.

AP7315
Document number: DS38352 Rev. 2 - 2

4 of 23
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October 2021
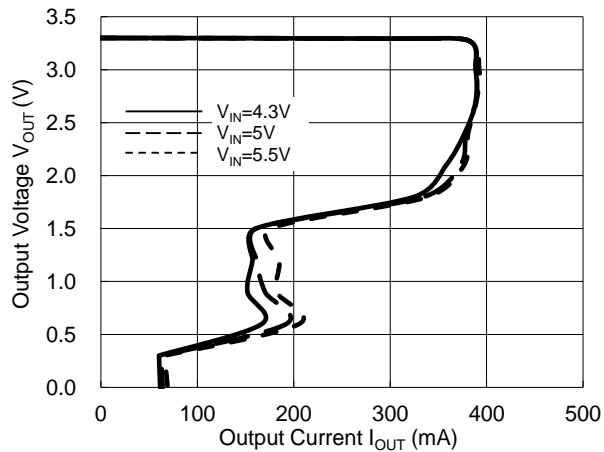© Diodes Incorporated

Typical Characteristics

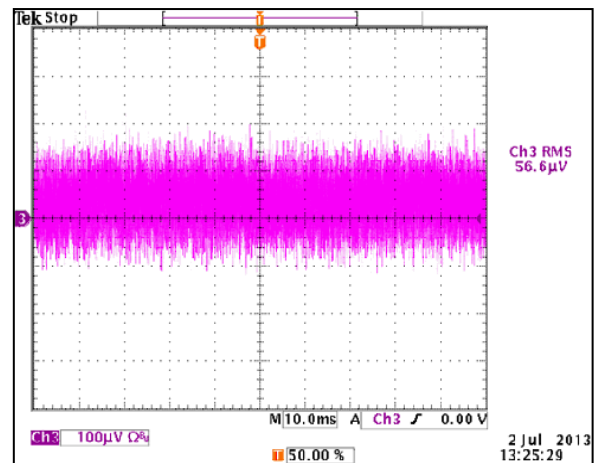**Output Voltage vs. Output Current**
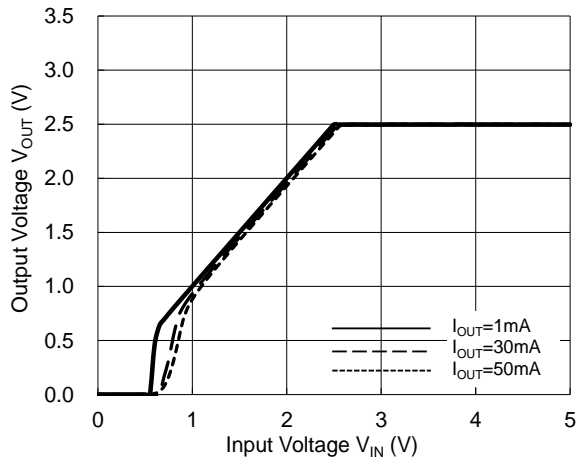AP7315-18

Output Voltage vs. Output Current
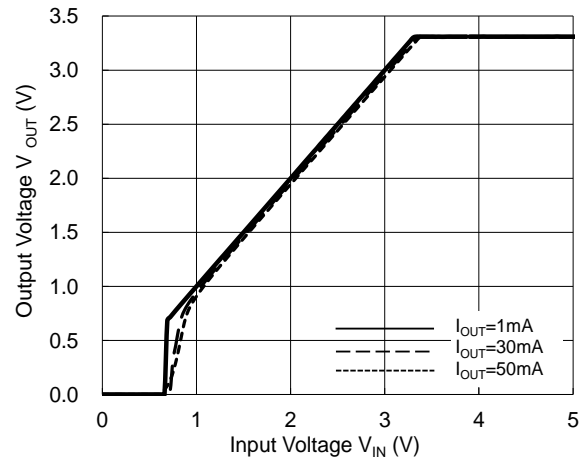AP7315-25
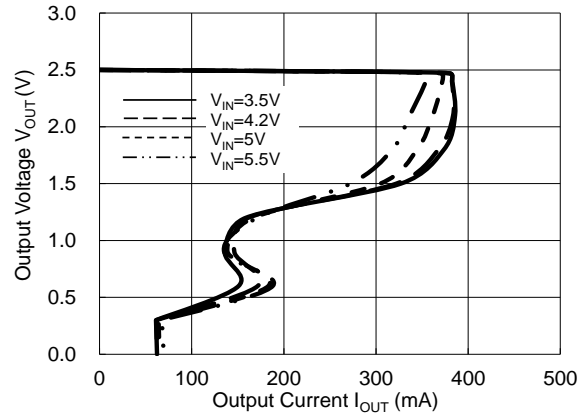
Output Voltage vs. Output Current
AP7315-33

Output Noise (AP7315-33)

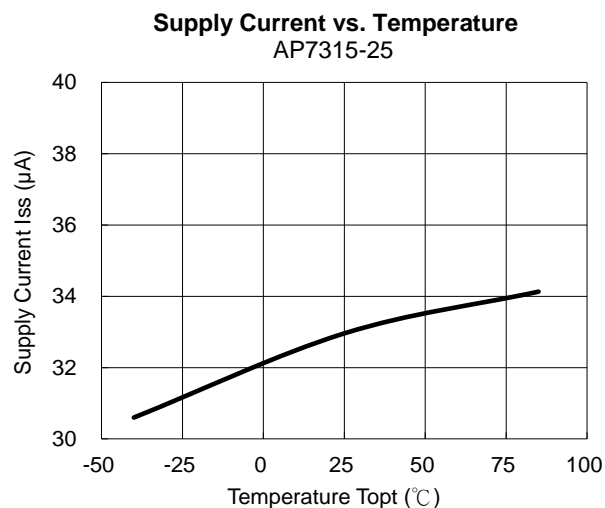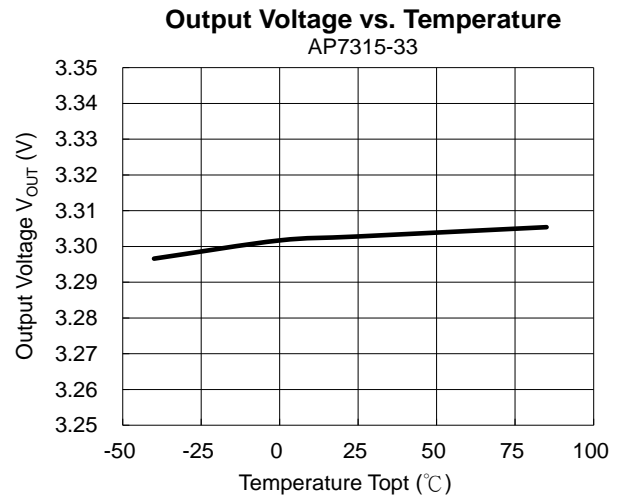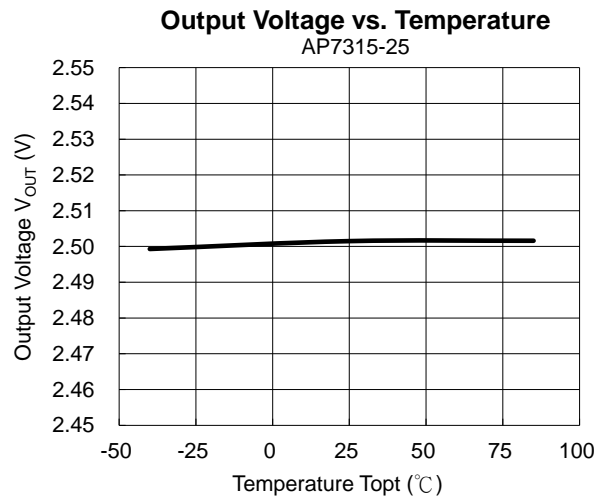**Output Voltage vs. Intput Voltage**
AP7315-25

Output Voltage vs. Intput Voltage
AP7315-33

AP7315
Document number: DS38352 Rev. 2 - 2

5 of 23
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Typical Characteristics (Continued)

Supply Current vs. Input Voltage
AP7315-25

Supply Current vs. Input Voltage
AP7315-33

Output Voltage vs. Temperature
AP7315-25

Output Voltage vs. Temperature
AP7315-33

Supply Current vs. Temperature
AP7315-25

Supply Current vs. Temperature
AP7315-33

AP7315
Document number: DS38352 Rev. 2 - 2

6 of 23
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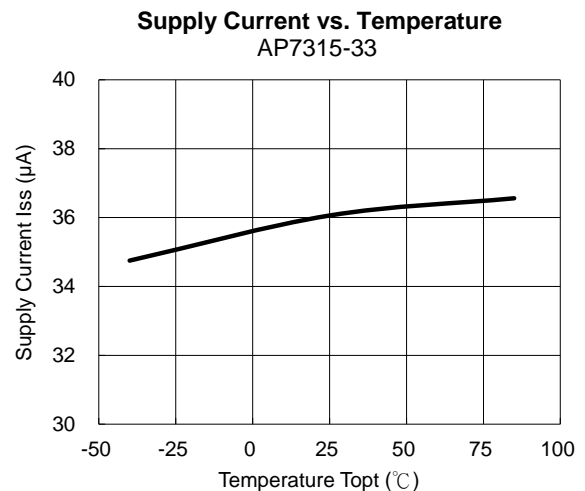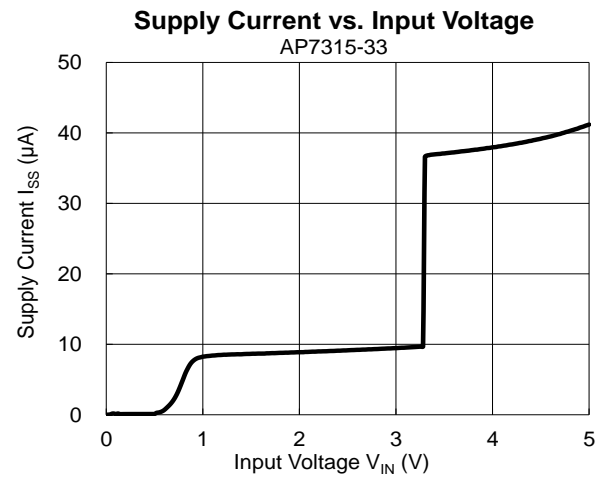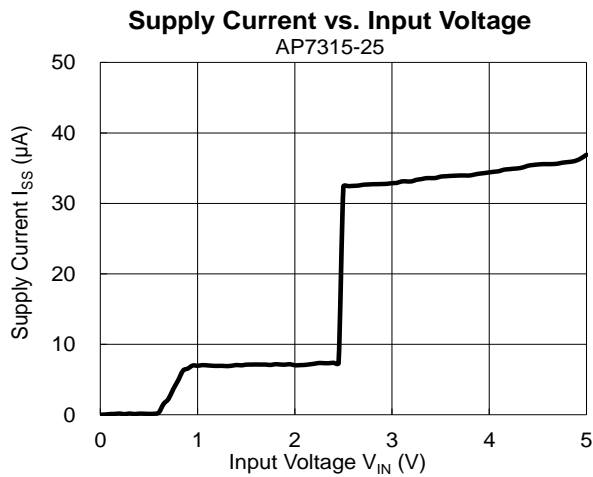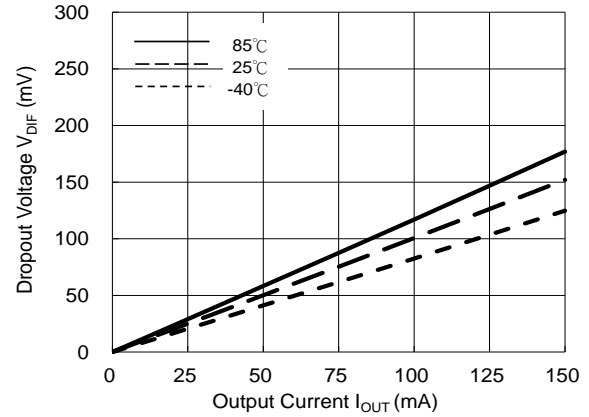
Typical Characteristics (Continued)

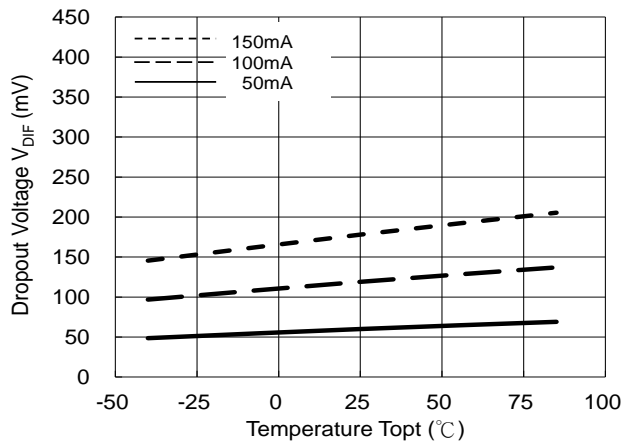**Dropout Voltage vs. Output Current**
AP7315-25
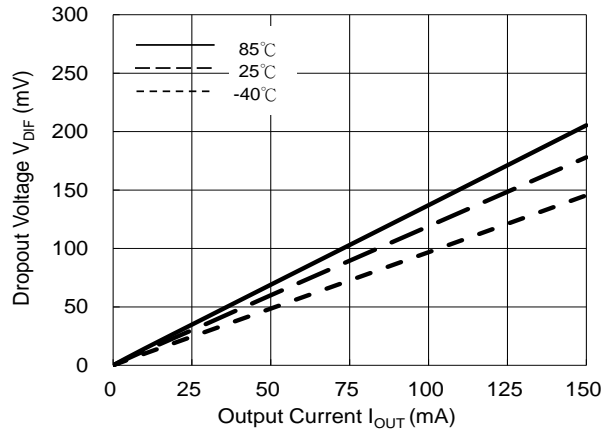
Dropout Voltage vs. Output Current
AP7315-33

Dropout Voltage vs. Temperature
AP7315-25

Dropout Voltage vs. Temperature
AP7315-33
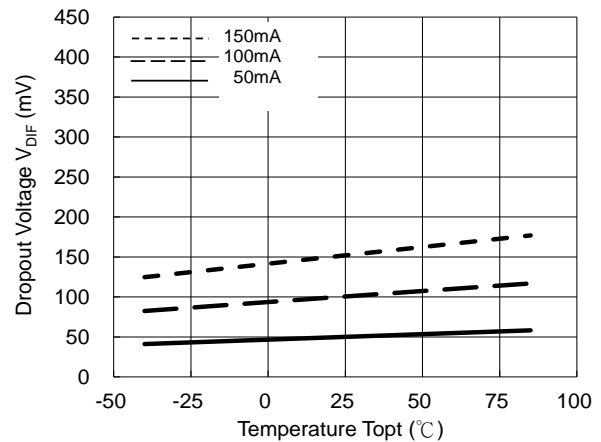
Ripple Rejection vs. Frequency
AP7315-18

Ripple Rejection vs. Frequency
AP7315-25

AP7315
Document number: DS38352 Rev. 2 - 2

7 of 23
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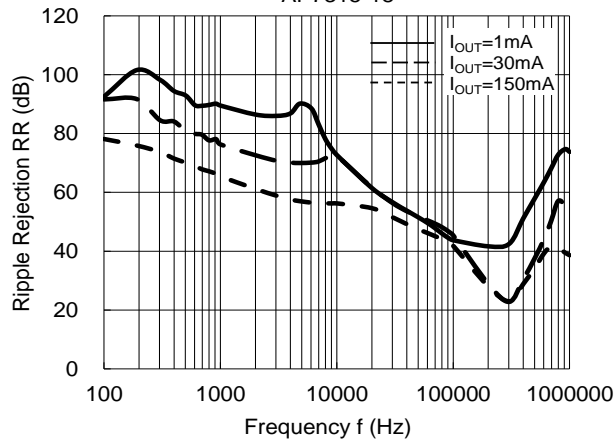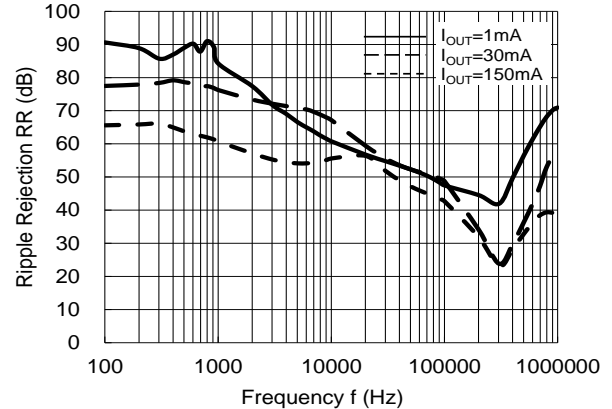
Typical Characteristics (Continued)

Ripple Rejection vs. Frequency
AP7315-33

AP7315-25

AP7315-33

AP7315-25

AP7315-33

AP7315
Document number: DS38352 Rev. 2 - 2

8 of 23
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Typical Characteristics (Continued)

AP7315-25 V_{IN}=3.5V   tr=tf=0.5µs

Output Current 1mA ↔ 150mA

Output Voltage

AP7315-33 V_{IN}=4.3V   tr=tf=0.5µs

Output Current 1mA ↔ 150mA

Output Voltage

AP7315-25 I_{OUT}=0mA

CE Input Voltage

Output Voltage

AP7315-25 I_{OUT}=150mA

CE Input Voltage

Output Voltage

AP7315-33 I_{OUT}=0mA

CE Input Voltage

Output Voltage

AP7315-33 I_{OUT}=150mA

CE Input Voltage

Output Voltage

AP7315
Document number: DS38352 Rev. 2 - 2

9 of 23
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Typical Characteristics (Continued)

AP7315D-18 I_{OUT}=0mA

AP7315D-18 I_{OUT}=150mA

AP7315D-33 I_{OUT}=0mA

AP7315D-33 I_{OUT}=150mA

AP7315
Document number: DS38352 Rev. 2 - 2

10 of 23
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Application Information

Output Capacitor

An output capacitor (C_{OUT}) is needed to improve transient response and maintain stability. The AP7315 is stable with very small ceramic output capacitors. The ESR (equivalent series resistance) and capacitance drives the selection. If the application has large load variations, it is recommended to utilize low-ESR bulk capacitors. It is recommended to place ceramic capacitors as close as possible to the load and the ground pin and care should be taken to reduce the impedance in the layout.

Input Capacitor

To prevent the input voltage from dropping during load steps, it is recommended to utilize an input capacitor (C_{IN}). A minimum 0.47µF ceramic capacitor is recommended between V_{IN} and GND pins to decouple input power supply glitch. This input capacitor must be located as close as possible to the device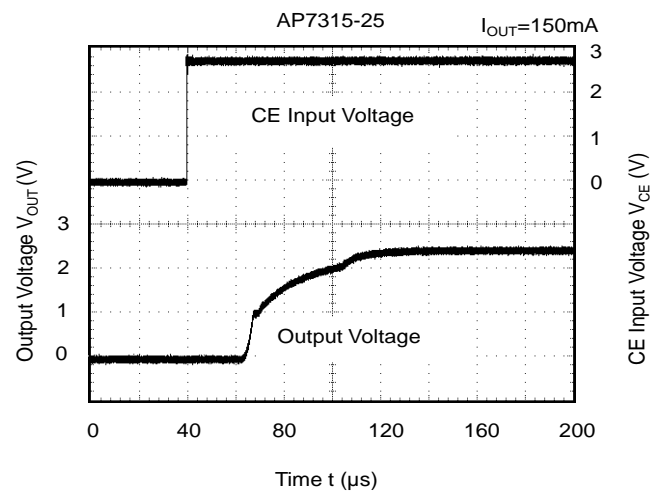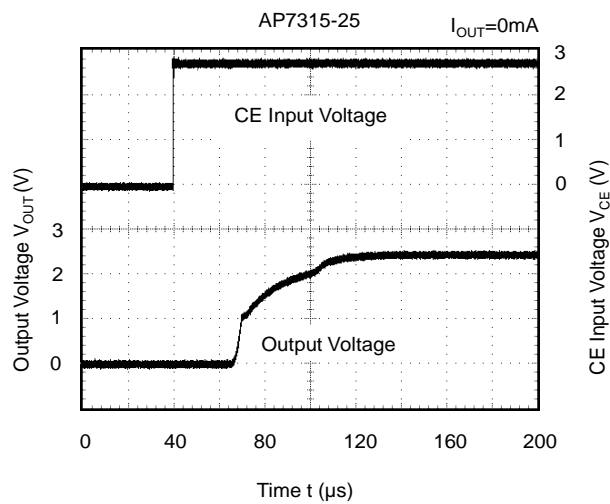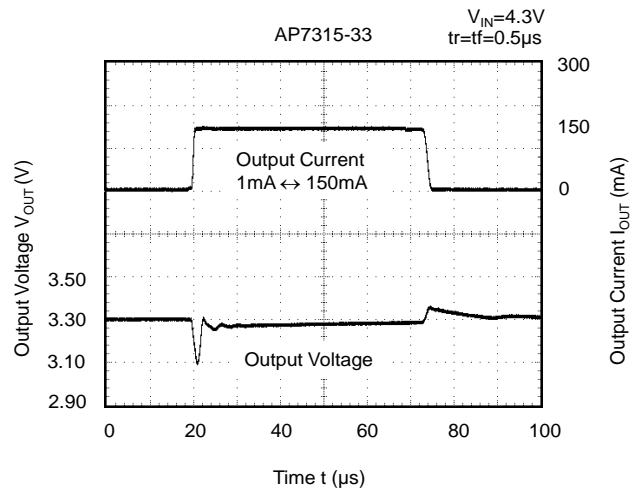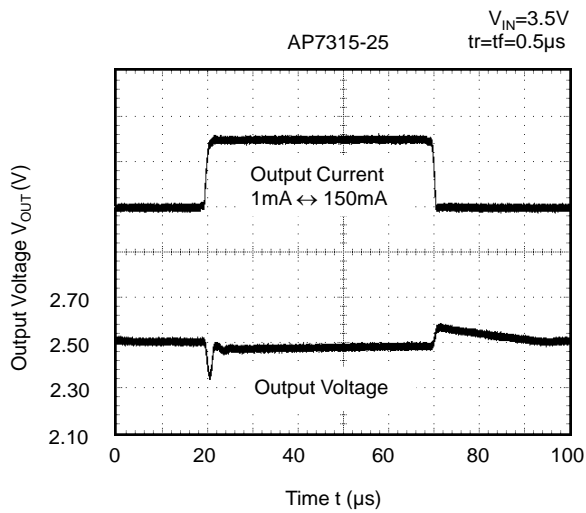 to assure input stability and reduce noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND pins.

Enable Control

The AP7315 is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to V_{IN} pin to keep the regulator output on at all time. To ensure proper operation, the signal source used to drive the EN pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section.

Short Circuit Protection

When V_{OUT} pin is short-circuit to GND, short circuit protection will be triggered and clamp the output current to approximately 60mA. This feature protects the regulator from overcurrent and damage due to overheating.

Layout Considerations

For good ground loop and stability, the input and output capacitors should be located close to the V_{IN}, V_{OUT}, and GND pins of the device. The regulator GND pin should be connected to the external circuit ground to reduce voltage drop caused by trace impedance. Ground plane is generally used to reduce trace impedance. Wide trace should be used for large current paths from V_{IN} to V_{OUT}, and load circuit.

ESR vs. Output Current

Ceramic type output capacitor is recommended for this series; however, the other output capacitors with low ESR also can be used. The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The stable region is marked as the hatched area in the graph.

Measurement conditions:

Frequency Band: 10Hz to 2MHz, Temperature: -40°C to +85°C

AP7315
Document number: DS38352 Rev. 2 - 2

11 of 23
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October 2021
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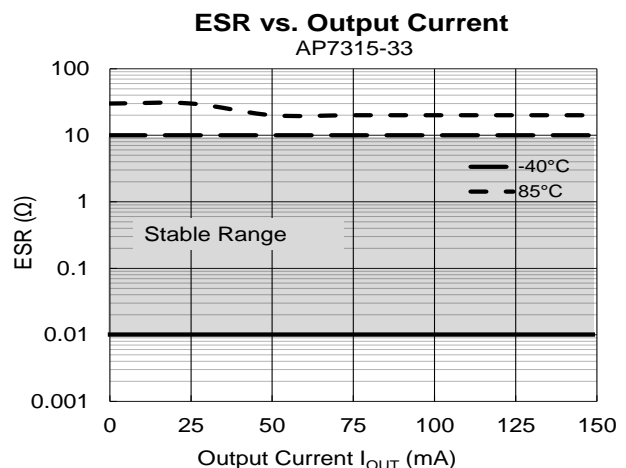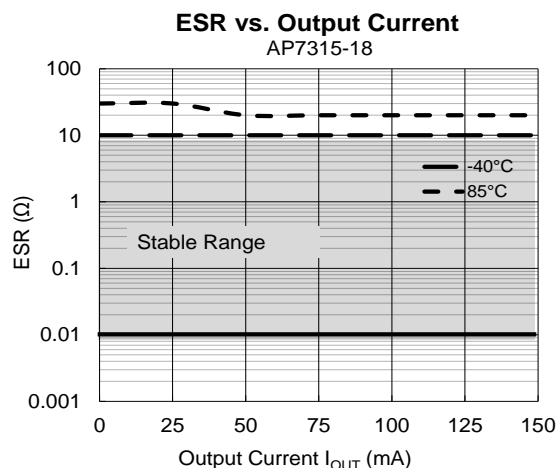
Ordering Information (Note 12)

AP7315X - XXX XXX - XX

Output Discharge	Output Voltage	Package Code	Packing
Blank: Non Discharge D: Discharge	11 : 1.1V 12 : 1.2V 135 : 1.35V 15 : 1.5V 18 : 1.8V 185 : 1.85V 22 : 2.2V 23 : 2.3V 25 : 2.5V 26 : 2.6V 28 : 2.8V 285 : 2.85V 29 : 2.9V 295 : 2.95V 30 : 3.0V 31 : 3.1V 32 : 3.2V 33 : 3.3V	FS4: X2-DFN1010-4 W5: SOT25 SA: SOT23 SR: SOT23	7B: Tape & Reel 7: Tape & Reel
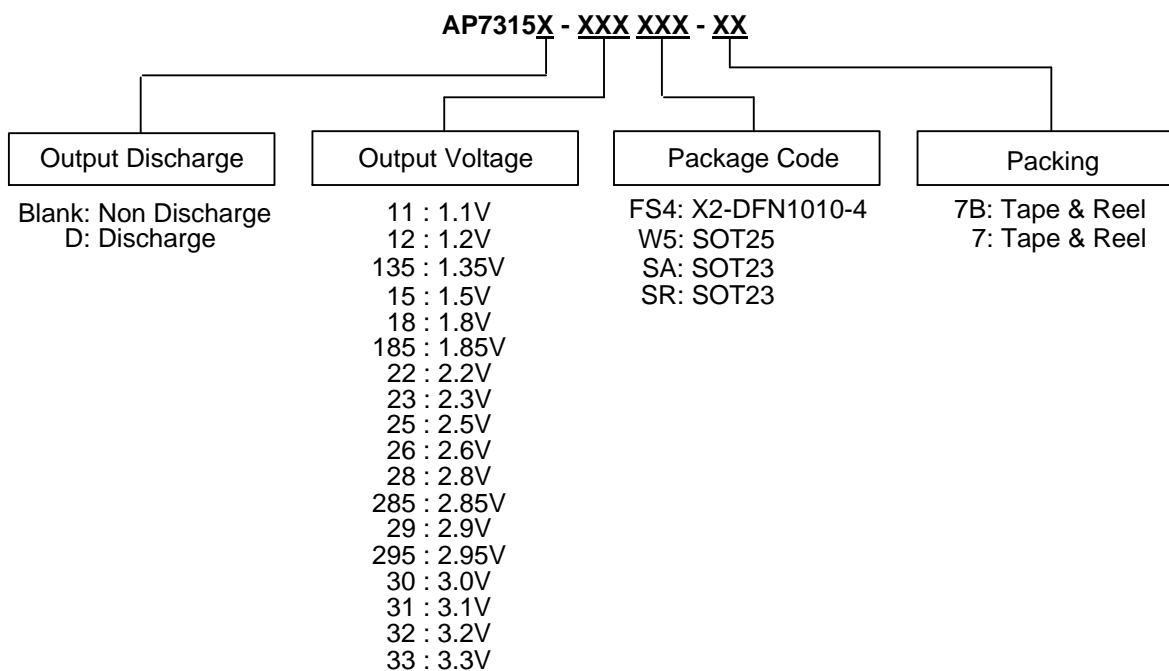
Part Number	Package Code	Packaging	7" Tape and Reel	
			Quantity	Part Number Suffix
AP7315-XXXFS4-7B	FS4	X2-DFN1010-4	10000/Tape & Reel	-7B
AP7315D-XXXFS4-7B	FS4	X2-DFN1010-4	10000/Tape & Reel	-7B
AP7315-XXXW5-7	W5	SOT25	3000/Tape & Reel	-7
AP7315D-XXXW5-7	W5	SOT25	3000/Tape & Reel	-7
AP7315-XXXSA-7	SA	SOT23	3000/Tape & Reel	-7
AP7315-XXXSR-7	SR	SOT23	3000/Tape & Reel	-7

Note: 12. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

AP7315
Document number: DS38352 Rev. 2 - 2

12 of 23
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October 2021
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Marking Information

(1) X2-DFN1010-4

(Top View)

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XXX

Y W X
●
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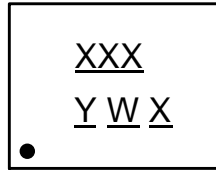
XXX : Identification Code
  Y : Year : 0~9
  W : Week : A~Z : 1~26 week;
     a~z : 27~52 week; z represents
     52 and 53 week
  X : Internal Code

Part Number	Package	Identification Code
AP7315-11FS4-7B	X2-DFN1010-4	15A
AP7315-12FS4-7B	X2-DFN1010-4	15B
AP7315-135FS4-7B	X2-DFN1010-4	15C
AP7315-15FS4-7B	X2-DFN1010-4	15D
AP7315-18FS4-7B	X2-DFN1010-4	15E
AP7315-185FS4-7B	X2-DFN1010-4	15F
AP7315-22FS4-7B	X2-DFN1010-4	15G
AP7315-23FS4-7B	X2-DFN1010-4	15H
AP7315-25FS4-7B	X2-DFN1010-4	15J
AP7315-26FS4-7B	X2-DFN1010-4	15K
AP7315-28FS4-7B	X2-DFN1010-4	15M
AP7315-285FS4-7B	X2-DFN1010-4	15N
AP7315-295FS4-7B	X2-DFN1010-4	15P
AP7315-30FS4-7B	X2-DFN1010-4	15R
AP7315-31FS4-7B	X2-DFN1010-4	15S
AP7315-32FS4-7B	X2-DFN1010-4	15T
AP7315-33FS4-7B	X2-DFN1010-4	15U
AP7315D-11FS4-7B	X2-DFN1010-4	5DA
AP7315D-12FS4-7B	X2-DFN1010-4	5DB
AP7315D-135FS4-7B	X2-DFN1010-4	5DC
AP7315D-15FS4-7B	X2-DFN1010-4	5DD
AP7315D-18FS4-7B	X2-DFN1010-4	5DE
AP7315D-185FS4-7B	X2-DFN1010-4	5DF
AP7315D-22FS4-7B	X2-DFN1010-4	5DG
AP7315D-23FS4-7B	X2-DFN1010-4	5DH
AP7315D-25FS4-7B	X2-DFN1010-4	5DJ
AP7315D-26FS4-7B	X2-DFN1010-4	5DK
AP7315D-28FS4-7B	X2-DFN1010-4	5DM
AP7315D-285FS4-7B	X2-DFN1010-4	5DN
AP7315D-29FS4-7B	X2-DFN1010-4	5DP
AP7315D-295FS4-7B	X2-DFN1010-4	5DR
AP7315D-30FS4-7B	X2-DFN1010-4	5DS
AP7315D-31FS4-7B	X2-DFN1010-4	5DT
AP7315D-32FS4-7B	X2-DFN1010-4	5DU
AP7315D-33FS4-7B	X2-DFN1010-4	5DV

AP7315
Document number: DS38352 Rev. 2 - 2

13 of 23
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October 2021
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Marking Information (Continued)

(2) SOT25

(Top View)

```
┌─5─┐        ┌─4─┐
│         XXX      │
│         Y W X    │
└─1─┘ ┌─2─┐ ┌─3─┐
```

<u>XXX</u> : Identification Code
 <u>Y</u> : Year 0 to 9
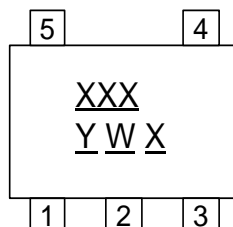 <u>W</u> : Week : A to Z : 1 to 26 week;
     a to z : 27 to 52 week; z represents
     52 and 53 week
 <u>X</u> : Internal Code

Part Number	Package	Identification Code
AP7315-11W5-7	SOT25	15A
AP7315-12W5-7	SOT25	15B
AP7315-135W5-7	SOT25	15C
AP7315-15W5-7	SOT25	15D
AP7315-18W5-7	SOT25	15E
AP7315-185W5-7	SOT25	15F
AP7315-22W5-7	SOT25	15G
AP7315-23W5-7	SOT25	15H
AP7315-25W5-7	SOT25	15J
AP7315-26W5-7	SOT25	15K
AP7315-28W5-7	SOT25	15M
AP7315-285W5-7	SOT25	15N
AP7315-295W5-7	SOT25	15P
AP7315-30W5-7	SOT25	15R
AP7315-31W5-7	SOT25	15S
AP7315-32W5-7	SOT25	15T
AP7315-33W5-7	SOT25	15U
AP7315D-11W5-7	SOT25	5DA
AP7315D-12W5-7	SOT25	5DB
AP7315D-135W5-7	SOT25	5DC
AP7315D-15W5-7	SOT25	5DD
AP7315D-18W5-7	SOT25	5DE
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AP7315D-285W5-7	SOT25	5DN
AP7315D-29W5-7	SOT25	5DP
AP7315D-295W5-7	SOT25	5DR
AP7315D-30W5-7	SOT25	5DS
AP7315D-31W5-7	SOT25	5DT
AP7315D-32W5-7	SOT25	5DU
AP7315D-33W5-7	SOT25	5DV

AP7315
Document number: DS38352 Rev. 2 - 2

14 of 23
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October 2021
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Marking Information (Continued)

(3) SOT23

(Top View)

```
    ┌───┐
    │ 3 │
┌───┴───┴───┐
│    XXX    │
│   Y W X   │
└──┬───┬────┘
 ┌─┴─┐ ┌─┴─┐
 │ 1 │ │ 2 │
 └───┘ └───┘
```

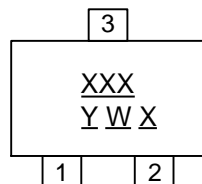XXX : Identification Code
 Y : Year 0 to 9
 W : Week : A to Z : 1 to 26 week;
       a to z : 27 to 52 week; z represents
       52 and 53 week
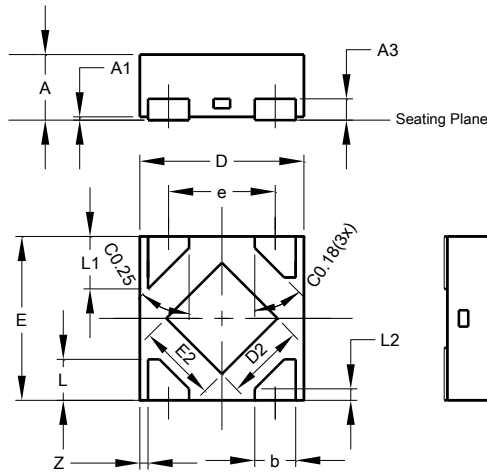 X : Internal Code

Part Number	Package	Identification Code
AP7315-11SA-7	SOT23	15A
AP7315-12SA-7	SOT23	15B
AP7315-135SA-7	SOT23	15C
AP7315-15SA-7	SOT23	15D
AP7315-18SA-7	SOT23	15E
AP7315-185SA-7	SOT23	15F
AP7315-22SA-7	SOT23	15G
AP7315-23SA-7	SOT23	15H
AP7315-25SA-7	SOT23	15J
AP7315-26SA-7	SOT23	15K
AP7315-28SA-7	SOT23	15M
AP7315-285SA-7	SOT23	15N
AP7315-295SA-7	SOT23	15P
AP7315-30SA-7	SOT23	15R
AP7315-31SA-7	SOT23	15S
AP7315-32SA-7	SOT23	15T
AP7315-33SA-7	SOT23	15U
AP7315-11SR-7	SOT23	5DA
AP7315-12SR-7	SOT23	5DB
AP7315-135SR-7	SOT23	5DC
AP7315-15SR-7	SOT23	5DD
AP7315-18SR-7	SOT23	5DE
AP7315-185SR7	SOT23	5DF
AP7315-22SR-7	SOT23	5DG
AP7315-23SR-7	SOT23	5DH
AP7315-25SR-7	SOT23	5DJ
AP7315-26SR-7	SOT23	5DK
AP7315-28SR-7	SOT23	5DM
AP7315-285SR-7	SOT23	5DN
AP7315-295SR-7	SOT23	5DR
AP7315-30SR-7	SOT23	5DS
AP7315-31SR-7	SOT23	5DT
AP7315-32SR-7	SOT23	5DU
AP7315-33SR-7	SOT23	5DV

AP7315
Document number: DS38352 Rev. 2 - 2

15 of 23
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1)   Package Type: X2-DFN1010-4

X2-DFN1010-4			
Dim	**Min**	**Max**	**Typ**
A	-	0.40	0.39
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	0.95	1.05	1.00
D2	0.38	0.58	0.48
E	0.95	1.05	1.00
E2	0.38	0.58	0.48
e	-	-	0.65
L	0.20	0.30	0.25
L1	0.27	0.37	0.32
L2	0.02	0.12	0.07
Z	-	-	0.050
All Dimensions in mm			

AP7315
Document number: DS38352 Rev. 2 - 2

16 of 23
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October 2021
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Package Outline Dimensions (All dimensions in mm.)

(2)   Package Type: SOT23

AP7315
Document number: DS38352 Rev. 2 - 2

17 of 23
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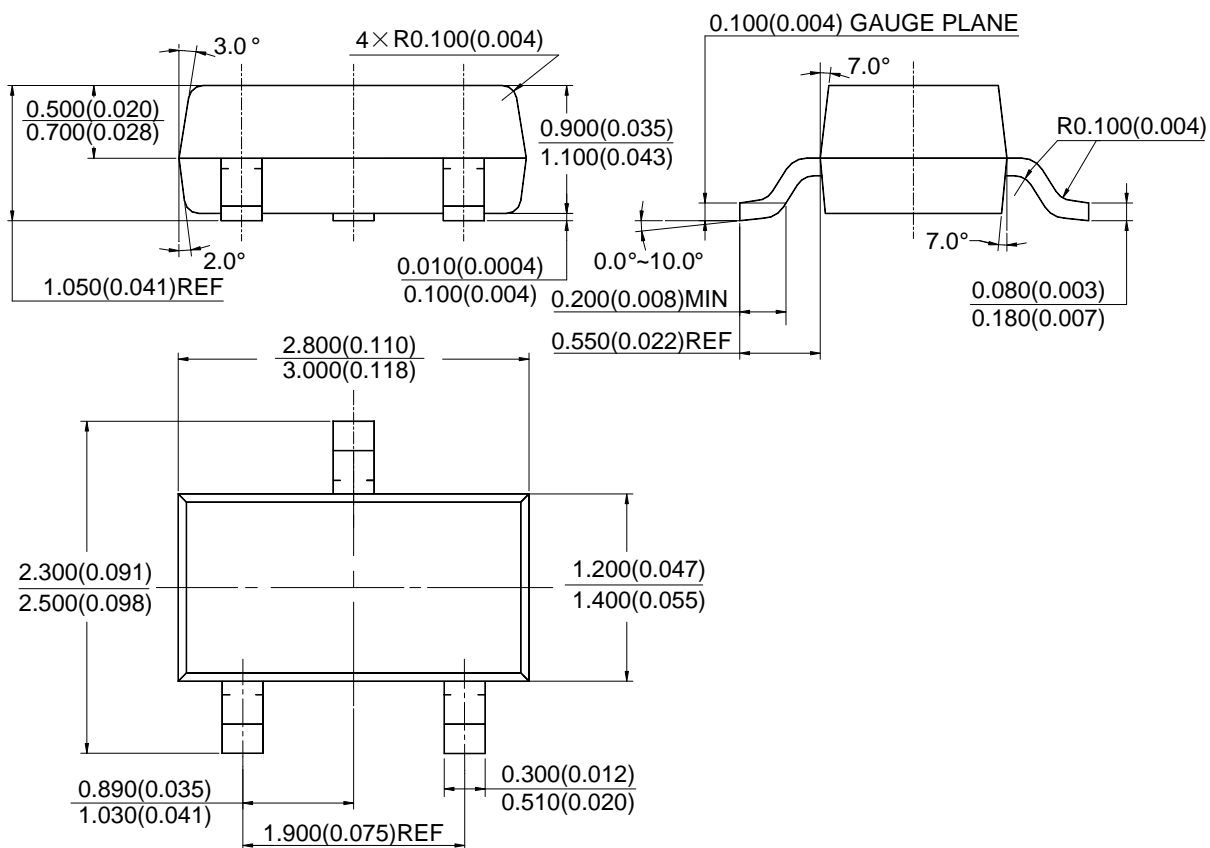
Package Outline Dimensions (All dimensions in mm.)

(3)   Package Type: SOT25

AP7315
Document number: DS38352 Rev. 2 - 2

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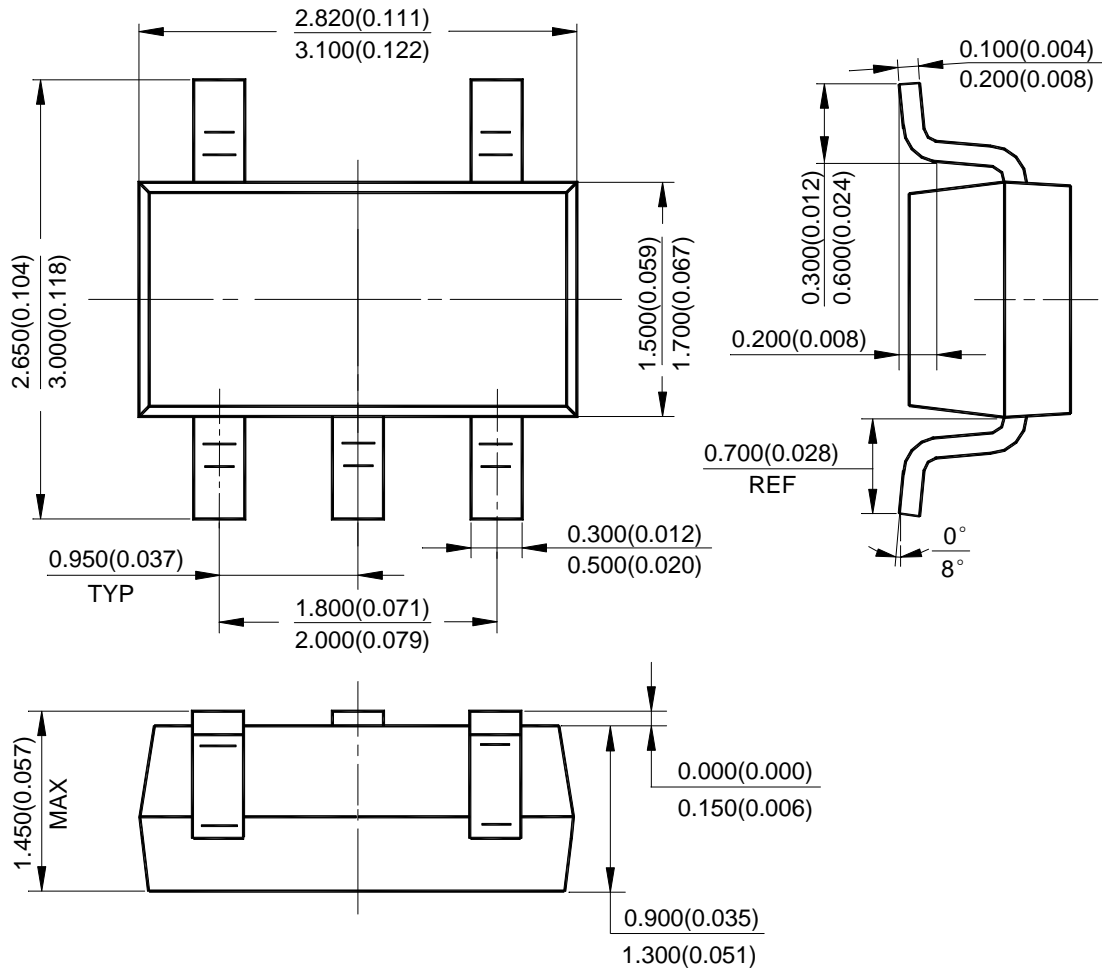
Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1)   Package Type: X2-DFN1010-4
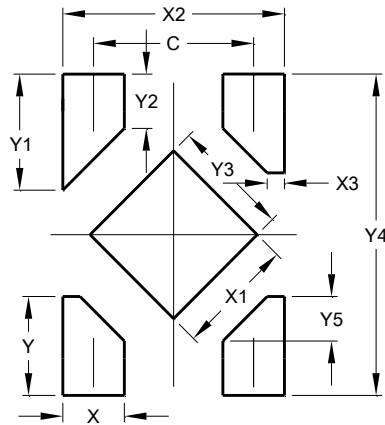
Dimensions	Value (in mm)
C	0.650
X	0.250
X1	0.480
X2	0.900
X3	0.070
Y	0.400
Y1	0.470
Y2	0.220
Y3	0.480
Y4	1.300
Y5	0.180

AP7315
Document number: DS38352 Rev. 2 - 2

19 of 23
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October 2021
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Suggested Pad Layout (Continued)

(2)   Package Type: SOT23

Grid placement courtyard
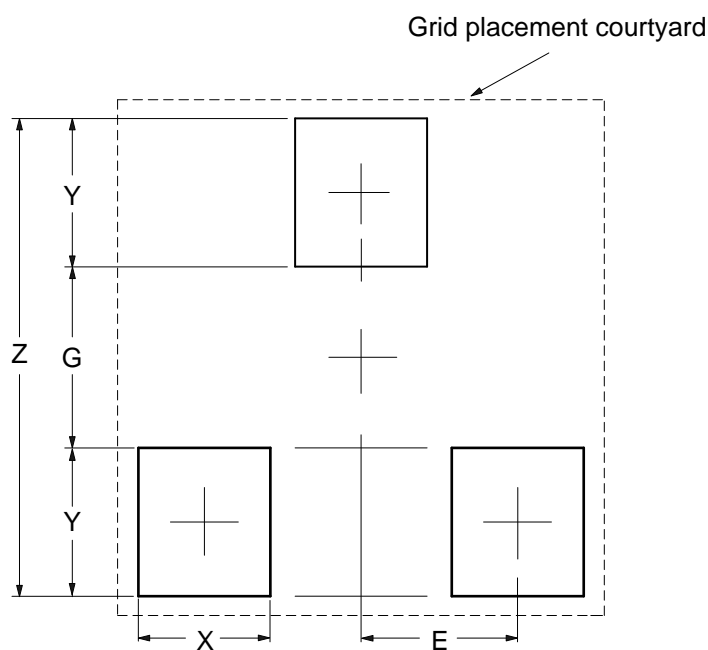
Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	2.900/0.114	1.100/0.043	0.800/0.031	0.900/0.035	0.950/0.037

AP7315
Document number: DS38352 Rev. 2 - 2

20 of 23
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October 2021
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Suggested Pad Layout (Continued)

(3)   Package Type: SOT25
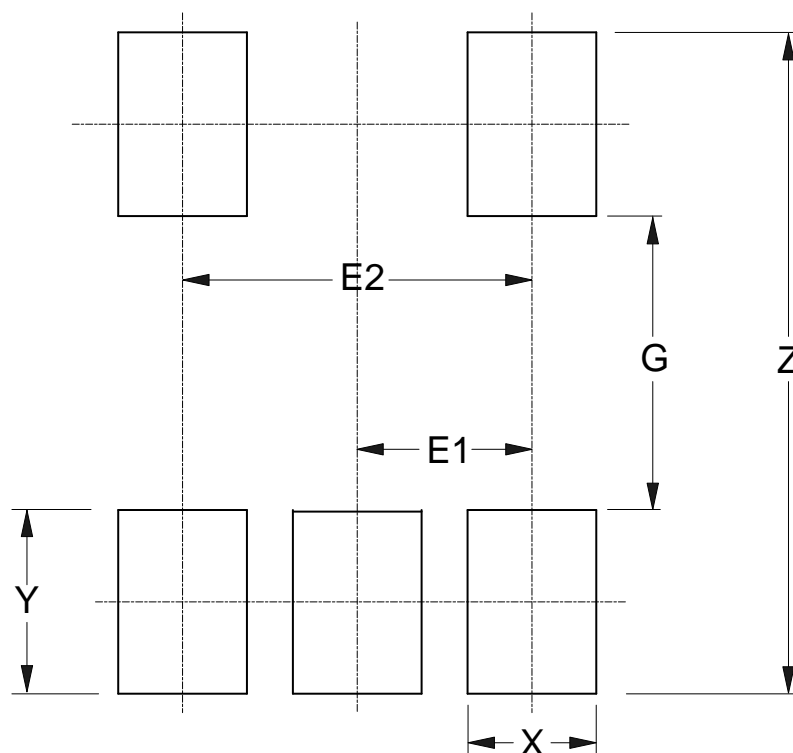
Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E1 (mm)/(inch)	E2 (mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037	1.900/0.075

Tape Orientation

For AP7315-XXXFS4-7B & AP7315D-XXXFS4-7B
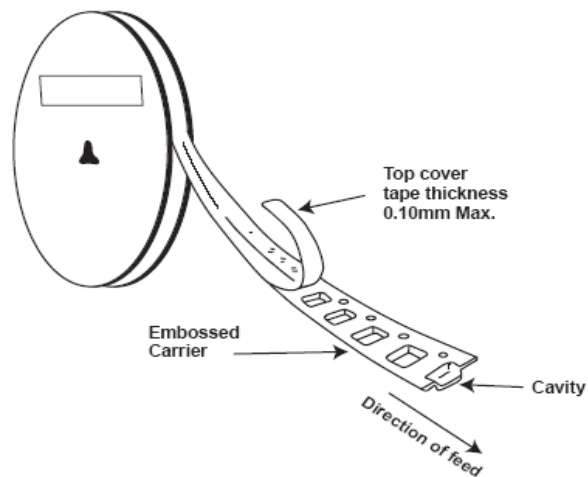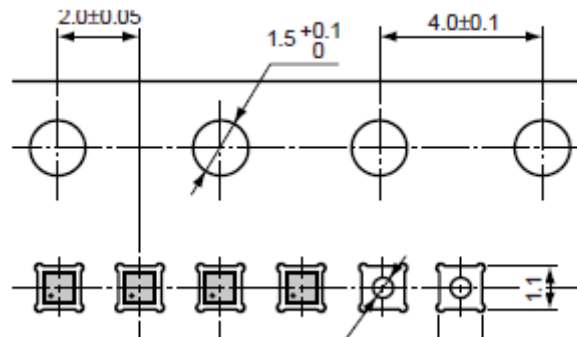
Note: 13. The taping orientation of the other package type can be found on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Mechanical Data

- Moisture Sensitivity: Level 1 Per J-STD-020
- Terminals:
  - SOT23, SOT25: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
  - X2-DFN1010-4: Finish - NiPdAu over Copper Leads, Solderable per MIL-STD-202, Method 208 @4
- Weight:
  - SOT23: 0.009 grams (Approximate)
  - SOT25: 0.016 grams (Approximate)
  - X2-DFN1010-4 (Type B): 0.001 grams (Approximate)

AP7315
Document number: DS38352 Rev. 2 - 2

22 of 23
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October 2021
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AP7315
Document number: DS38352 Rev. 2 - 2

23 of 23
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