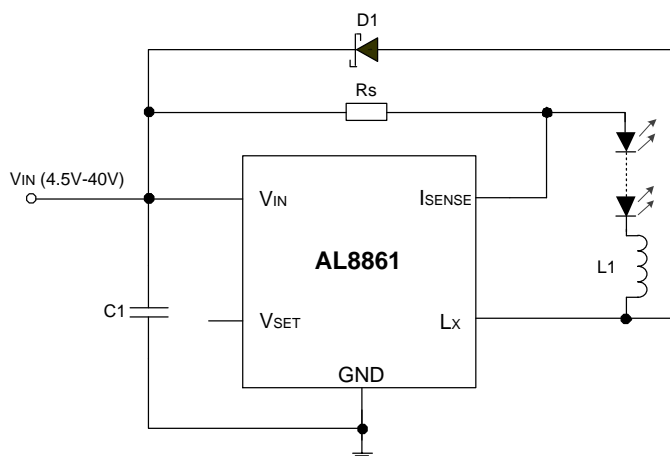


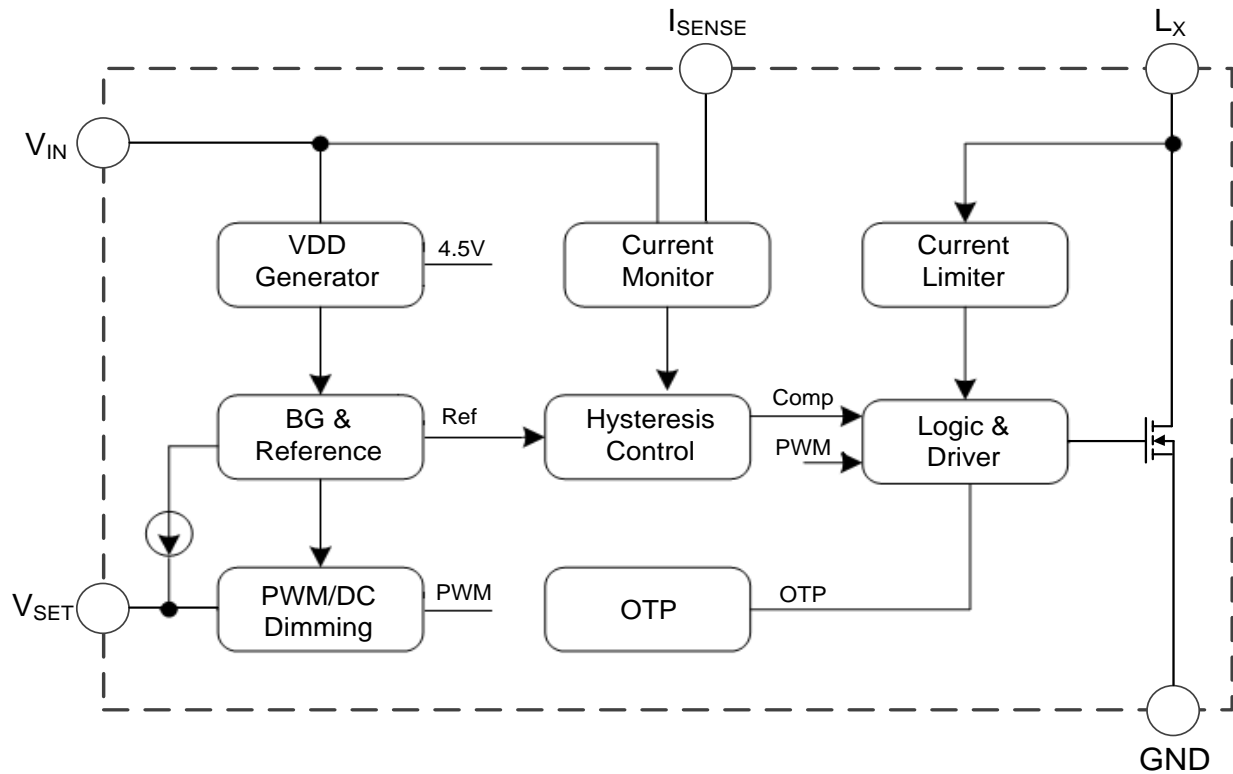
## Typical Applications Circuit



## Pin Descriptions

Pin Number		Pin Name	Function
TSOT25/ SOT89-5	MSOP-8EP		
1	5, 6	Lx	Drain of NDMOS switch.
2	2, 3	GND	Ground (0V)
3	4	VSET	<p>Multi-Function On/Off and Brightness Control Pin:</p> <ul style="list-style-type: none"> <li>• Leave floating, internal current source (typical 1.4μA) will pull up this pin to 4.5V</li> <li>• Drive to voltage below 0.2V to turn off output current</li> <li>• Drive with DC voltage (<math>0.3V &lt; V_{SET} &lt; 2.5V</math>) to adjust output current from 0 to 100% of <math>I_{OUTNOM}</math>. Linear adjustment ranges from 5% to 100% of <math>I_{OUTNOM}</math></li> <li>• Drive with PWM signal from open-collector or open-drain transistor, to adjust output current.</li> </ul> <p>Linear adjustment ranges from 1% to 100% of <math>I_{OUTNOM}</math> for <math>f &lt; 500Hz</math></p> <ul style="list-style-type: none"> <li>• Connect a capacitor from this pin to Ground to increase soft-start time. (Default soft-start time = 0.1ms. Additional soft-start time is approximately 1.5ms/1nF)</li> </ul>
4	1	ISENSE	Connect resistor $R_s$ from this pin to $V_{IN}$ to define nominal average output current. $I_{OUTNOM} = 0.1/R_s$
5	8	VIN	Input voltage (4.5V to 40V). Decouple to Ground with 10μF or higher X7R ceramic capacitor close to device.
—	7	N/C	No Connection

## Functional Block Diagram



## Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating		Unit
$V_{IN}$	Input Voltage	-0.3 to +42		V
$V_{LX}, V_{ISENSE}$	LX, ISENSE Pin Voltage	-0.3 to +42		V
$V_{VSET}$	VSET Pin Voltage	-0.3 to +6		V
$T_J$	Operating Junction Temperature	+150		°C
$T_{STG}$	Storage Temperature Range	-65 to +150		°C
$T_{LEAD}$	Lead Temperature (Soldering, 10s)	+260		°C
$\theta_{JA}$	Thermal Resistance (Junction to Ambient)	TSOT25 (Note 5)	147	°C/W
		SOT89-5 (Note 6)	116	
		MSOP-8EP (Note 7)	56	
$\theta_{JC}$	Thermal Resistance (Junction to Case)	TSOT25 (Note 5)	27	°C/W
		SOT89-5 (Note 6)	24	
		MSOP-8EP (Note 7)	15	

- Notes:
- Stresses greater than those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability. Besides, if the voltage on VSET Pin is bigger than 5V, the device will enter the test mode for parameter test. Therefore, the voltage on VSET Pin should keep below 5V for normal operation.
  - Device mounted on 1"×1" FR-4 MRP substrate PC board, 2oz cooper, with minimum recommended pad layout.
  - Device mounted on 1"×1" FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on 2"×2" FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

## ESD Ratings

Symbol	Parameter	Rating	Unit
V <sub>ESD</sub>	Human Body Model (HBM)	±2500	V
	Machine Model (MM)	±200	

## Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V <sub>IN</sub>	Input Voltage	4.5	40	V
V <sub>VSET</sub>	V <sub>SET</sub> Pin Voltage	0	5	V
f <sub>SW</sub>	Switching Frequency	—	1	MHz
I <sub>OUT</sub>	Continuous Output Current	TSOT25	1	A
		SOT89-5	1.5	
		MSOP-8EP	1.5	
T <sub>A</sub>	Operating Ambient Temperature	-40	+85	°C
T <sub>J</sub>	Operating Junction Temperature	-40	+125	°C
—	Recommended Analog Dimming Range	5	100	%

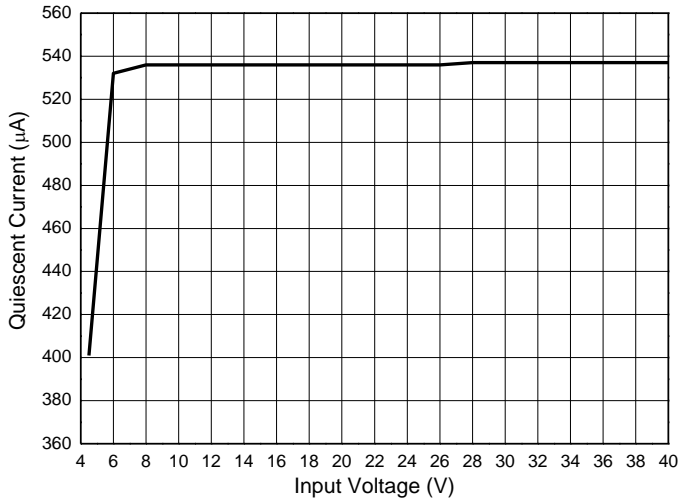
## Electrical Characteristics (@V<sub>IN</sub> = 16V, T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>SUPPLY VOLTAGE</b>						
V <sub>IN</sub>	Input Voltage	—	4.5	—	40	V
I <sub>Q</sub>	Quiescent Current	V <sub>SET</sub> Pin Floating, V <sub>IN</sub> = 16V	—	0.55	—	mA
I <sub>SHDN</sub>	Shutdown Supply Current	V <sub>SET</sub> Pin Grounded	—	55	100	μA
V <sub>SENSE</sub>	Mean Current Sense Threshold Voltage	Measured on I <sub>SENSE</sub> Pin with Respect to V <sub>IN</sub>	96	100	104	mV
V <sub>SENSE_HYS</sub>	Sense Threshold Hysteresis	—	—	±13	—	%
I <sub>SENSE</sub>	I <sub>SENSE</sub> Pin Input Current	V <sub>SENSE</sub> = V <sub>IN</sub> - 0.1V	—	8	—	μA
V <sub>EN</sub>	V <sub>SET</sub> Range on V <sub>SET</sub> Pin	For Analog Dimming	0.3	—	2.5	V
V <sub>EN(ON)</sub>	DC Voltage on V <sub>SET</sub> Pin to Enable	V <sub>EN</sub> Rising	—	0.25	—	V
V <sub>EN(OFF)</sub>	DC Voltage on V <sub>SET</sub> Pin to Disable	V <sub>EN</sub> Falling	—	0.2	—	V
R <sub>LX</sub>	L <sub>X</sub> Switch On-Resistance	@I <sub>LX</sub> = 100mA	—	0.2	—	Ω
I <sub>LX(LEAK)</sub>	L <sub>X</sub> Switch Leakage Current	—	—	—	5	μA
t <sub>SS</sub>	Soft-Start Time	V <sub>IN</sub> = 16V, C <sub>EN</sub> = 1nF	—	1.5	—	ms
f <sub>LX</sub>	Operating Frequency	V <sub>I</sub> = 16V, V <sub>O</sub> = 9.6V (3 LEDs) L = 47μF, ΔI = 0.25A (I <sub>LED</sub> = 1A)	—	250	—	kHz
t <sub>ON_REC</sub>	Recommended Minimum Switch ON Time	For 4% Accuracy	—	500	—	ns
f <sub>LX(MAX)</sub>	Recommended Maximum Switch Frequency	—	—	—	1.0	MHz
DL <sub>X(MAX)</sub>	Maximum Duty Cycle	—	—	98	—	%
DL <sub>X</sub>	Recommended Duty Cycle Range	—	25	—	75	%
t <sub>PD</sub>	Internal Comparator Propagation Delay (Note 8)	—	—	45	—	ns
T <sub>OTP</sub>	Over Temperature Protection	—	—	+150	—	°C
T <sub>OTP_HYS</sub>	Temp Protection Hysteresis	—	—	+30	—	°C
I <sub>XL(MAX)</sub>	Current Limit	Peak Inductor Current	2	—	—	A

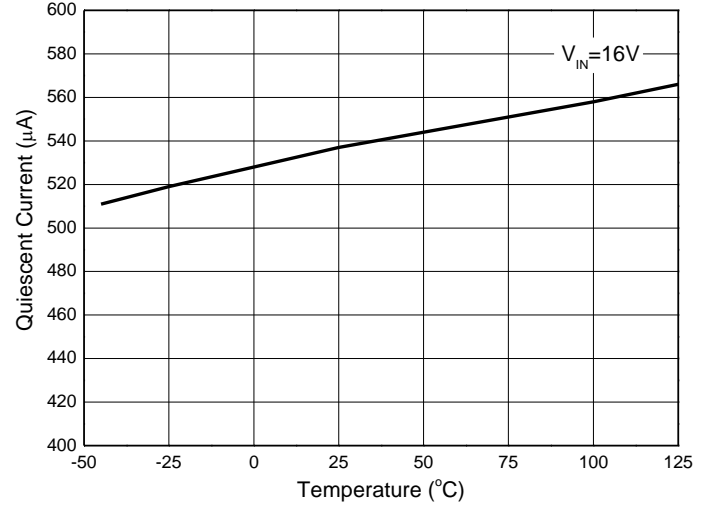
Note: 8. Guaranteed by design.

**Typical Performance Characteristics** (@ $T_A = +25^\circ\text{C}$ ,  $V_{IN} = 16\text{V}$ , unless otherwise specified.)

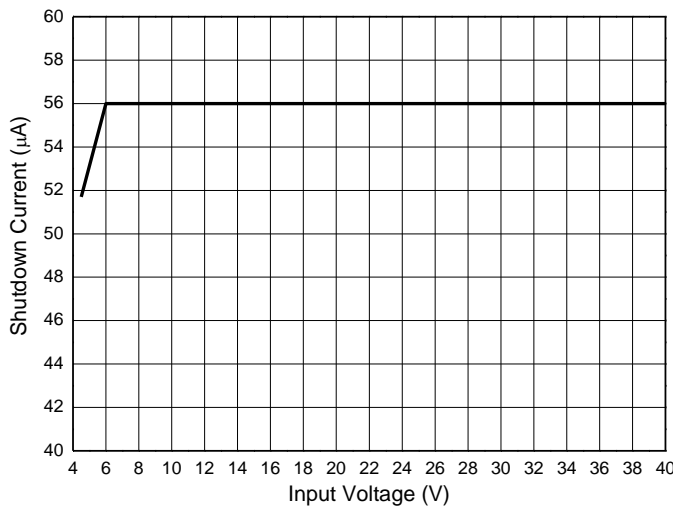
**Quiescent Current vs. Input Voltage**



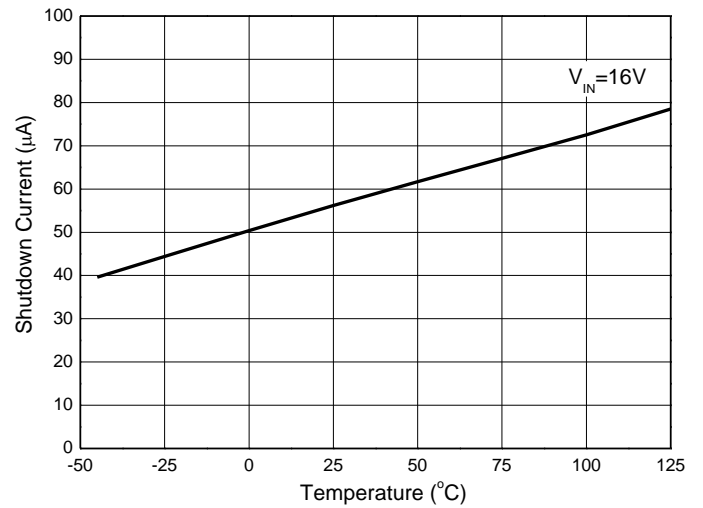
**Quiescent Current vs. Temperature**



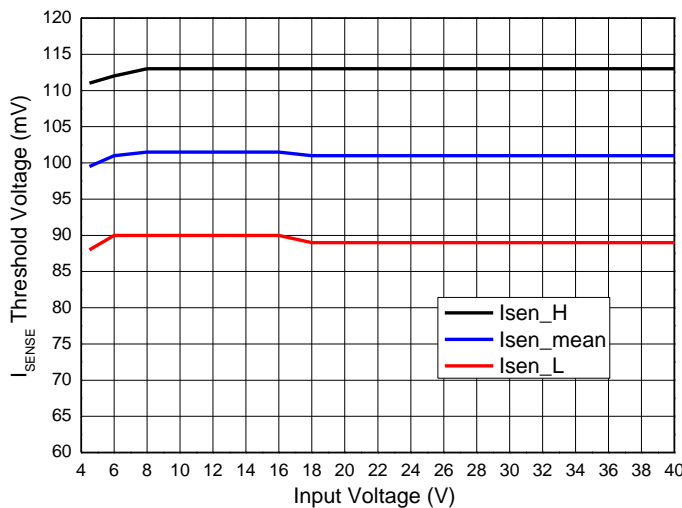
**Shutdown Current vs. Input Voltage**



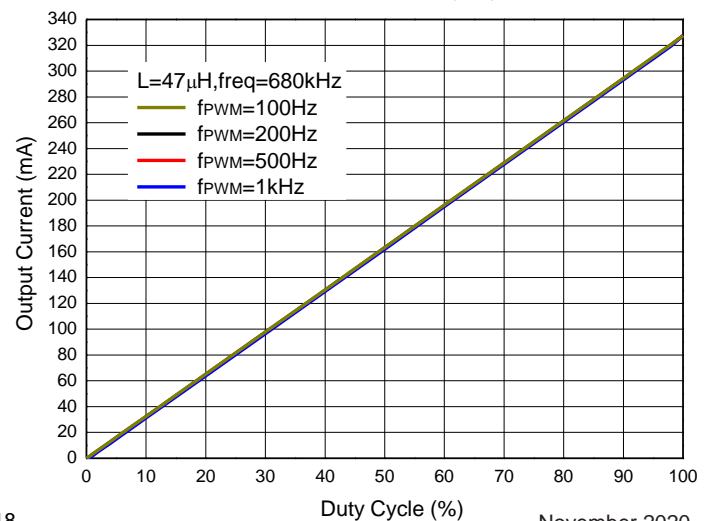
**Shutdown Current vs. Temperature**



**I<sub>SENSE</sub> Threshold Voltage vs. Input Voltage**

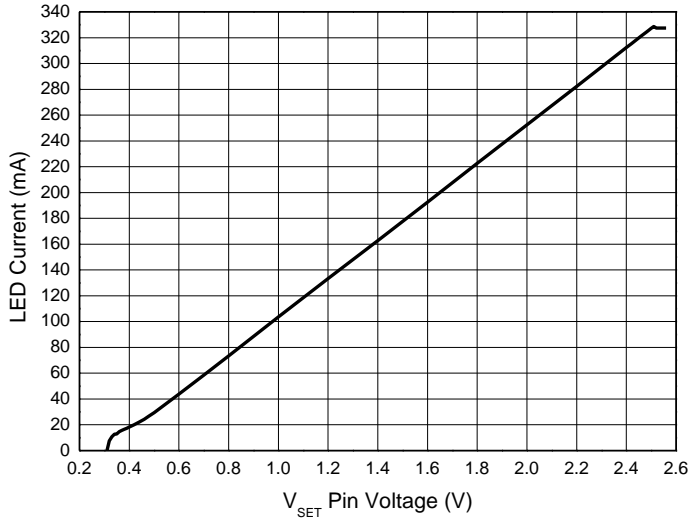


**PWM Dimming ( $V_{IN}=16\text{V}$ , 3 LEDs,  $47\mu\text{H}$ ,  $R_S=0.3\Omega$ )  
LED Current vs. Duty Cycle**

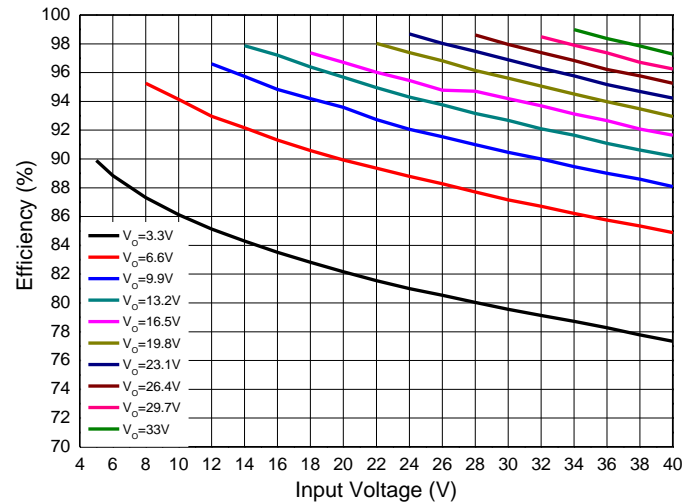


**Typical Performance Characteristics** (continued) (@T<sub>A</sub> = +25°C, V<sub>IN</sub> = 16V, unless otherwise specified.)

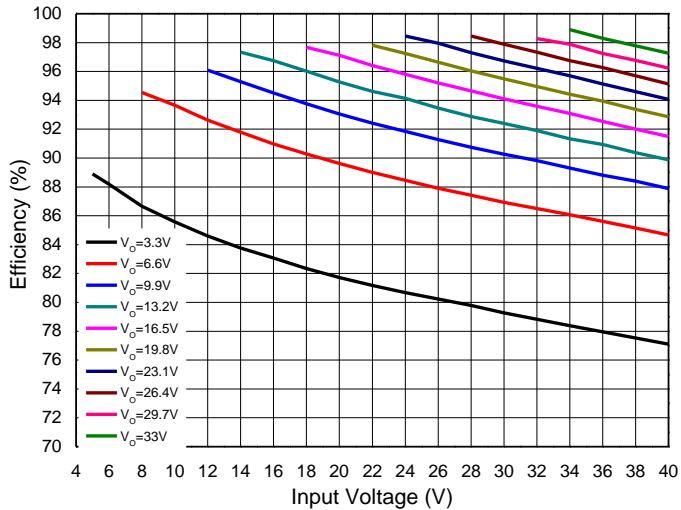
**Analog Dimming (V<sub>IN</sub>=16V, 3LEDs, 47μH, R<sub>S</sub>=0.3Ω)**  
**LED Current vs. V<sub>SET</sub> Voltage**



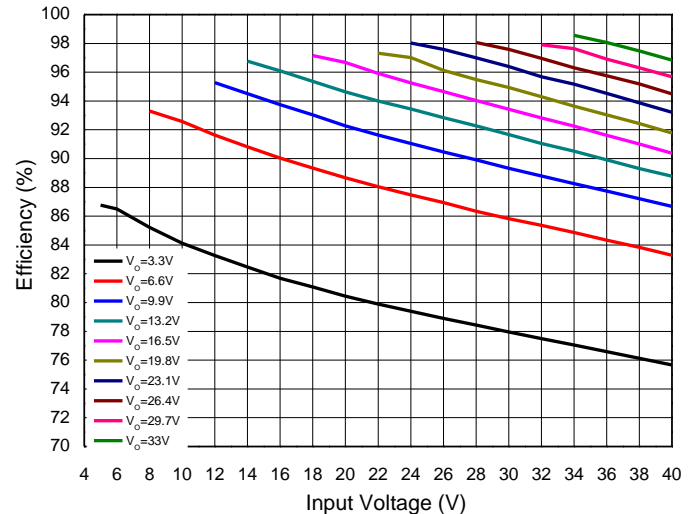
**Efficiency vs. Input Voltage**  
**(TSOT25, R<sub>S</sub>=0.3Ω, L=100μH)**



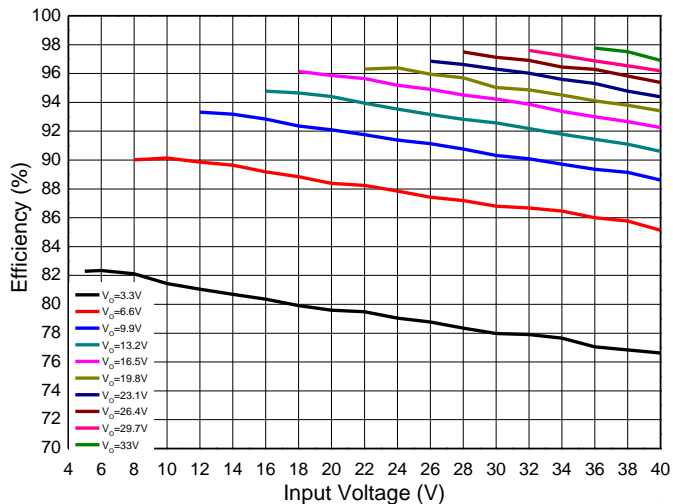
**Efficiency vs. Input Voltage**  
**(TSOT25, R<sub>S</sub>=0.15Ω, L=47μH)**



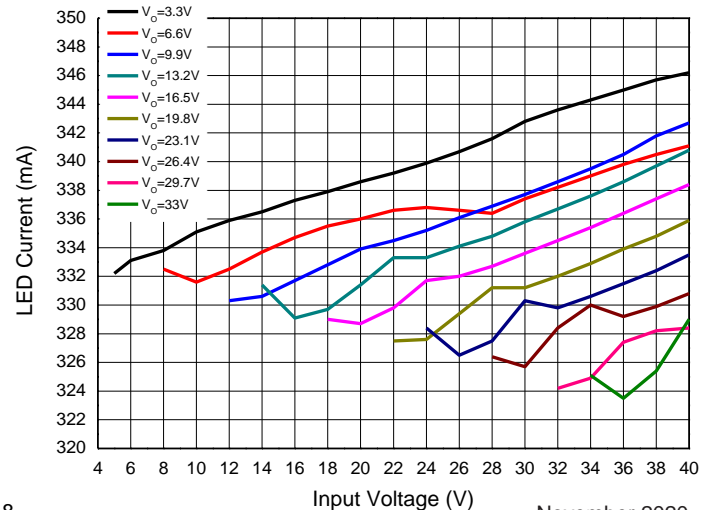
**Efficiency vs. Input Voltage**  
**(TSOT25, R<sub>S</sub>=0.1Ω, L=33μH)**



**Efficiency vs. Input Voltage**  
**(SOT89-5, R<sub>S</sub>=0.067Ω, L=47μH)**

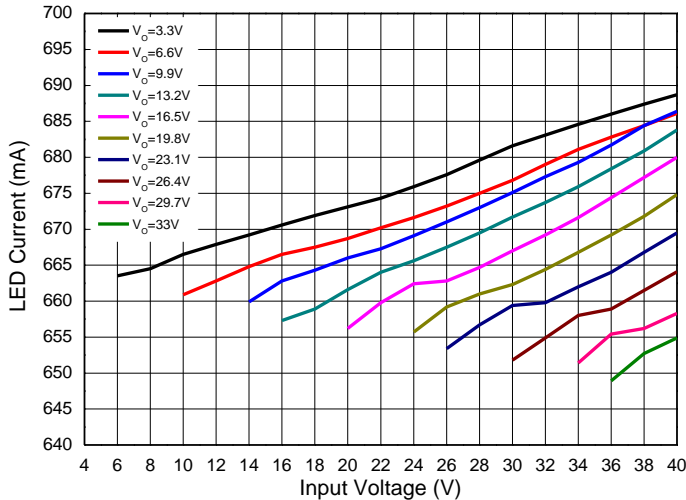


**LED Current vs. Input Voltage**  
**(TSOT25, R<sub>S</sub>=0.3Ω, L=100μH)**

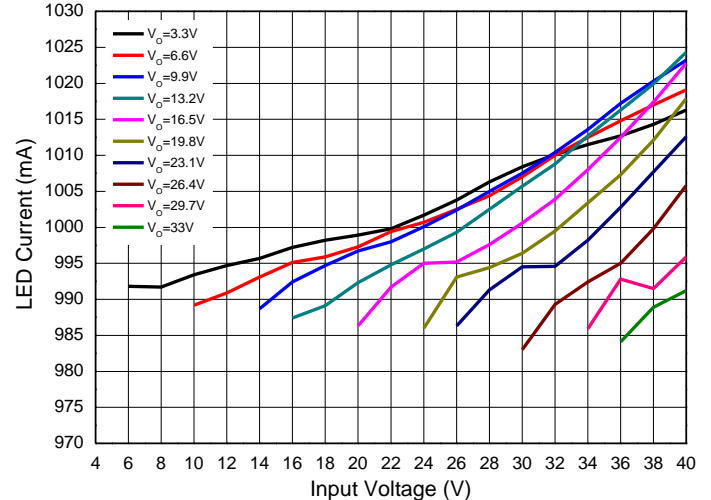


**Typical Performance Characteristics** (continued) (@T<sub>A</sub> = +25°C, V<sub>IN</sub> = 16V, unless otherwise specified.)

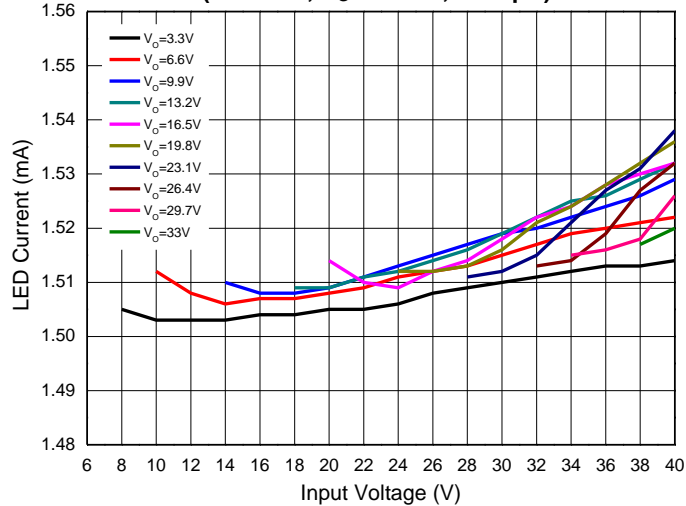
**LED Current vs. Input Voltage**  
(TSOT25, R<sub>S</sub>=0.15Ω, L=47μH)



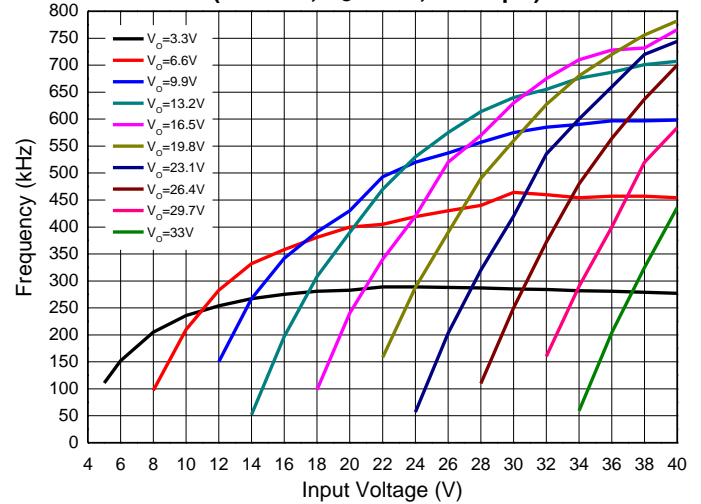
**LED Current vs. Input Voltage**  
(TSOT25, R<sub>S</sub>=0.1Ω, L=33μH)



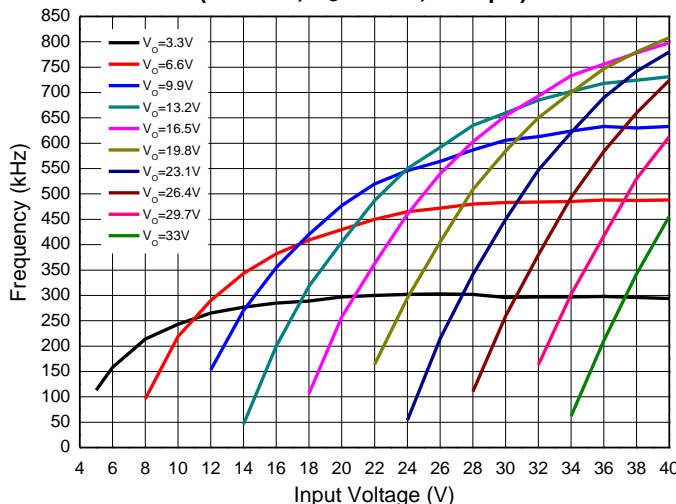
**LED Current vs. Input Voltage**  
(SOT89-5, R<sub>S</sub>=0.067Ω, L=47μH)



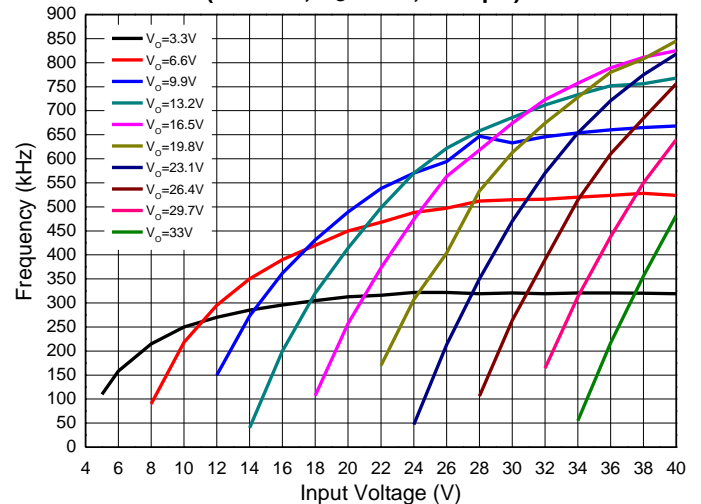
**Operating Frequency vs. Input Voltage**  
(TSOT25, R<sub>S</sub>=0.3Ω, L=100μH)



**Operating Frequency vs. Input Voltage**  
(TSOT25, R<sub>S</sub>=0.15Ω, L=47μH)

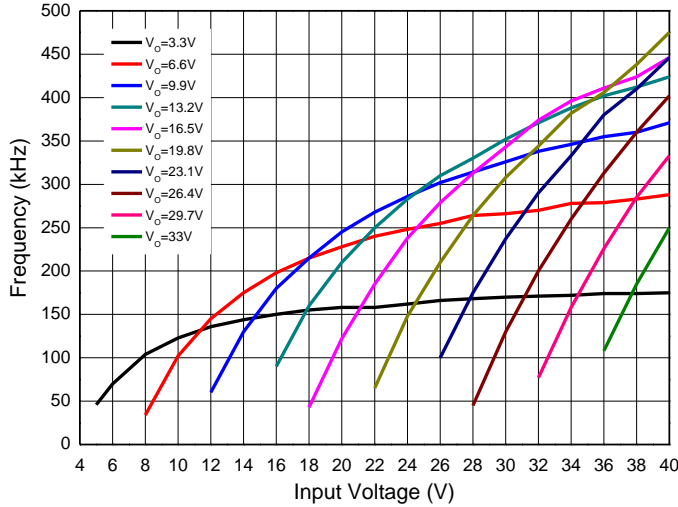


**Operating Frequency vs. Input Voltage**  
(TSOT25, R<sub>S</sub>=0.1Ω, L=33μH)

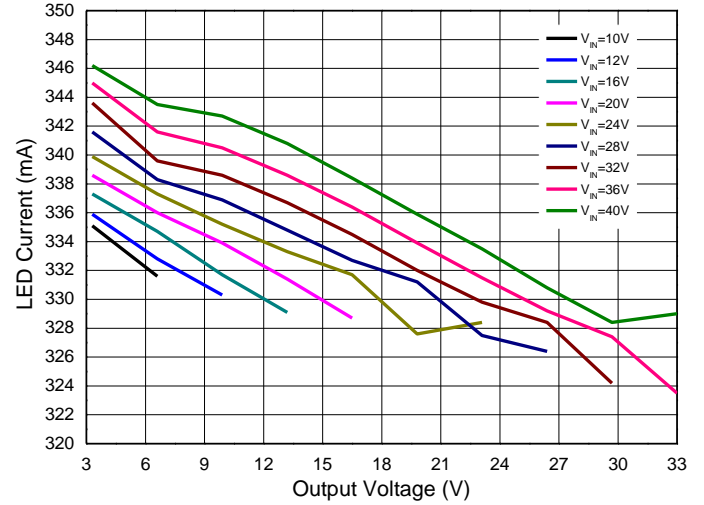


**Typical Performance Characteristics** (continued) (@T<sub>A</sub> = +25°C, V<sub>IN</sub> = 16V, unless otherwise specified.)

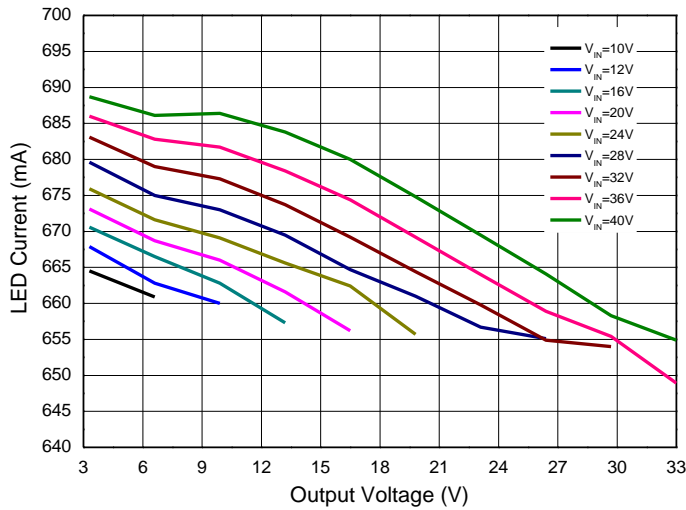
**Operating Frequency vs. Input Voltage**  
(SOT89-5, R<sub>S</sub>=0.067Ω, L=47μH)



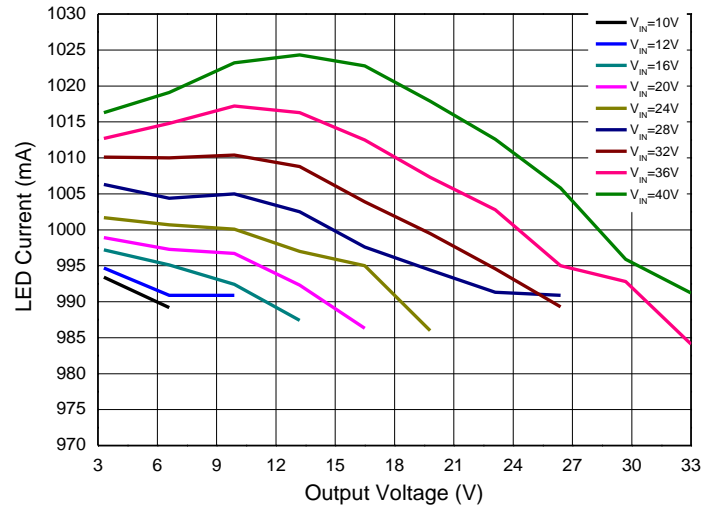
**LED Current vs. Output Voltage**  
(TSOT25, R<sub>S</sub>=0.3Ω, L=100μH)



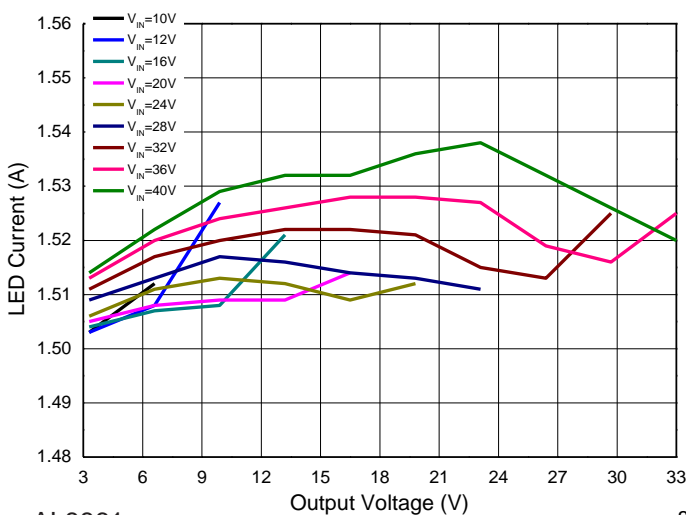
**LED Current vs. Output Voltage**  
(TSOT25, R<sub>S</sub>=0.15Ω, L=47μH)



**LED Current vs. Output Voltage**  
(TSOT25, R<sub>S</sub>=0.1Ω, L=33μH)

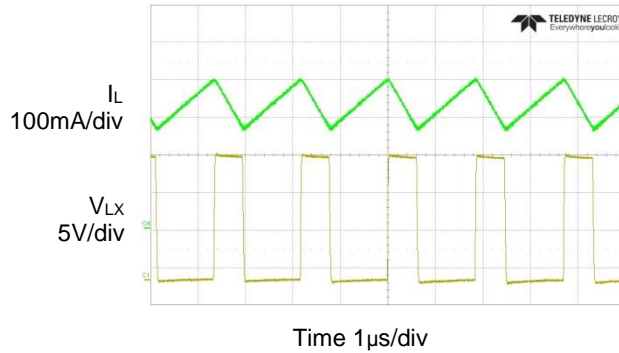


**LED Current vs. Output Voltage**  
(SOT89-5, R<sub>S</sub>=0.067Ω, L=47μH)

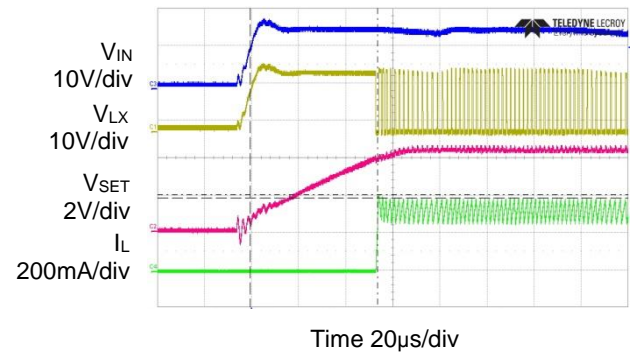


**Performance Characteristics** (@ $V_{IN} = 16V$ , 3 LEDs,  $R_S = 0.3\Omega$ ,  $L = 47\mu H$ ,  $T_A = +25^\circ C$ , unless otherwise specified.)

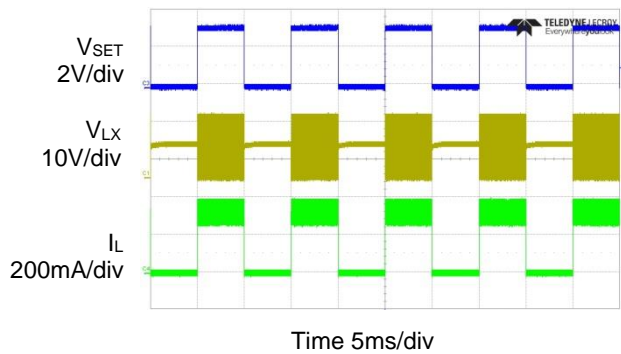
**Steady State**



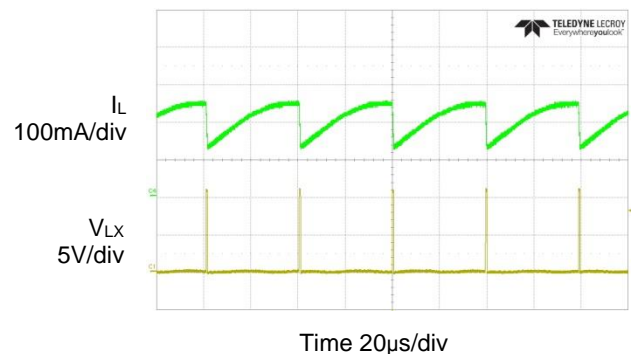
**Start Up**



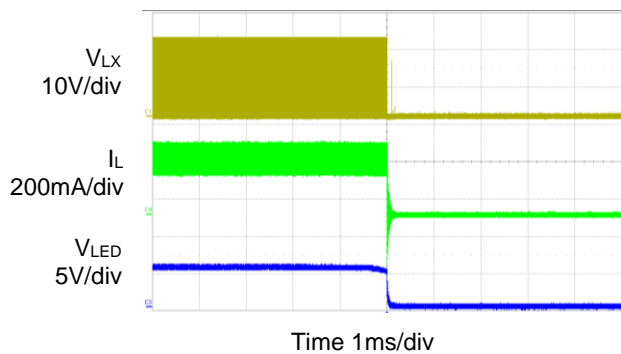
**PWM Dimming (100Hz, Duty=50%)**



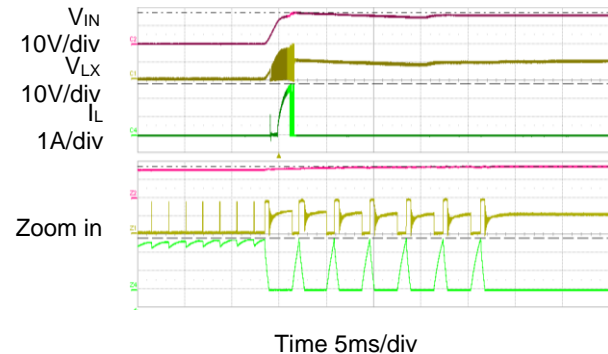
**Pulse Skip Mode**



**LED Open Protection**



**$R_S$  Short Protection**





## Application Information

The AL8861 is a hysteretic mode LED driver with integrated power switch. It is available in two packages that provide a trade-off between PCB area and power dissipation capability. It is recommended that at higher LED currents/smaller PCBs that the SOT89-5 version should be used to maximize the allowable LED current over a wider ambient temperature range.

### AL8861 Operation

In normal operation, when normal input voltage is applied at +V<sub>IN</sub>, the AL8861 internal switch will turn on. Current starts to flow through sense resistor R<sub>S</sub>, inductor L1, and the LEDs. The current ramps up linearly, and the ramp rate is determined by the input voltage V<sub>IN</sub> and the inductor L1. This rising current produces a voltage ramp across R<sub>S</sub>. The internal circuit of the AL8861 senses the voltage across R<sub>S</sub> and applies a proportional voltage to the input of the internal comparator. When this voltage reaches an internally set upper threshold, the internal switch is turned off. The inductor current continues to flow through R<sub>S</sub>, L1, LEDs and diode D1, and back to the supply rail, but it decays, with the rate determined by the forward voltage drop of LEDs and the diode D1.

This decaying current produces a falling voltage on R<sub>S</sub>, which is sensed by the AL8861. A voltage proportional to the sense voltage across R<sub>S</sub> will be applied at the input of internal comparator. When this voltage falls to the internally set lower threshold, the internal switch is turned on again.

This switch-on-and-off cycle continues to provide the average LED current set by the sense resistor R<sub>S</sub>.

### LED Current Configuration

The nominal average output current in the LED(s) is determined by the value of the external current sense resistor (R<sub>S</sub>) connected between V<sub>IN</sub> and I<sub>SENSE</sub> and is given by:

$$I_{OUT(NOM)} = \frac{0.1}{R_S}$$

The table below gives values of nominal average output current for several preferred values of current setting resistor (R<sub>S</sub>) in the *Typical Application Circuit* shown on Page 2.

R <sub>S</sub> (Ω)	Nominal Average Output Current (mA)
0.066	1,500
0.1	1,000
0.13	760
0.15	667
0.3	333

The above values assume that the V<sub>SET</sub> pin is floating and at a nominal reference voltage for internal comparator. It is possible to use different values of R<sub>S</sub> if the V<sub>SET</sub> pin is driven by an external dimming signal.

### Analog Dimming

Applying a DC voltage from 0.3V to 2.5V on the V<sub>SET</sub> pin can adjust output current from 0 to 100% of I<sub>OUTNOM</sub>, as shown in Figure 1. Recommended dimming range is from 5% to 100%. If the V<sub>SET</sub> pin is brought higher than 2.5V, the LED current will be clamped to 100% of I<sub>OUTNOM</sub> while if the V<sub>SET</sub> voltage falls below the threshold of 0.3V, the output switch will turn off.

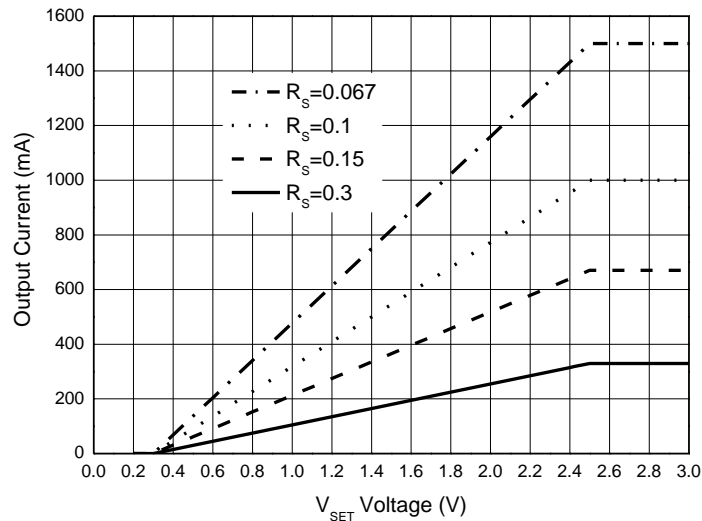
### PWM Dimming

LED current can be adjusted digitally, by applying a low frequency pulse-width-modulated (PWM) logic signal to the V<sub>SET</sub> pin to turn the device on and off. This will produce an average output current proportional to the duty cycle of the control signal. To achieve a high resolution, the PWM frequency is recommended to be lower than 500Hz, however higher dimming frequencies can be used at the expense of dimming dynamic range and accuracy. Typically, for a PWM frequency of 500Hz the accuracy is better than 1% for PWM ranging from 1% to 100%.

The accuracy of the low duty cycle dimming is affected by both the PWM frequency and the switching frequency of the AL8861. For best accuracy/resolution, the switching frequency should be increased while the PWM frequency should be reduced.

The V<sub>SET</sub> pin is designed to be driven by both 3.3V and 5V logic levels directly from a logic output with either an open drain output or push pull output stage.

## Application Information (continued)



**Figure 1. Analog Dimming Curve**

### Soft-Start

The default soft-start time for AL8861 is only 0.1ms – this provides very fast turn-on of the output, improving PWM dimming accuracy.

Nevertheless, adding an external capacitor from the V<sub>SET</sub> pin to Ground will provide a longer soft-start delay. This is achieved by increasing the time for the V<sub>SET</sub> voltage rising to the turn-on threshold, and by slowing down the rising rate of the control voltage at the input of hysteresis comparator. The additional soft-start time is related to the capacitance between V<sub>SET</sub> and GND, the typical value will be 1.5ms/nF.

### Capacitor Selection

A low ESR capacitor should be used for input decoupling, as the ESR of this capacitor appears in series with the supply source impedance and will lower overall efficiency. This capacitor has to supply the relatively high peak current to the coil and smooth the ripple on the input current.

The minimum capacitance needed is determined by input power, cable's length and peak current. 4.7μF to 10μF is a commonly used value for most cases. A higher value will improve performance at lower input voltages, especially when the source impedance is high. The input capacitor should be placed as close as possible to the IC.

For maximum stability of over temperature and voltage, capacitors with X7R, X5R or better dielectric are recommended. Capacitors with Y5V dielectric are not suitable for decoupling in this application and should NOT be used.

### Diode Selection

For maximum efficiency and performance, the freewheeling diode (D1) should be a fast low capacitance Schottky diode with low reverse leakage current. It also provides better efficiency than silicon diodes, due to lower forward voltage and reduced recovery time.

It is important to select parts with a peak current rating above the peak coil current, and a continuous current rating higher than the maximum output load current. It is very important to consider the reverse leakage current of the diode when operating above +85°C. Excess leakage current will increase power dissipation.

The higher forward voltage and overshoot due to reverse recovery time in silicon diodes will increase the peak voltage on the L<sub>x</sub> output. If a silicon diode is used, more care should be taken to ensure that the total voltage appearing on the L<sub>x</sub> pins including supply ripple, won't exceed the specified maximum value.

## Application Information (continued)

### Inductor Selection

Recommended inductor values for the AL8861 are in the range 33µH to 100µH. Higher inductance are recommended at higher supply voltages in order to minimize output current tolerance due to switching delays, which will result in increased ripple and lower efficiency. Higher inductance also results in a better line regulation. The inductor should be mounted as close to the device as possible with low resistance connections to Lx pins.

The chosen coil should have saturation current higher than the peak output current and a continuous current rating above the required mean output current.

The inductor value should be chosen to maintain operating duty cycle and switch 'on'/'off' times within the specified limits over the supply voltage and load current range. The following equations can be used as a guide.

$$t_{ON} = \frac{\text{Lx Switch 'On' Time} \quad L\Delta I}{V_{IN} - V_{LED} - I_{LED}(R_S + R_L + R_{LX})}$$

$$t_{OFF} = \frac{\text{Lx Switch 'Off' Time} \quad L\Delta I}{V_{LED} + V_D + I_{LED}(R_S + R_L)}$$

Where: L is the coil inductance; R<sub>L</sub> is the coil resistance; R<sub>S</sub> is the current sense resistance; I<sub>LED</sub> is the required LED current; ΔI is the coil peak-peak ripple current (internally set to 0.26 × I<sub>LED</sub>); V<sub>IN</sub> is the supply voltage; V<sub>LED</sub> is the total LED forward voltage; R<sub>LX</sub> is the switch resistance (0.2Ω nominal); V<sub>D</sub> is the diode forward voltage at the required load current.

### Thermal Protection

The AL8861 includes over-temperature protection (OTP) circuitry that will turn off the device if its junction temperature gets too high. This is to protect the device from excessive heat damage. The OTP circuitry includes thermal hysteresis that will cause the device to restart normal operation once its junction temperature has cooled down by approximately +30°C.

### Open Circuit LEDs

The AL8861 has by default open LED protection. If the LEDs should become open circuit the AL8861 will stop oscillating; the I<sub>SENSE</sub> pin will rise to V<sub>IN</sub> and the Lx pin will then fall to GND. No excessive voltages will be seen by the AL8861.

### LED Chain Shorted Together

If the LED chain should become shorted together (the anode of the top LED becomes shorted to the cathode of the bottom LED) the AL8861 will continue to switch and the current through the AL8861's internal switch will still be at the expected current - so no excessive heat will be generated within the AL8861. However, the duty cycle at which it operates will change dramatically and the switching frequency will most likely decrease. See Figure 2 for an example of this behavior at 24V input voltage driving 3 LEDs.

The on-time of the internal power MOSFET switch is significantly reduced because almost all of the input voltage is now developed across the inductor. The off-time is significantly increased because the reverse voltage across the inductor is now just the Schottky diode voltage (See Figure 2) causing a much slower decay in inductor current.

## Application Information (continued)

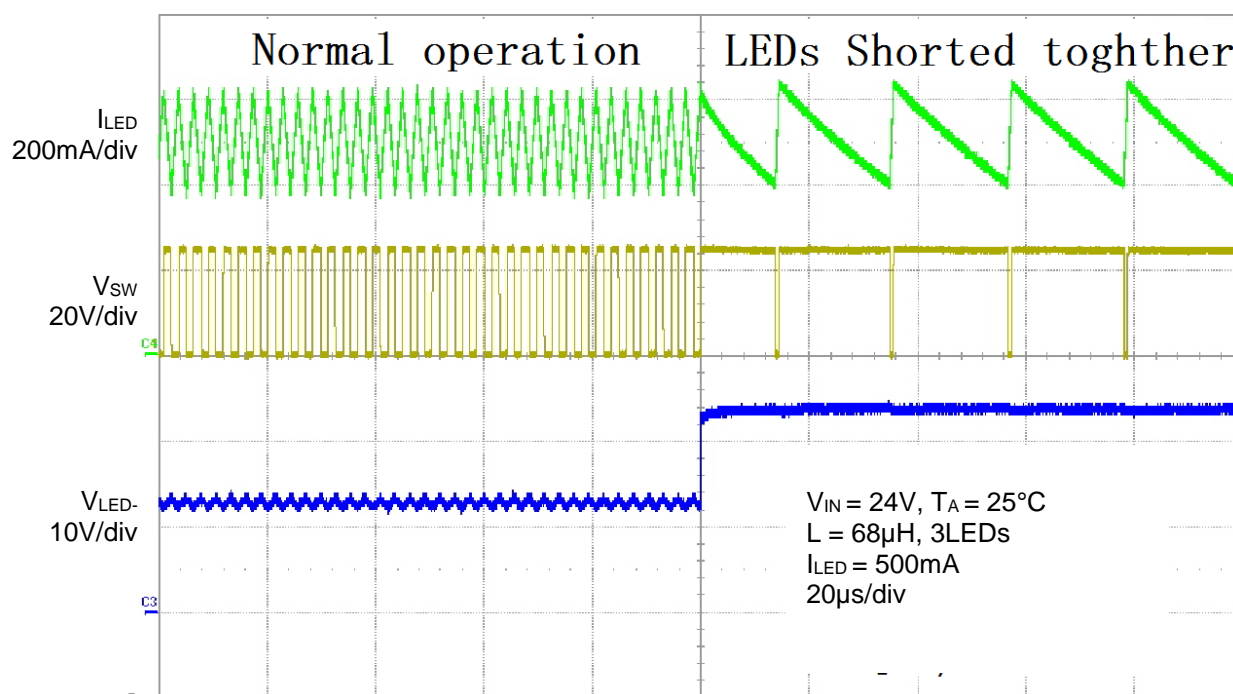
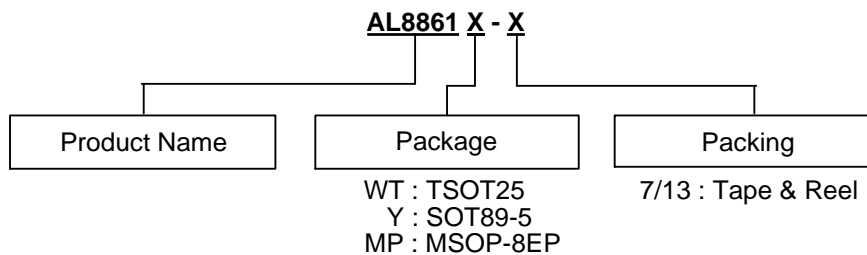


Figure 2. Switching Characteristics (Normal Operation to LED Chain Shorted Out)

### Rs Short Protection

The AL8861 has a current limit at about 2.8A. If  $R_s$  is shorted, current limit is triggered for accumulated 7 times and the switch will shut down and latch up.

## Ordering Information (Note 9)

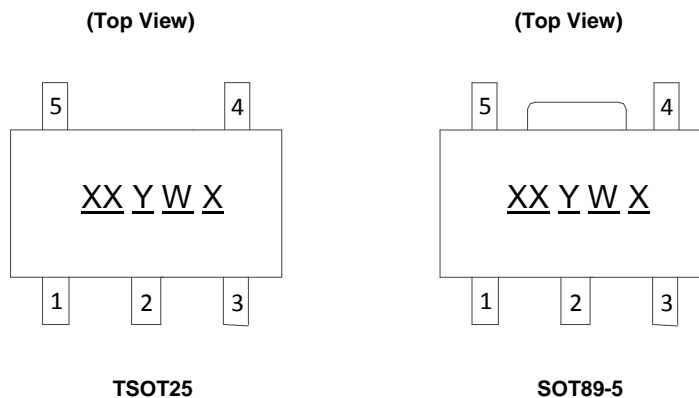


Part Number	Package Code	Package	Tape and Reel	
			Quantity	Part Number Suffix
AL8861WT-7	WT	TSOT25	3,000/Tape & Reel	-7
AL8861Y-13	Y	SOT89-5	2,500/Tape & Reel	-13
AL8861MP-13	MP	MSOP-8EP	2500/Tape & Reel	-13

Note: 9. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

### (1) TSOT25, SOT89-5

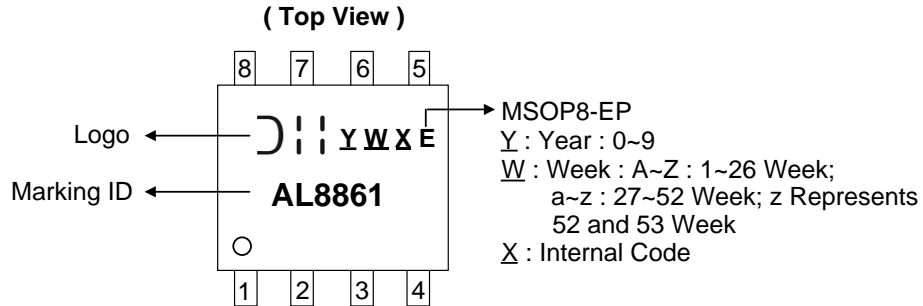


XX : Identification Code  
Y : Year 0 to 9  
W : Week : A to Z : 1 to 26 week;  
           a to z : 27 to 52 week; z represents  
           52 and 53 week  
X : Internal Code

Part Number	Package	Identification Code
AL8861WT-7	TSOT25	A4
AL8861Y-13	SOT89-5	A4

## Marking Information (continued)

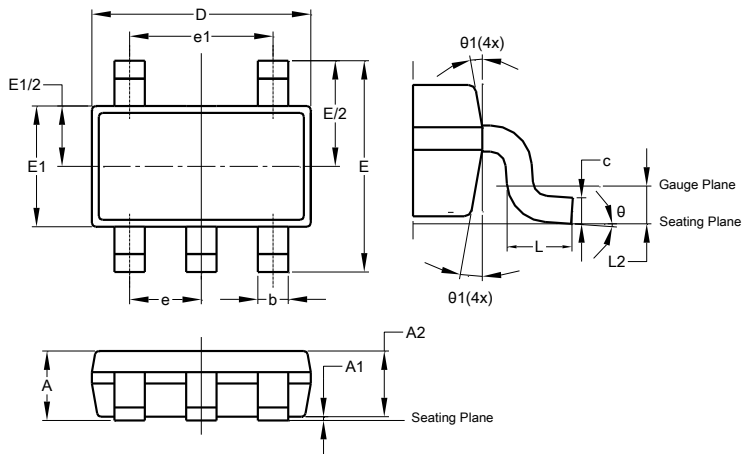
### (2) MSOP-8EP



## Package Outline Dimensions

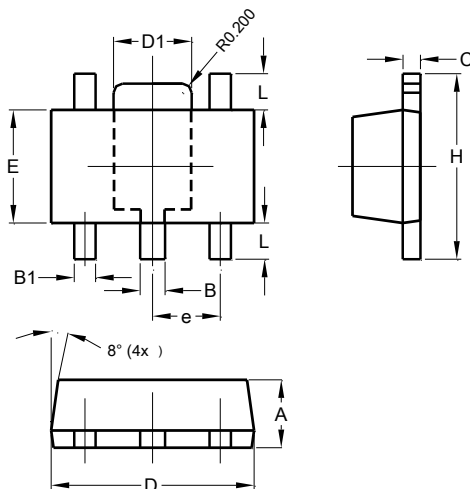
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (1) Package Type: TSOT25



TSOT25			
Dim	Min	Max	Typ
A	-	1.00	-
A1	0.01	0.10	-
A2	0.84	0.90	-
b	0.30	0.45	-
c	0.12	0.20	-
D	-	-	2.90
E	-	-	2.80
E1	-	-	1.60
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.50	-
L2	0.25 BSC		
θ	0°	8°	4°
θ1	4°	12°	-
All Dimensions in mm			

### (2) Package Type: SOT89-5

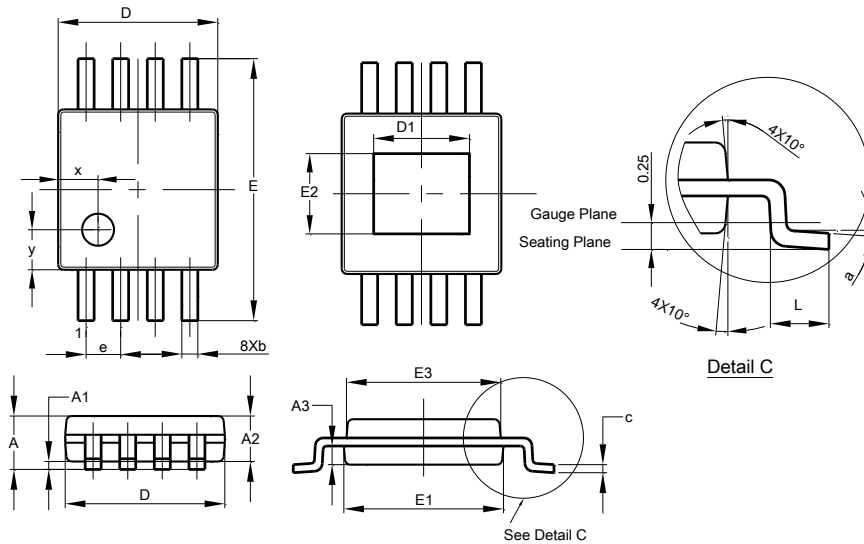


SOT89-5			
Dim	Min	Max	Typ
A	1.40	1.60	1.50
B	0.50	0.62	0.56
B1	0.44	0.54	0.48
C	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.62	1.83	1.733
E	2.40	2.60	2.50
e	-	-	1.50
H	3.95	4.25	4.10
L	0.65	0.95	0.80
All Dimensions in mm			

## Package Outline Dimensions (continued)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (3) Package Type: MSOP-8EP

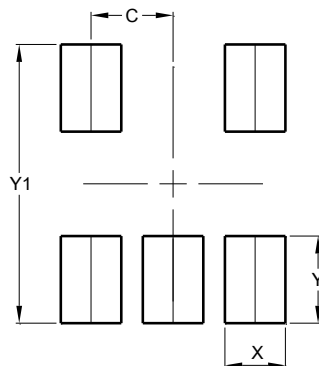


MSOP-8EP			
Dim	Min	Max	Typ
A	-	1.10	-
A1	0.05	0.15	0.10
A2	0.75	0.95	0.86
A3	0.29	0.49	0.39
b	0.22	0.38	0.30
c	0.08	0.23	0.15
D	2.90	3.10	3.00
D1	1.60	2.00	1.80
E	4.70	5.10	4.90
E1	2.90	3.10	3.00
E2	1.30	1.70	1.50
E3	2.85	3.05	2.95
e	-	-	0.65
L	0.40	0.80	0.60
a	0°	8°	4°
x	-	-	0.750
y	-	-	0.750
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (1) Package Type: TSOT25

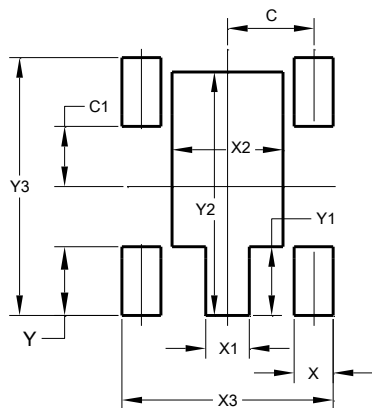


Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199

## Suggested Pad Layout (continued)

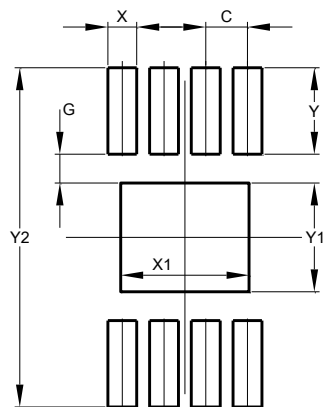
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### (2) Package Type: SOT89-5



Dimensions	Value (in mm)
C	1.500
C1	1.050
X	0.680
X1	0.760
X2	1.930
X3	3.680
Y	1.200
Y1	1.200
Y2	4.250
Y3	4.500

### (3) Package Type: MSOP-8EP



Dimensions	Value (in mm)
C	0.650
G	0.450
X	0.450
X1	2.000
Y	1.350
Y1	1.700
Y2	5.300

## Mechanical Data

### TSOT25

- Moisture Sensitivity: MSL1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per JESD22-B102 ⑥③
- Weight: 0.013 grams (Approximate)

### SOT89-5

- Moisture Sensitivity: MSL3 per J-STD-020
- Terminals: Fin Finish – Matte Tin Plated Leads, Solderable per JESD22-B102 ⑥③
- Weight: 0.059 grams (Approximate)

### MSOP-8EP

- Moisture Sensitivity: MSL1 per J-STD-020
- Terminals: Finish-Matte Tin Plated Leads, Solderable per MIL-STD-202 ⑥③
- Weight: 0.26 grams (Approximate)



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