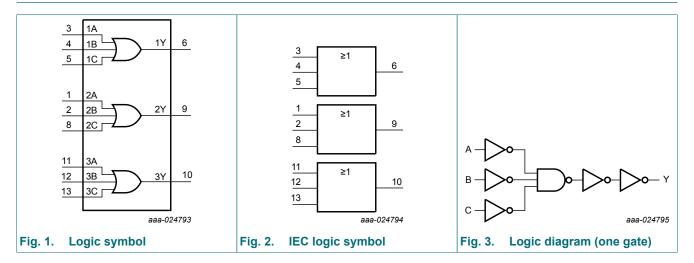
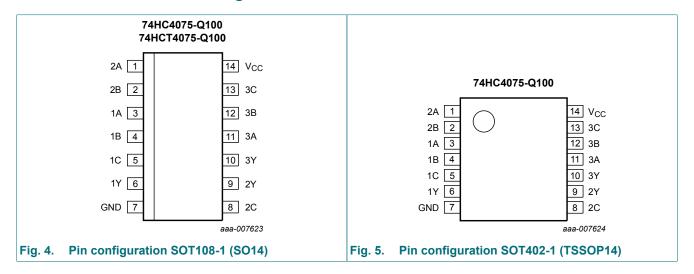
# 4. Functional diagram



# 5. Pinning information

## 5.1. Pinning



## 5.2. Pin description

Table 2. Pin description

Table 2. I III decemption		
Symbol	Pin	Description
1A, 2A, 3A	3, 1, 11	data input
1B, 2B, 3B	4, 2, 12	data input
1C, 2C, 3C	5, 8, 13	data input
1Y, 2Y, 3Y	6, 9, 10	data output
GND	7	ground (0 V)
V <sub>CC</sub>	14	supply voltage

# 6. Functional description

#### **Table 3. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$ 

Inputs			Outputs		
nA	nB	nC	nY L H		
L	L	L	L		
Х	X	Н	Н		
X	Н	X	Н		
Н	X	X	Н		

# 7. Limiting values

#### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7	V
I <sub>IK</sub>	input clamping current	$V_1 < -0.5 \text{ V or } V_1 > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I <sub>OK</sub>	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
Io	output current	-0.5 V < V <sub>O</sub> < V <sub>CC</sub> + 0.5 V	-	±25	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	[2]	-	500	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	74H	IC4075-Q	100	74H	CT4075-0	Q100	Unit
			Min	Тур	Max	Min	Тур	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 2.0 V	-	-	625	-	-	-	ns/V
		V <sub>CC</sub> = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V <sub>CC</sub> = 6.0 V	-	-	83	-	-	-	ns/V

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<sup>[2]</sup> For SO14 package: Ptot derates linearly with 8 mW/K above 70 °C.

For TSSOP14 packages: P<sub>tot</sub> derates linearly with 5.5 mW/K above 60 °C.

# 9. Static characteristics

**Table 6. Static characteristics** 

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74HC40	75-Q100									
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V <sub>CC</sub> = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V <sub>CC</sub> = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
	voltage	V <sub>CC</sub> = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V <sub>CC</sub> = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V
V <sub>OH</sub>	HIGH-level	$V_I = V_{IH}$ or $V_{IL}$								
	output voltage	$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 2.0 $V$	1.9	2.0	-	1.9	-	1.9	-	V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 6.0 $V$	5.9	6.0	-	5.9	-	5.9	-	V
		$I_{O}$ = -4.0 mA; $V_{CC}$ = 4.5 V	3.98	4.32	-	3.84	-	3.7	-	V
		$I_{O}$ = -5.2 mA; $V_{CC}$ = 6.0 V	5.48	5.81	-	5.34	-	5.2	-	V
V <sub>OL</sub>	LOW-level	$V_I = V_{IH}$ or $V_{IL}$								
	output voltage	$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 2.0 $V$	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 6.0 $V$	-	0	0.1	-	0.1	-	0.1	V
		$I_{O}$ = 4.0 mA; $V_{CC}$ = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		$I_{O}$ = 5.2 mA; $V_{CC}$ = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
l <sub>l</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±0.1	-	±1.0	-	±1.0	μΑ
I <sub>CC</sub>	supply current	V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 6.0 V	-	-	2.0	-	20	-	40	μΑ
C <sub>I</sub>	input capacitance		-	3.5	-	-	-	-	-	pF
74HCT4	075-Q100			ı	1			<u>I</u>		
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_I = V_{IH}$ or $V_{IL}$ ; $V_{CC} = 4.5 V$								
	output voltage	I <sub>O</sub> = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA	3.98	4.32	-	3.84	-	3.7	-	V
V <sub>OL</sub>	LOW-level	$V_I = V_{IH}$ or $V_{IL}$ ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι <sub>Ο</sub> = 20 μΑ	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA	-	0.15	0.26	-	0.33	-	0.4	٧
l <sub>l</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±0.1	-	±1.0	-	±1.0	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$ ; $I_O = 0 \text{ A}$	-	-	2.0	-	20	-	40	μΑ

74HC\_HCT4075\_Q100

Symbol	Parameter	Conditions		25 °C			C to	-40 ° +12	Unit	
			Min	Тур	Max	Min	Max	Min	Max	
ΔI <sub>CC</sub>	additional supply current	per input pin; $V_I = V_{CC} - 2.1 \text{ V}$ ; other inputs at $V_{CC}$ or GND; $V_{CC} = 4.5 \text{ V}$ to 5.5 V; $I_O = 0 \text{ A}$								
		nA, nB, nC inputs	-	150	540	-	675	-	735	μΑ
Cı	input capacitance		-	3.5	-	-	-	-	-	pF

# 10. Dynamic characteristics

## **Table 7. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V);  $C_L$  = 50 pF unless otherwise specified; for test circuit see Fig. 7.

<b>74HC407</b> t <sub>pd</sub>	Parameter	Conditions			25 °C		-40	0 °C to +′	125 °C	Unit
				Min	Тур	Max	Min	Max (85 °C)	Max (125 °C)	
74HC407	75-Q100									
t <sub>pd</sub>	propagation	nA, nB, nC to nY; see Fig. 6	[1]							
	delay	V <sub>CC</sub> = 2.0 V		-	28	100	-	125	150	ns
		V <sub>CC</sub> = 4.5 V		-	10	20	-	25	30	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF		-	8	-	-	-	-	ns
		V <sub>CC</sub> = 6.0 V		-	8	17	-	21	26	ns
t <sub>t</sub>	transition time	see Fig. 6	[2]							
		V <sub>CC</sub> = 2.0 V		-	19	75	-	95	110	ns
		V <sub>CC</sub> = 4.5 V		-	7	15	-	19	22	ns
		V <sub>CC</sub> = 6.0 V		-	6	13	-	16	19	ns
$C_{PD}$	power dissipation capacitance	per package; $V_I$ = GND to $V_{CC}$	[3]	-	28	-	-	-	-	pF
74HCT4	075-Q100									
t <sub>pd</sub>	propagation	nA, nB, nC to nY; see Fig. 6	[1]							
	delay	V <sub>CC</sub> = 4.5 V		-	12	24	-	30	36	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF		-	10	-	-	-	-	ns
t <sub>t</sub>	transition time	V <sub>CC</sub> = 4.5 V; see <u>Fig. 6</u>	[2]	-	7	15	-	19	22	ns
C <sub>PD</sub>	power dissipation capacitance	per package; V <sub>I</sub> = GND to V <sub>CC</sub> - 1.5 V	[3]	-	32	-	-	-	-	pF

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

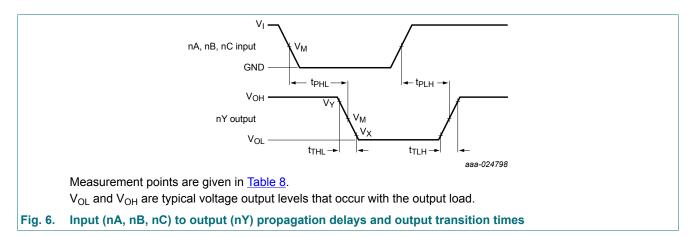
V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0) = \text{sum of outputs.}$ 

<sup>[3]</sup>  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ):

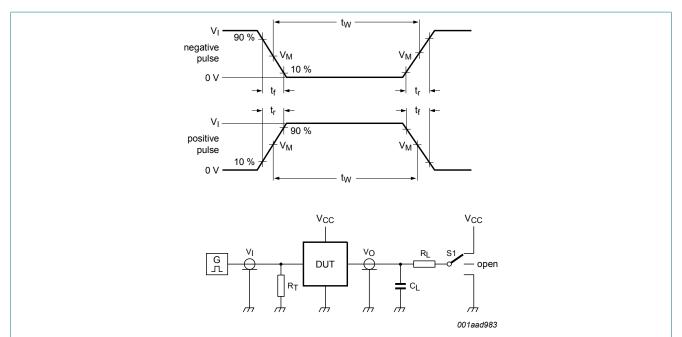
## 10.1. Waveforms and test circuit



**Table 8. Measurement points** 

Туре	Input	Output	Dutput				
	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>			
74HC4075-Q100	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>			
74HCT4075-Q100	1.3 V	1.3 V	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>			

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Test data is given in Table 9.

Definitions test circuit:

 $R_T$  = Termination resistance should be equal to output impedance  $Z_0$  of the pulse generator.

C<sub>L</sub> = Load capacitance including jig and probe capacitance.

R<sub>L</sub> = Load resistance.

S1 = Test selection switch

## Fig. 7. Test circuit for measuring switching times

Table 9. Test data

Туре	Input		Load	S1 position	
	V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	CL	R <sub>L</sub>	t <sub>PHL</sub> , t <sub>PLH</sub>
74HC4075-Q100	V <sub>CC</sub>	6 ns	15 pF, 50 pF	1 kΩ	open
74HCT4075-Q100	3 V	6 ns	15 pF, 50 pF	1 kΩ	open

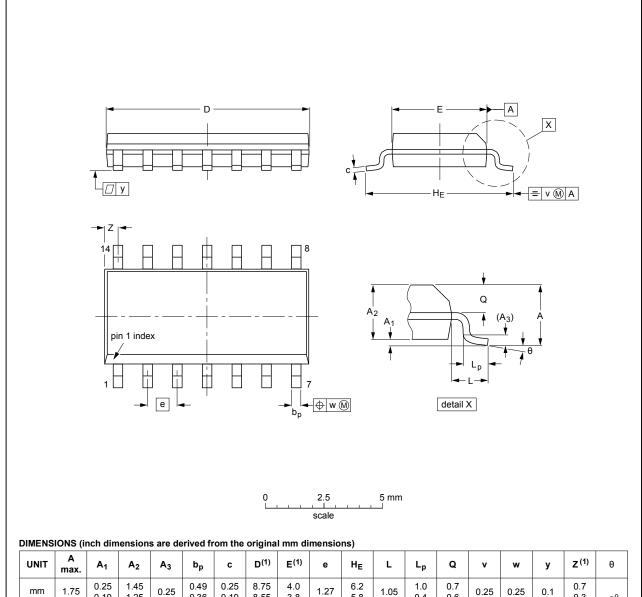
**Product data sheet** 

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# 11. Package outline

## SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



UN	IT	A ax.	A <sub>1</sub>	A <sub>2</sub>	<b>A</b> <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	HE	L	Lp	q	v	w	у	Z <sup>(1)</sup>	θ
mı	n 1.3	.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inch	nes 0.0	069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.35 0.34	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	0°

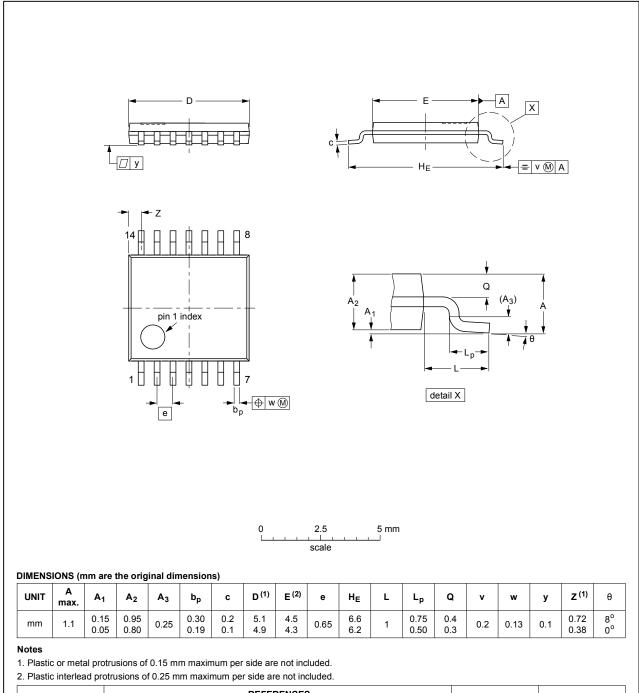
1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT108-1	076E06	MS-012			<del>99-12-27</del> 03-02-19

Package outline SOT108-1 (SO14)

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT402-1		MO-153			<del>-99-12-27-</del> 03-02-18

Fig. 9. Package outline SOT402-1 (TSSOP14)

# 12. Abbreviations

## **Table 10. Abbreviations**

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model
TTL	Transistor-Transistor Logic

# 13. Revision history

## Table 11. Revision history

Table 11. Nevision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
74HC_HCT4075_Q100 v.2	20190204	Product data sheet	-	74HC_HCT4075_Q100 v.1			
Modifications:	of Nexperia. Legal texts h Type number	ave been adapted to the ne r 74HCT4075PW-Q100 (SC Q100; added value for V <sub>OL</sub>	ew company name DT402-1) removed	d.			
74HC_HCT4075_Q100 v.1	20130522	Product data sheet	-	-			

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## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 4 February 2019

**Product data sheet** 

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