

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{PK}	40	W
Maximum Peak Pulse Current ($t_p = 8/20\mu s$)	I_{PP}	5	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V_{ESD}	± 20 ± 15	kV
Operating Temperature	T_{OP}	-40 to +85	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

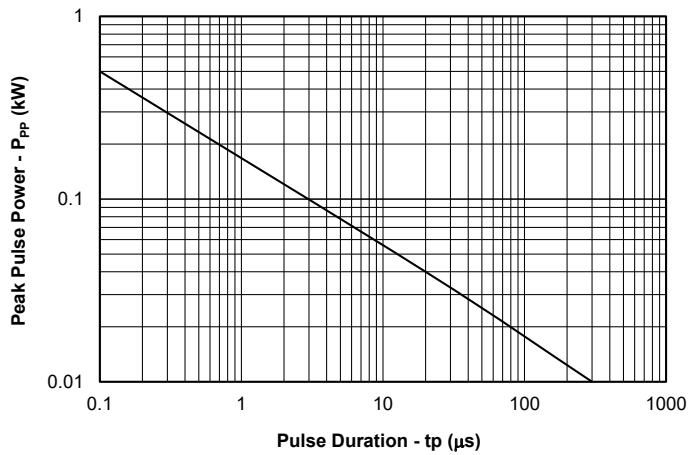
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V_{RWM}				2.5	V
Punch Through Voltage	V_{PT}	$I_{PT} = 2\mu A$	2.7	3.1	3.6	V
Snap-Back Voltage	V_{SB}	$I_{SB} = 50mA$	2.8			V
Reverse Leakage Current	I_R	$V_{RWM} = 2.5V$		0.01	0.05	μA
Clamping Voltage	V_C	$I_{PP} = 1A, t_p = 8/20\mu s$			5	V
		$I_{PP} = 5A, t_p = 8/20\mu s$			7.5	
Forward Voltage	V_F	$I_{PP} = 1A, t_p = 8/20\mu s$			2.4	
Junction Capacitance	C_J	$V_R = 0V, f = 1MHz, \text{Pin 2 to 1}$		25	30	pF
		$V_R = 2.5V, f = 1MHz, \text{Pin 2 to 1}$		14		

Notes:

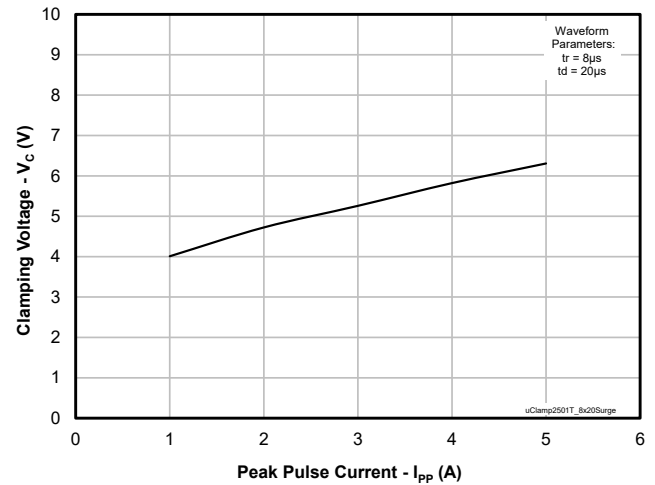
1) ESD gun return path connected to ESD ground plane.

Typical Characteristics

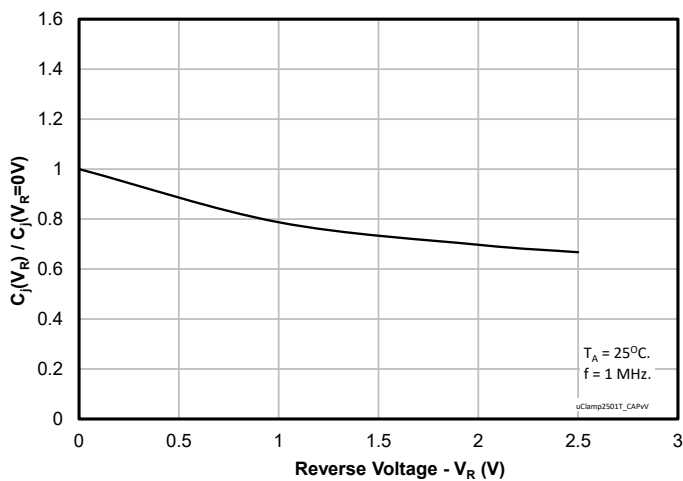
Non-Repetitive Peak Pulse Power vs. Pulse Time



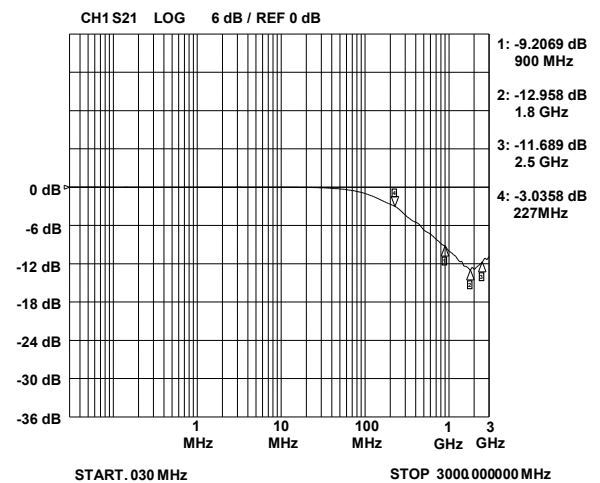
Clamping Voltage vs. Peak Pulse Current ($t_p=8/20\mu$ s)



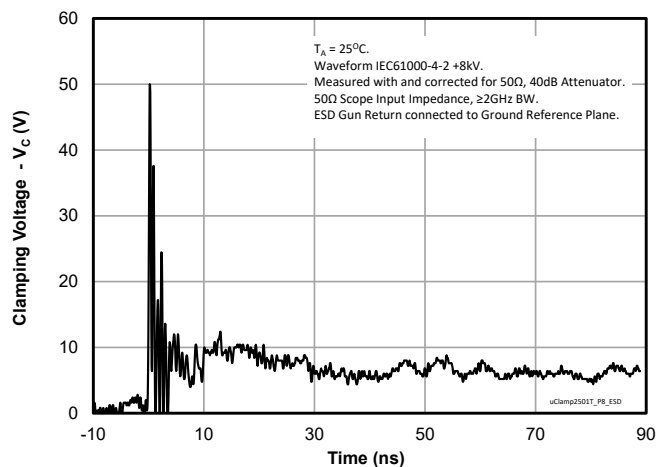
Normalized Capacitance vs. Voltage



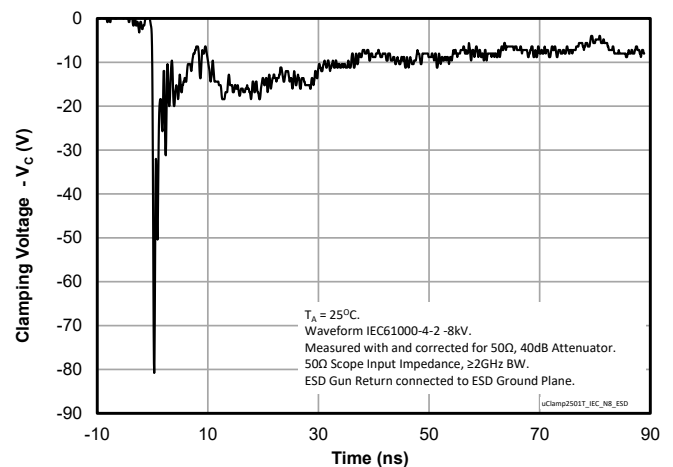
Insertion Loss (S21)



ESD Clamping (+8kV Contact per IEC 61000-4-2)



ESD Clamping (-8kV Contact per IEC 61000-4-2)



Application Information

Device Connection Options

The μ Clamp2501T is designed to protect one data line operating up to 2.5 volts. It will present a high impedance up to 2.5 volts. It will start conducting when the line voltage exceeds 2.7 volts. The device is unidirectional and may be used on lines where the signal polarity is above ground. These devices are not recommended for use on DC power supply lines due to their snap-back voltage characteristic.

EPD TVS Characteristics

These devices are constructed using Semtech's proprietary EPD technology. The structure of the EPD TVS is vastly different from the traditional pn junction devices. At voltages below 5V, high leakage current and junction capacitance render the conventional avalanche technology impractical for most applications. By utilizing the EPD technology, these devices can effectively operate at 2.5V while maintaining excellent electrical characteristics.

The EPD TVS employs a complex npnp structure in contrast to the pn structure normally found in traditional silicon-avalanche TVS diodes. The EPD mechanism is achieved by engineering the center region of the device such that the reverse biased junction does not avalanche, but will "punch-through" to a conducting state. This structure results in a device with superior DC electrical parameters at low voltages while maintaining the capability to absorb high transient currents.

Circuit Board Layout Recommendations for Suppression of ESD

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

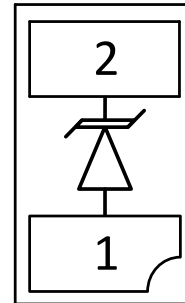
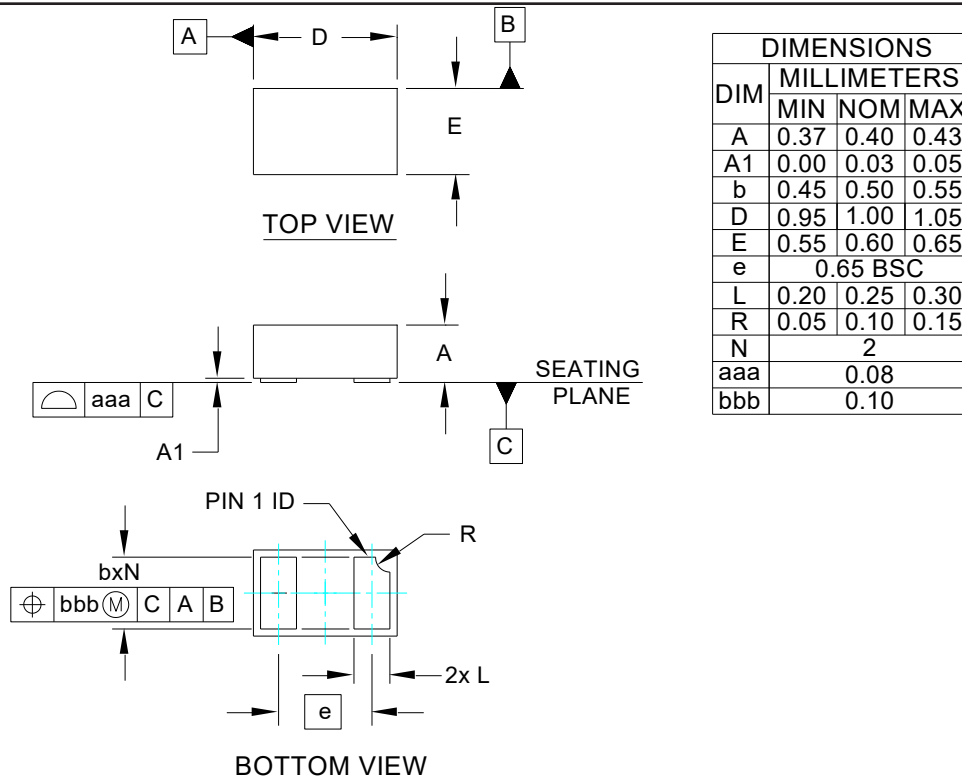


Figure 1 - Device Schematic

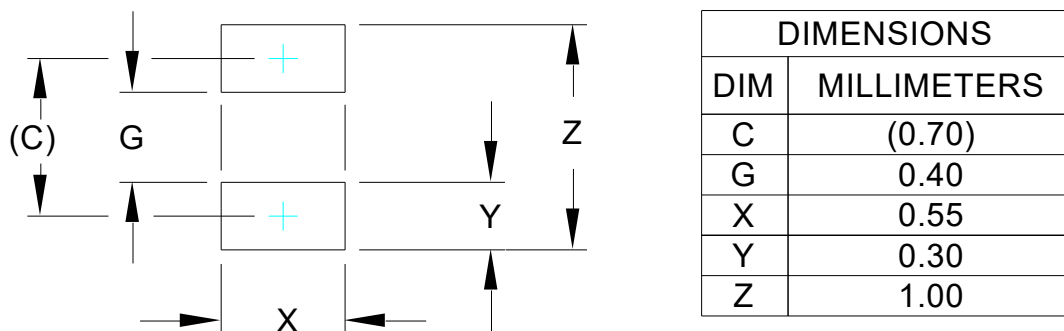
Outline Drawing - SLP1006P2



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

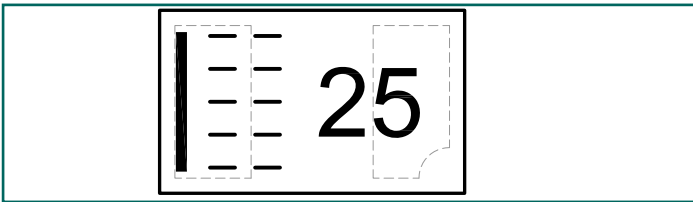
Land Pattern - SLP1006P2



NOTES:

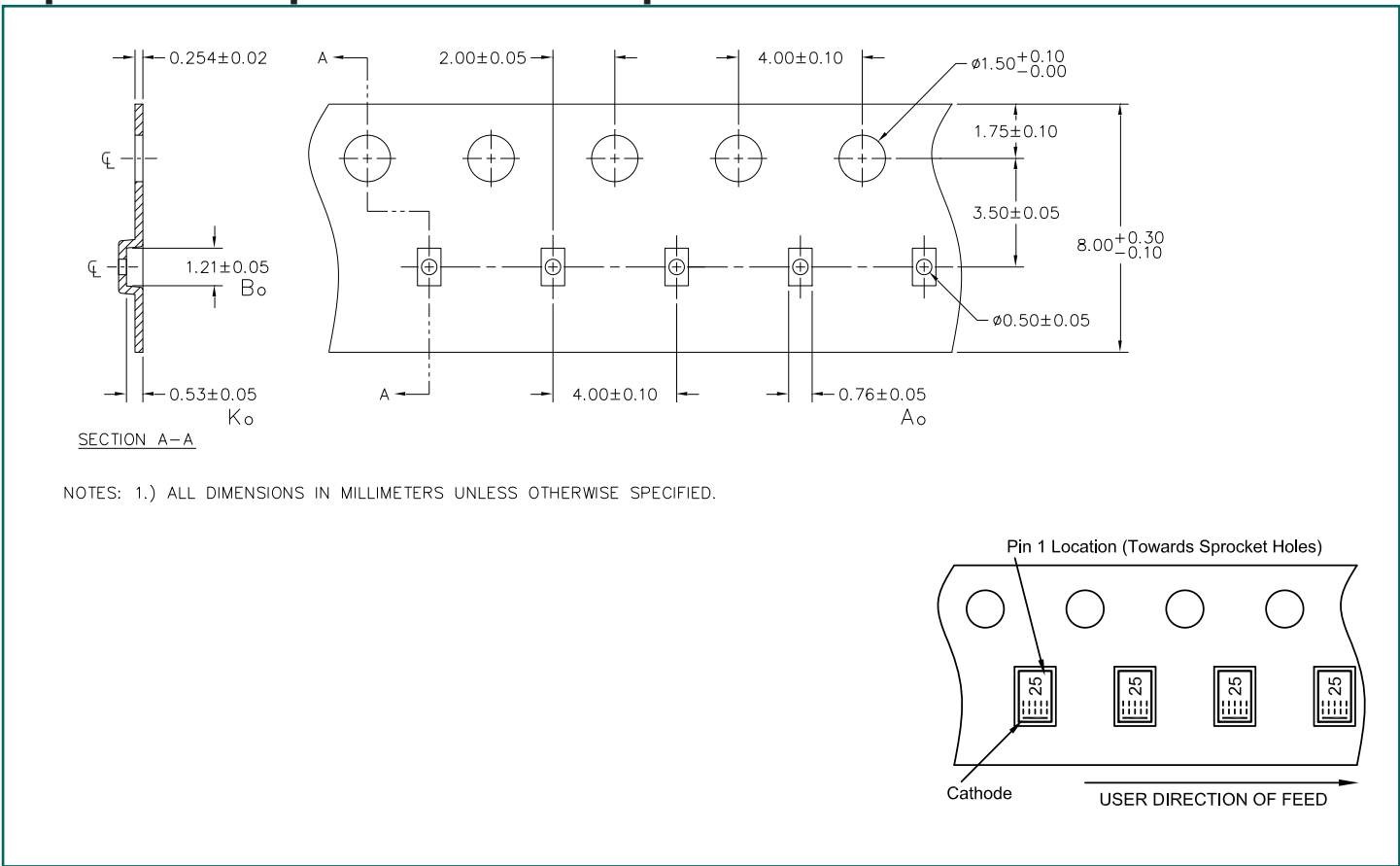
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Marking Code



- Notes:
- 1. Chamfer indicated Pin 1.
 - 2. Bar indicates Pin 2 Cathode location.

Tape and Reel Specification: uClamp2501T.TCT



Ordering Information

Part Number	Qty per Reel	Reel Size
uClamp2501T.TCT	3,000	7"



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