**ABSOLUTE MAXIMUM RATINGS**These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

V <sub>00</sub>	+7V
V <sub>cc</sub> Input Voltages	
Drivers	0.5V to (V <sub>CC</sub> +0.5V)
	±14V
Output Voltages	
Drivers	±14V
Receivers	0.5V to (V <sub>cc</sub> +0.5V)
Storage Temperature	65°C to +150°
Power Dissipation	1000mW

# **ELECTRICAL CHARACTERISTICS**

 $T_{MIN}$  to  $T_{MAX}$  and  $V_{CC}$  = 5V ± 5% unless otherwise noted.

PARAMETERS	MIN.	TYP.	MAX.	UNITS	CONDITIONS
SP1490E/1491E DRIVER DC Characteristics Differential Output Voltage Differential Output Voltage	3.5 2		V <sub>cc</sub> V <sub>cc</sub>	Volts Volts	Unloaded; $R = \infty$ ; see figure 1 With Load; $R = 50\Omega$ ; (RS422);
Differential Output Voltage figure 1 Change in Magnitude of Driver Differential Output Voltage for Complimentary States Driver Common-Mode Output Voltage Input High Voltage Input Low Voltage Input Current Driver Short-Circuit Current Vout = HIGH Vout = LOW	2.0		0.2 3 0.8 ±10 ±250 ±250	Volts Volts Volts Volts Volts Volts pA mA	see figure 1 With Load; $R = 27\Omega$ ; (RS485); see $R = 27\Omega \text{ or } R = 50\Omega; \text{ see figure 1}$ $R = 27\Omega \text{ or } R = 50\Omega; \text{ see figure 1}$ Applies to D Applies to D Applies to D $-7V \le V_o \le +12V$ $-7V \le V_o \le +12V$
SP1490E/1491E DRIVER AC Characteristics Maximum Data Rate Driver Input to Output Driver Input to Output Driver Skew Driver Rise or Fall Time SP1491E only Driver Enable to Output High Driver Enable to Output Low Driver Disable Time from Low Driver Disable Time from High	20	30 30 8 20 40 40 40	40 40 5 20 70 70 70	Mbps ns ns ns ns ns ns	$\begin{split} &t_{_{R/F}}; R_{_{DIFF}} = 54\Omega, C_{_{L1}} = C_{_{L2}} = 100pF;\\ \text{see figures 3 and 6}\\ &t_{_{R/F}}; R_{_{DIFF}} = 54\Omega, C_{_{L1}} = C_{_{L2}} = 100pF;\\ \text{see figures 3 and 6}\\ \text{see figures 3 and 6},\\ &t_{_{R/F}} =  t_{_{PLH}} - t_{_{PHL}} \\ &\text{From 10\% to 90\%; } R_{_{DIFF}} = 54\Omega,\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures 3 and 6}\\ &t_{_{R/F}}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{1}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{1}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{1}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &4 \text{ and 7; } S_{_{2}} \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &6 \text{ closed}\\ &C_{_{L1}} = C_{_{L2}} = 100pF; \text{ see figures}\\ &C_{_{L2}} = 100pF; \text{ see figures}\\ &C_{_{L3}} = C_{_{L3}} = 100pF; \text{ see figures}\\ &C_{_{L3}} = 100pF; \text{ see figures}\\ &C_{_{L3}} = 100pF;  see $

SP1490E/1491E RECEIVER					
DC Characteristics					
Differential Input Threshold	0.2		+0.2	Volts	-7V ≤ V <sub>CM</sub> ≤ 12V
Input Hysteresis		70		mV	$V_{CM} = 0 \tilde{V}$
Output Voltage High	3.5			Volts	$I_0 = -4mA, V_{1D} = +200mV$
Output Voltage Low			0.4	Volts	$I_0 = +4mA, \ V_{1D} = -200mV$
Input Resistance	12	15		kΩ	-7V ≤ V <sub>CM</sub> ≤ 12V (1 unit load)
Input Current (A, B); V <sub>IN</sub> = 12V			±1.0	mA	$V_{IN} = 12\widetilde{V}$ $V_{IN} = -7V$
Input Current (A, B); $V_{IN} = -7V$			-0.8	mA	$V_{IN}^{m} = -7V$
Short-Circuit Current			85	mA	$0\ddot{V} \le V_0 \le V_{CC}$
PARAMETERS	MIN.	TYP.	MAX.	UNITS	CONDITIONS
SP1490E/1491E RECEIVER					
AC Characteristics					
Maximum Data Rate	20			Mbps	
Receiver Input to Output	20	45	50	ns	t · R = 540
recourse input to Gutput		10		110	$t_{PLH}$ ; $R_{DIFF} = 54\Omega$ , $C_{L1} = C_{L2} = 100pF$ ; Figures 3 & 8
Receiver Input to Output	20	45	50	ns	t · R = 540
receiver input to output		10		110	$t_{PHL}$ ; $R_{DIFF} = 54\Omega$ , $C_{L1} = C_{L2} = 100 pF$ ; Figures 3 & 8
Diff. Receiver Skew It <sub>PI H</sub> -t <sub>PHI</sub> I		5	10	ns	$R_{\text{DIFF}} = 54\Omega$ ; $C_{1,1} = C_{1,2} = 100\text{pF}$ ;
Receiver Tplh/Tphl		40	70	ns	Table   Tabl
receiver rpiiii rpiii			, 0	110	
POWER REQUIREMENTS					
Supply Voltage	+4.75		+5.25	Volts	
Supply Current	74.73	900	+3.23	μΑ	
,		300		μΛ	
ENVIRONMENTAL AND					
MECHANICAL					
Operating Temperature					
Commercial (_C_)	0		+70	°C	
Industrial (_E_)	-40		+85	°C	
Storage Temperature	-65		+150	°C	

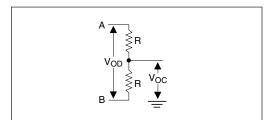


Figure 1. Driver DC Test Load Circuit

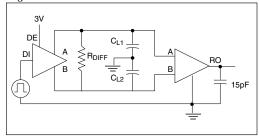


Figure 3. Driver/Receiver Timing Test Circuit

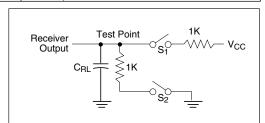


Figure 2. Receiver Timing Test Load Circuit

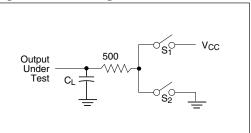


Figure 4. Driver Timing Test Load #2 Circuit

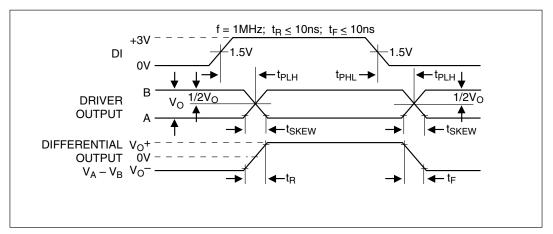


Figure 6. Driver Propagation Delays

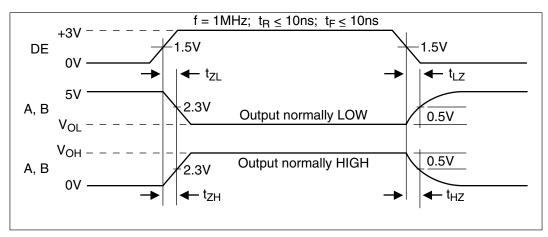


Figure 7. Driver Enable and Disable Times SP1491E only

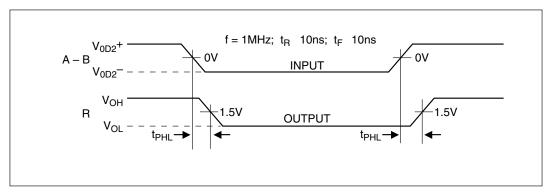


Figure 8. Receiver Propagation Delays

The SP1490E and SP1491E are full-duplex differential transceivers that meet the requirements of RS-485 and RS-422. Fabricated with a Exar proprietary BiCMOS process, both products require a fraction of the power of older bipolar designs.

The RS-485 standard is ideal for multi-drop applications or for long-distance interfaces. RS-485 allows up to 32 drivers and 32 receivers to be connected to a data bus, making it an ideal choice for multi-drop applications. Since the cabling can be as long as 4,000 feet, RS-485 transceivers are equipped with a wide (-7V to +12V) common mode range to accommodate ground potential differences. Because RS-485 is a differential interface, data is virtually immune to noise in the transmission line.

#### Driver...

The drivers for both the SP1490E and SP1491E have differential outputs. The typical voltage output swing with no load will be 0 volts to +5 volts. With worst case loading of  $54\Omega$  across the differential outputs, the driver can maintain greater than 1.5V voltage levels.

The driver of the SP1491E has a driver enable control line which is active high. A logic high on DE (pin 4) of the SP1491E will enable the differential driver outputs.

INPUTS	OUTPUTS		
DE SP1491E only	DI	Y	Z
1	1	1	0
1	0	0	1
0	X	Z	z

Transmit Function Truth Table

A logic low on DE (pin 4) of the SP1491E will tri-state the driver outputs. The SP1490E does not have a driver enable.

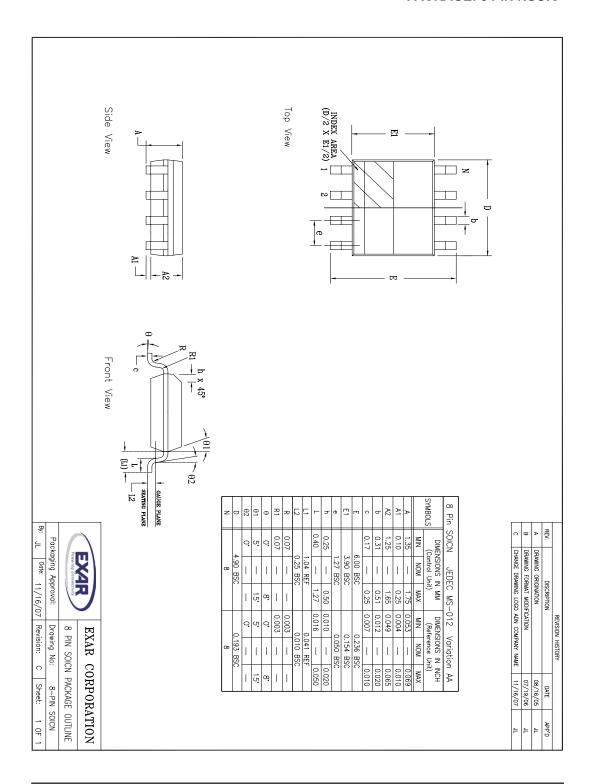
#### Receiver...

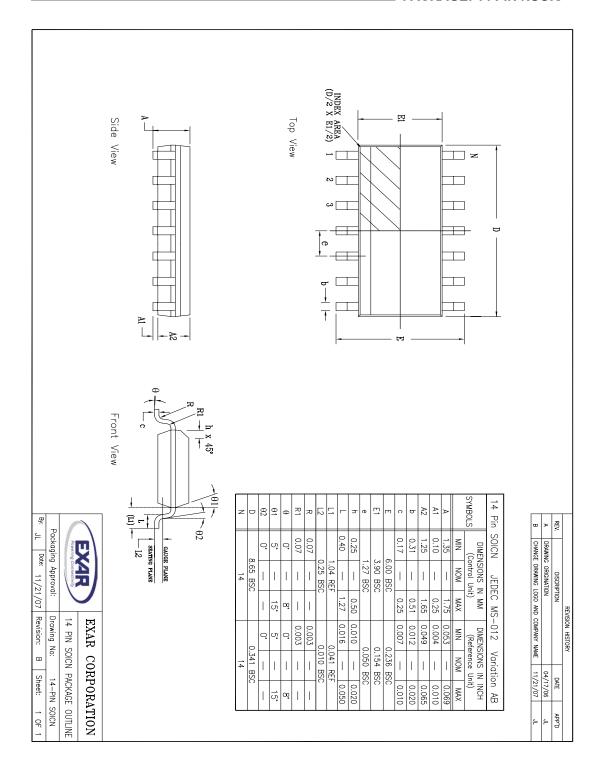
The receivers for both the SP1490E and SP1491E have differential inputs with an input sensitivity as low as  $\pm 200 \text{mV}$ . Input impedance of the receivers is typically  $15 \text{K}\Omega$  (12K $\Omega$  minimum). A wide common mode range of -7V to +12V allows for large ground potential differences between systems. The receivers for both the SP1490E and SP1491E are equipped with the fail-safe feature. Fail-safe guarantees that the receiver output will be in a high state when the input is left unconnected and floating.

The receiver of the SP1491E has a receiver enable control line which is active low. A logic low on  $\overline{REB}$  (pin 3) of the SP1491E will enable the differential receiver. A logic high on  $\overline{REB}$  (pin 3) of the SP1491E will tri-state the receiver.

INPUTS	OUTPUTS	
RE SP1491E only	A-B	R
0	+0.2V	1
0	-0.2V	0
0	open	1
1	X	Z

Recieve Function Truth Table





## ORDERING INFORMATION

Part Number	TopMark	Temperature Range	Package
SP1490ECN-L	1490ECNYYWW	0°C to +70°C	8-Pin NSOIC
SP1490ECN-L/TR	1490ECNYYWW	0°C to +70°C	8-Pin NSOIC
SP1490EEN-L	SP1490EENYYWW	40°C to +85°C	8-Pin NSOIC
SP1490EEN-L/TR	SP1490EENYYWW	40°C to +85°C	8-Pin NSOIC
SP1491ECN-L	SP1491ECNYYWW	0°C to +70°C	14-Pin NSOIC
SP1491ECN-L/TR	SP1491ECNYYWW	0°C to +70°C	14-Pin NSOIC
SP1491EEN-L	SP1491EENYYWW	40°C to +85°C	14-Pin NSOIC
SP1491EEN-L/TR	SP1491EENYYWW	40°C to +85°C	14-Pin NSOIC

### /TR = Tape and Reel

Pack quantity is 2500 for Narrow SOIC.

DATE	REVISION	DESCRIPTION
03/08/07	J	Legacy Sipex Datasheet
06/12/09	1.0.0	Convert to Exar format, update ordering information and change revision to 1.0.0
05/24/13	1.0.1	Correct type error per PCN 13-0503-01 ECN: 1322-02 05/24/13

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