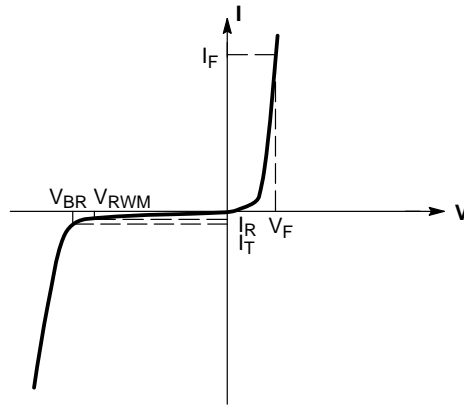


DF6A6.8FUT1



V-I Curve

ELECTRICAL CHARACTERISTICS

Device	Device Marking	Breakdown Voltage V_{BR} @ 5 mA (Volts)			Leakage Current I_{RM} @ $V_{RWM} = 5$ V (μ A)	Typical Capacitance @ 0 V Bias (pF)	Max V_F @ $I_F = 10$ mA (V)	Max Z_Z @ 5 mA (Ω)	Max Z_{ZK} @ 0.5 mA (Ω)
		Min	Nom	Max					
DF6A6.8FUT1	68	6.4	6.8	7.2	1.0	40	1.25	30	300

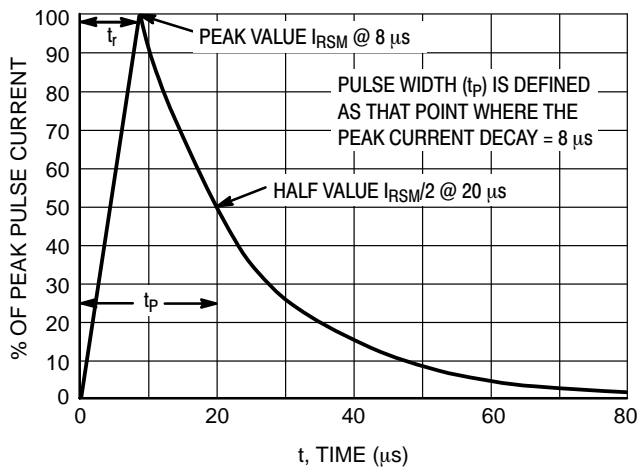


Figure 1. $8 \times 20 \mu s$ Pulse Waveform

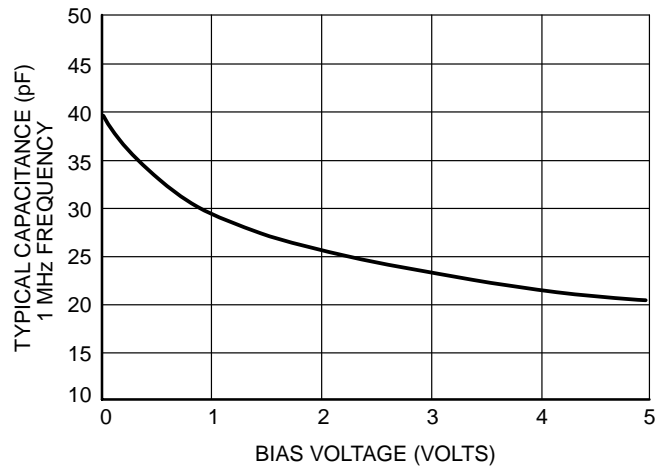


Figure 2. Capacitance

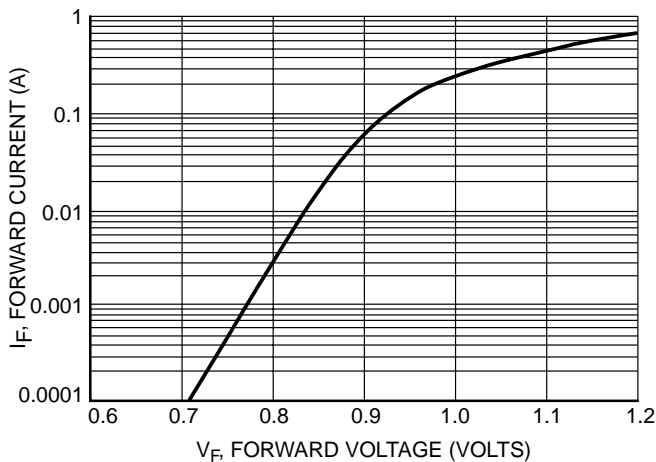


Figure 3. Forward Voltage

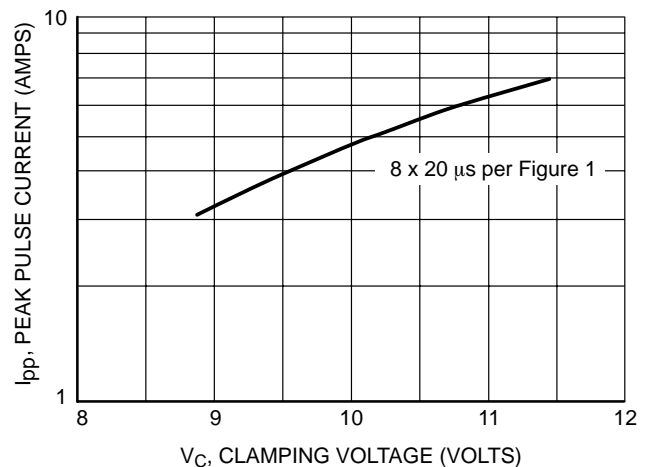
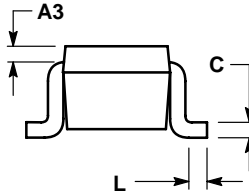
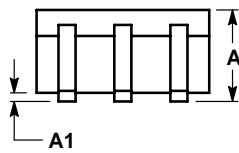
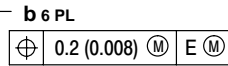
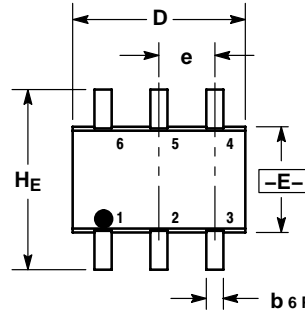


Figure 4. Clamping Voltage versus Peak Pulse Current

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PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363
CASE 419B-02
ISSUE V

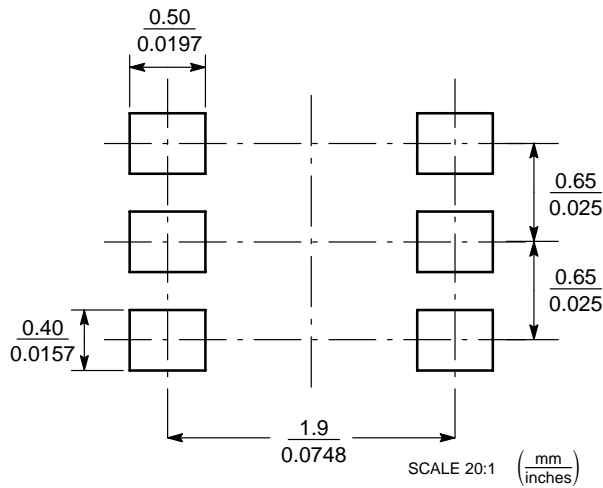


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
C	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
E	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65 BSC			0.026 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086


SOLDERING FOOTPRINT*



SC-88/SC70-6/SOT-363

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

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