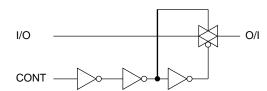
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Logic Diagram

1/4 TC4066B



Absolute Maximum Ratings

Characteristics	Symbol	Rating	Unit
DC supply voltage	V _{DD}	V _{SS} - 0.5 to V _{SS} + 20	V
Control input voltage	VCIN	V _{SS} - 0.5 to V _{DD} + 0.5	V
Switch I/O voltage	VI/VO	V _{SS} - 0.5 to V _{DD} + 0.5	V
Power dissipation	PD	300 (DIP)/180 (SOP/TSSOP)	mW
Potential difference across I/O during ON	VI - VO	±0.5	V
Control input current	ICIN	±10	mA
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Operating Ranges (V_{SS} = 0 V)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V _{DD}	—	3	_	18	V
Input/Output voltage	VIN/VOUT	—	0	_	V _{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused control inputs must be tied to either V_{DD} or V_{SS} .

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Electrical Characteristics (V_{SS} = 0 V, unless specified otherwise)

Characteristics			Test Condition		-40°C		25°C			85°C		
		Symbol		V _{DD} (V)	Min	Max	Min	Typ. Max Min Max		Max	Unit	
Control in high volta		VIH	I _{IS} = 10 μΑ	5 10 15	3.5 7.0 11.0		3.5 7.0 11.0	2.75 5.50 8.25		3.5 7.0 11.0		V
Control in voltage	nput low	VIL	I _{IS} = 10 μΑ	5 10 15		1.5 3.0 4.0		2.25 4.50 6.75	1.5 3.0 4.0		1.5 3.0 4.0	V
On-state resistanc		Ron	0 ≤ V _{IS} ≤ VDD R _L = 10 kΩ	5 10 15	 	800 210 140		290 120 85	950 250 160		1200 300 200	Ω
∆On-stat resistanc (between switches)	ce n any 2	RonΔ	_	5 10 15				10 6 4				Ω
Input/out leakage		IOFF	VIN = 18 V, VOUT = 0 V VIN = 0 V, VOUT = 18 V	18 18	_	±100 ±100	_	±0.1 ±0.1	±100 ±100	_	±1000 ±1000	nA
Quiescer supply cu		IDD	VIN = VSS, VDD (Note 1)	5 10 15		0.25 0.50 1.00		0.001 0.001 0.002	0.25 0.50 1.00		7.5 15.0 30.0	μA
Control Input current	"H" level	Ιн	VIH = 18 V	18		0.1	_	10 ⁻⁵	0.1	_	1.0	μA
	"L" level	ΙL	VIL = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ

Note 1: All valid input combinations.

Switching Characteristics (Ta = 25°C)

		Test Condition				Min			
Characteristics	Symbol			V _{SS} V _{DD} (V) (V)			Тур.	Max	Unit
Phase difference between input to output	φ ι -Ο	C _L = 50 pF		0 0 0	5 10 15	 	15 8 5	40 20 15	ns
Propagation delay time (control-OUT)	t _P ZL t _P ZH	R _L = 1 kΩ C _L = 50 pF		0 0 0	5 10 15		55 25 20	120 40 30	ns
Propagation delay time (control -OUT)	t _{pLZ} t _{pHZ}	RL = 1 kΩ CL = 50 pF		0 0 0	5 10 15		45 30 25	80 70 60	ns
Max control input repetition rate	f _{max} (C)	R _L = 1 kΩ C _L = 50 pF		0 0 0	5 10 15		10 12 12		MHz
-3dB cutoff frequency	f _{max} (I-O)	RL = 1 kΩ CL = 15 pF	(Note 1)	-5	5	_	30	_	MHz
Total harmonic distortion	_	RL = 10 kΩ f = 1 kHz	(Note 2)	-5	5	_	0.03	_	%
-50dB feed through frequency	_	RL = 1 kΩ	(Note 3)	-5	5	_	600	_	kHz
-50dB crosstalk frequency	-	RL = 1 kΩ	(Note 4)	-5	5	_	1	_	MHz
Crosstalk (control-OUT)	_	R _{IN} = 1 kΩ R _{OUT} = 10 kΩ C _L = 15 pF		0 0 0	5 10 15		200 400 600		mV
Input capacitance	CIN	Control input				—	5	7.5	pF
		Switch I/O			10	—			
Feed through capacitance	CIN-OUT		_			—	0.5	—	pF

Note 1: Sine wave of ±2.5 V_{p-p} shall be used for V_{IS} and the frequency of 20 log 10 $\frac{V_{OS}}{V_{IS}}$ = -3 dB shall be fmax.

Note 2: V_{IS} shall be sine wave of ±2.5 V_{p-p}

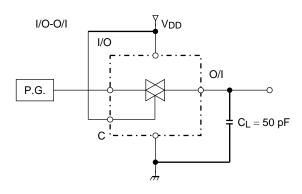
Note 3: Sine wave of ±2.5 V_{p-p} shall be used for V_{IS} and the frequency of 20 log 10 $\frac{V_{OS}}{V_{IS}}$ = -50 dB shall be feed-through.

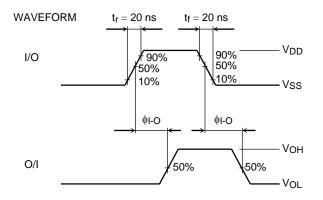
Note 4: Sine wave of ±2.5 V_{p-p} shall be used for V_{IS} and the frequency of 20 log 10 $\frac{V_{OS}}{V_{IS}}$ = -50 dB shall be crosstalk.

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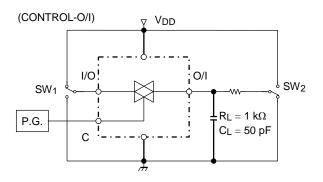
Circuit for Measurement of Electrical Characteristics

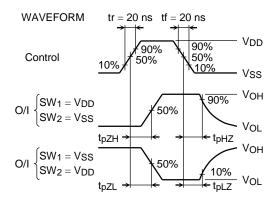
1. φι-ο



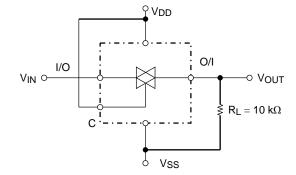


2. tpZL, tpZH, tpLZ, tpHZ





3. RON



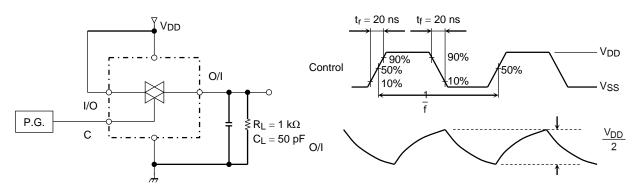
RON Calculation Method

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R_{ON} = 10 \times \frac{(V_{IN} - V_{OUT})}{V_{OUT}} \left[ k \Omega \right]
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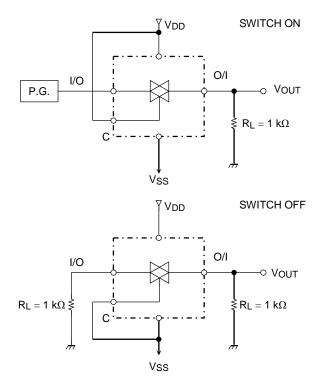
4. fmax (C)

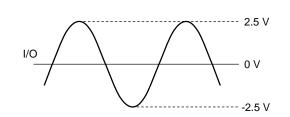
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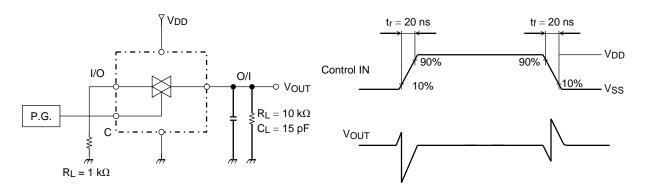


5. Crosstalk between Any Two Switches

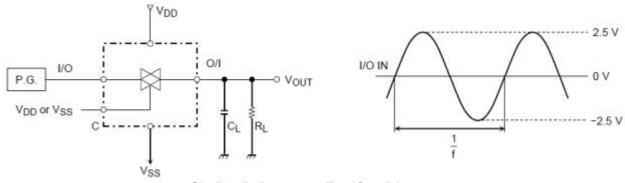


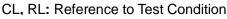


6. Crosstalk, Control to Input



7. Total Harmonic Distortion, fmax (I-O), Feedthrough (Switch OFF)



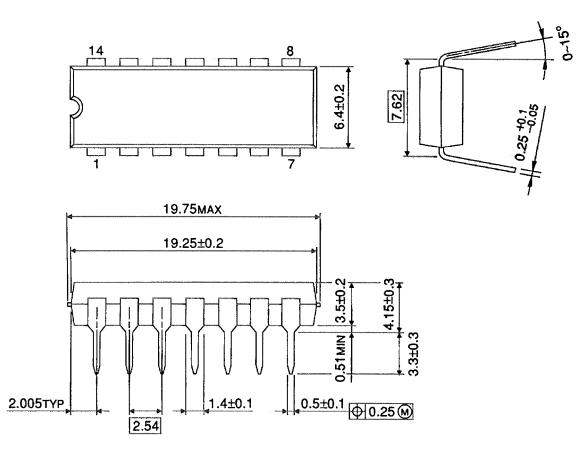




Package Dimensions

DIP14-P-300-2.54

Unit : mm



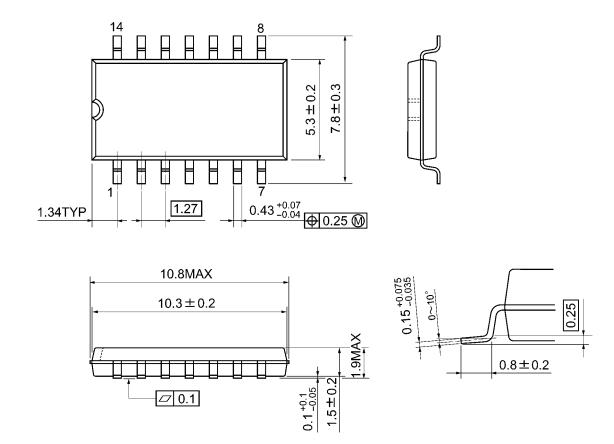
Weight: 0.96 g (typ.)



Package Dimensions

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

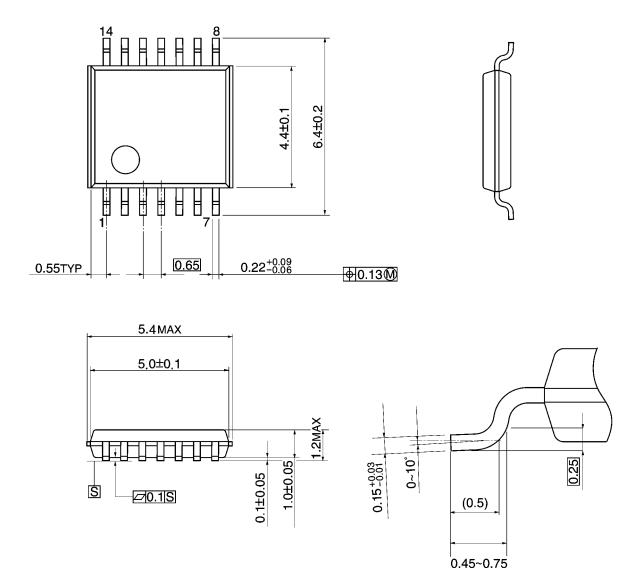


Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm

TC4066BP/BF/BFT



Weight: 0.06 g (typ.)

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