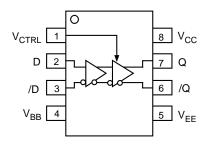
PACKAGE/ORDERING INFORMATION



8-Pin SOIC and 8-PinMSOP

Ordering Information⁽¹⁾

| Part Number | Package Type | Operating Range | Package Marking | Lead Finish | |
|-----------------------------------|-----------------|--------------------|---|-------------------|--|
| SY100EL16VSKC | K8-1 | Commercial | XLEL16VS | Sn-Pb | |
| SY100EL16VSKCTR ⁽²⁾ | K8-1 | Commercial | XLEL16VS | Sn-Pb | |
| SY100EL16VSZC | Z8-1 | Commercial | XEL16VS | Sn-Pb | |
| SY100EL16VSZCTR ⁽²⁾ | Z8-1 | Commercial | XEL16VS | Sn-Pb | |
| SY100EL16VSKI | K8-1 | Industrial | XLEL16VS | Sn-Pb | |
| SY100EL16VSKITR ⁽²⁾ | K8-1 | Industrial | XLEL16VS | Sn-Pb | |
| SY100EL16VSZI | Z8-1 | Industrial | XEL16VS | Sn-Pb | |
| SY100EL16VSZITR ⁽²⁾ | Z8-1 | Industrial | XEL16VS | Sn-Pb | |
| SY100EL16VSKG ⁽³⁾ | K8-1 | Industrial | XLEL16VS with Pb-Free bar-line indicator | Pb-Free NiPdAu | |
| SY100EL16VSKGTR ^(2, 3) | K8-1 | Industrial | XLEL16VS with Pb-Free bar-line indicator | Pb-Free NiPdAu | |
| SY100EL16VSZG ⁽³⁾ | Z8-1 | Industrial | XEL16VS with Pb-Free bar-line indicator | Pb-Free NiPdAu | |
| SY100EL16VSZGTR ^(2, 3) | Z8-1 | Industrial | XEL16VS with Pb-Free bar-line indicator | Pb-Free NiPdAu | |

Notes:

1. Contact factory for die availability. Dice are guaranteed at $T_{\rm A}$ = 25°C, DC Electricals only.

2. Tape and Reel.

3. Pb-Free package is recommended for new designs.

DC ELECTRICAL CHARACTERISTICS⁽¹⁾

VEE = VEE (Min.) to VEE (Max.); VCC = GND

| | | Т | TA = -40°C | | | TA = 0°C | | | TA = +25°C | | | TA = +85°C | | |
|--------|--|-------|------------|-----------|-------|----------|-----------|-------|------------|-----------|-------|------------|-----------|------|
| Symbol | Parameter | Min. | Тур. | Max. | Min. | Тур. | Max. | Min. | Тур. | Max. | Min. | Тур. | Max. | Unit |
| IEE | Power Supply Current | | 18 | 22 | 9 | 18 | 22 | 9 | 18 | 22 | 9 | 21 | 26 | mA |
| VBB | Output Reference Voltage | -1.38 | — | -1.26 | -1.38 | — | -1.26 | -1.38 | — | -1.26 | -1.38 | _ | -1.26 | V |
| Ін | Input HIGH -D, D Current -VCTRL | | | 150 40 | | | 150 40 | — | _ | 150 40 | _ | | 150 40 | μΑ |
| Vol | Output LOW Voltage ⁽²⁾ VCTRL = VBB | -1890 | — | -1620 | -1870 | — | -1680 | -1870 | -1775 | -1680 | -1870 | — | -1680 | mV |
| Vol | Output LOW Voltage ⁽²⁾ VCTRL = VCC | -1180 | _ | -975 | -1135 | _ | -990 | -1135 | -1065 | -990 | -1135 | _ | -990 | mV |
| Vон | Output HIGH Voltage ⁽³⁾ | -1085 | _ | -880 | -1025 | _ | -880 | -1025 | -955 | -880 | -1025 | _ | -880 | mV |

NOTES:

1. Parametric values specified at: 100EL16VS Series: -3.0V to -5.5V.

2. If VCTRL is an open circuit, use the VOH (max. & min.) and VOL (VCTRL = VBB: max only) limits.

3. VCC \leq VCTRL \leq VEE.

AC ELECTRICAL CHARACTERISTICS⁽¹⁾

VEE = VEE (Min.) to VEE (Max.); VCC = GND

| | | TA = -40°C | | | ٦ | ΓA = 0°C | ; | TA = +25°C | | | TA = +85°C | | | |
|--------------|--|------------|---------|------------|------------|----------|------------|------------|---------|------------|------------|---------|------------|------|
| Symbol | Parameter | Min. | Тур. | Max. | Min. | Тур. | Max. | Min. | Тур. | Max. | Min. | Тур. | Max. | Unit |
| tPLH tPHL | Propagation D (Diff) Delay to Output D (SE) | 175 125 | 250 | 325 425 | 175 125 | 250 | 325 375 | 175 125 | 250 | 325 375 | 205 155 | 280 | 355 405 | ps |
| tskew | Duty Cycle Skew ⁽²⁾ (Diff) | _ | 5 | — | — | 5 | 20 | — | 5 | 20 | — | 5 | 20 | ps |
| Vpp | Minimum Input Swing ⁽³⁾ | 150 | — | — | 150 | _ | — | 150 | — | — | 150 | — | _ | mV |
| VCMR | Common Mode Range ⁽⁴⁾ | -1.3 | _ | -0.4 | -1.4 | — | -0.4 | -1.4 | — | -0.4 | -1.4 | — | -0.4 | V |
| tr tf | Output Rise/Fall Times Q (20% to 80%) | | 160 | 260 | _ | 160 | 260 | — | 160 | 260 | — | 160 | 260 | ps |

NOTES:

1. Parametric values specified at: 100EL16VS Series:

-3.0V to -5.5V.

2. Duty cycle skew is the difference between a tPLH and tPHL propagation delay through a device.

3. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ~40 when output has a full swing.

4. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between VPP min. and 1V. The lower end of the CMR range varies 1:1 with VEE. The numbers in the spec table assume a nominal VEE = -3.3V. Note for PECL operation, the VCMR (min) will be fixed at 3.3V – |VCMR (min)|.

APPLICATION IMPLEMENTATION

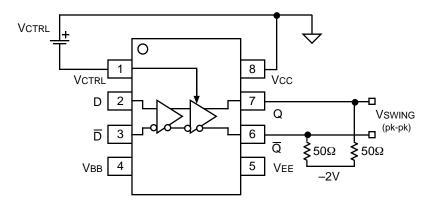


Figure 2. Voltage Source Implementation

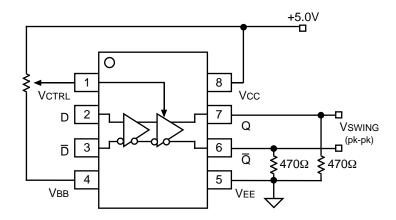
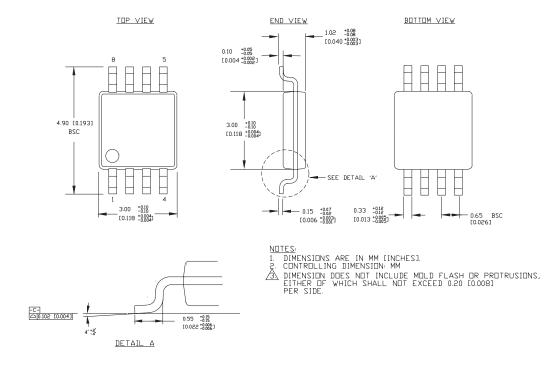
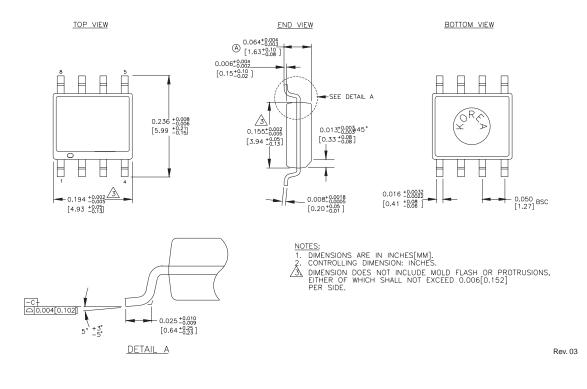


Figure 3. Alternative Implementation

8-PIN MSOP (K8-1)



8-PIN SOIC .150" WIDE (Z8-1)



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