# SUD50N04-05L

# Vishay Siliconix



Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static	<b>-,</b>			1 -74		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1	μΑ
		V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0044	0.0054	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C			0.0083	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C			0.0130	
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0055	0.0069	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	20	80		S
Dynamic <sup>b</sup>	•			•		
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		5600		pF
Output Capacitance	C <sub>oss</sub>			590		
Reversen Transfer Capacitance	C <sub>rss</sub>			365		
Total Gate Charge <sup>c</sup>	$Q_{g}$	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A		90	135	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			19		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			19		
Gate Resistance	R <sub>g</sub>			1.6		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD}$ = 20 V, $R_L$ = 0.4 $\Omega$ $I_D$ $\cong$ 50 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 $\Omega$		15	25	ns ns
Rise Time <sup>c</sup>	t <sub>r</sub>			20	30	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			65	100	
Fall Time <sup>c</sup>	t <sub>f</sub>			11	20	
Source-Drain Diode Ratings and Cha	racteristics	(T <sub>C</sub> = 25 °C) <sup>b</sup>		1	_	
Continuous Current	I <sub>S</sub>				50	٨
Pulsed Current	I <sub>SM</sub>				100	A
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		0.90	1.50	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs		30	45	ns

### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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120

100

80

60

40

20

I D - Drain Current (A)

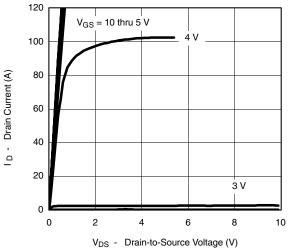


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55 °C

4.0

### TYPICAL CHARACTERISTICS 25 °C unless noted

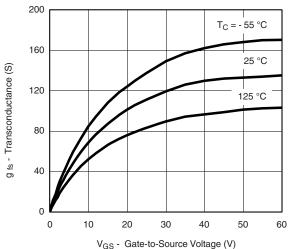


**Output Characteristics** 

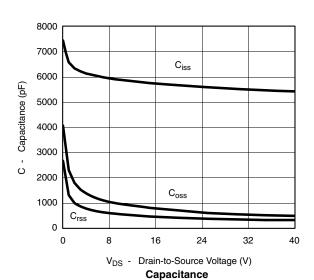


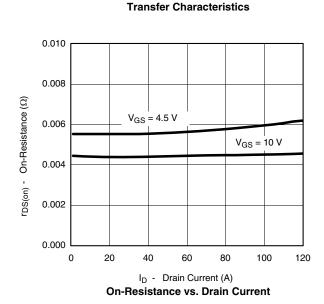
0 0.0 0.5 1.0 1.5 2.0 2.5 3.0 V<sub>GS</sub> - Gate-to-Source Voltage (V)

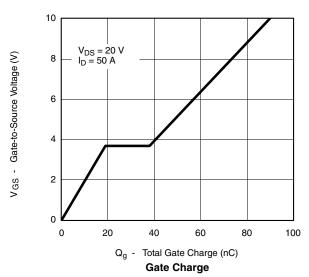
T<sub>C</sub> = 125 °C



Transconductance







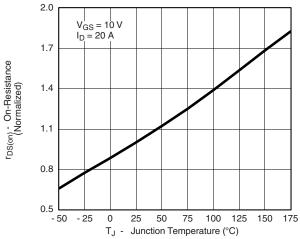
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# TYPICAL CHARACTERISTICS 25 °C unless noted



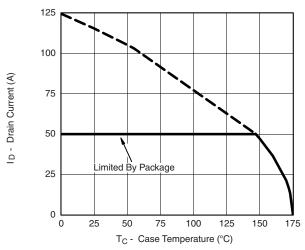


On-Resistance vs. Junction Temperature

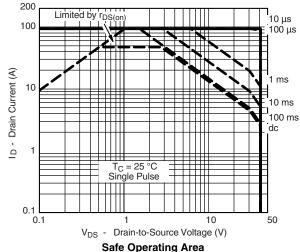
# T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C T<sub>J</sub> = 25 °C T<sub>J</sub> = 25 °C V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

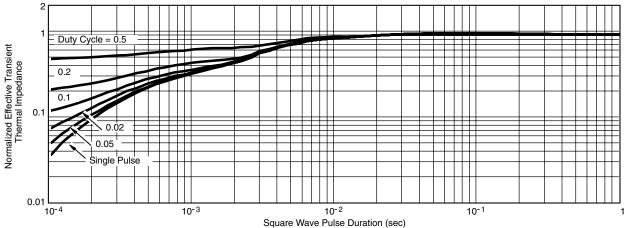
### THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Sale Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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