

**Table 1. SKY13350-385LF Signal Descriptions**

Pin #	Name	Description	Pin #	Name	Description
1	VCTL1	DC control voltage	4	OUTPUT2	RF port. Must be DC blocked.
2	OUTPUT1	RF port. Must be DC blocked.	5	VCTL2	DC control voltage
3	GND	Ground	6	INPUT	RF port. Must be DC blocked

**Table 2. SKY13350-385LF Absolute Maximum Ratings**

Parameter	Symbol	Minimum	Typical	Maximum	Units
Input power >500 MHz, 5 V >500 MHz, 3 V	P <sub>IN</sub>			+32 +31	dBm dBm
Storage temperature	T <sub>STG</sub>	−65		+150	°C
Operating temperature	T <sub>OP</sub>	−40		+105	°C

**Note:** Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**CAUTION:** Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

**Table 3. SKY13350-385LF Recommended Operating Conditions**

Parameter	Symbol	Minimum	Typical	Maximum	Units
Frequency	f	0.01		6.0	GHz
Control voltage: low high	V <sub>CTL_L</sub> V <sub>CTL_H</sub>	0 1.6		0.2 5.0	V V
Operating temperature	T <sub>OP</sub>		+25		°C

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13350-385LF are provided in Table 2. The recommended operating conditions are specified in Table 3 and electrical specifications are provided in Table 4.

Typical performance characteristics of the SKY13350-385LF are illustrated in Figures 3 through 9.

The state of the SKY13350-385LF is determined by the logic provided in Table 5.

**Table 4. SKY13350-385LF Electrical Specifications (Note 1)****( $V_{CTL} = 0\text{ V}$  and  $+2.7\text{ V}$ ,  $T_{OP} = +25\text{ }^{\circ}\text{C}$ ,  $P_{IN} = 0\text{ dBm}$ , Characteristic Impedance  $[Z_0] = 50\text{ }\Omega$ , Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Insertion loss		0.01 to 3.0 GHz 3.0 to 6.0 GHz		0.35 0.60	0.50 0.80	dB dB
Isolation		0.01 to 3.0 GHz 3.0 to 6.0 GHz	18 17	20 18		dB dB
Return loss (insertion loss state)		0.01 to 3.0 GHz 3.0 to 6.0 GHz		21 18		dB dB
Switching characteristics: Rise/fall time On/off time		10/90% or 90/10% RF 50% $V_{CTL}$ to 90/10% RF		45 45		ns ns
Video feedthrough				25		mV
0.5 dB Input Compression Point	IP0.5dB	f = 2.45 GHz: $V_{CTL} = 1.8\text{ V}$ $V_{CTL} = 2.7\text{ V}$ f = 5.8 GHz: $V_{CTL} = 1.8\text{ V}$ $V_{CTL} = 2.7\text{ V}$		+25 +30  +25 +30		dBm dBm  dBm dBm
Input IP3	IIP3	$P_{IN} = +10\text{ dBm/}$ tone, tone spacing = 1 MHz f = 2.45 GHz: $V_{CTL} = 1.8\text{ V}$ $V_{CTL} = 2.7\text{ V}$ f = 5.8 GHz: $V_{CTL} = 1.8\text{ V}$ $V_{CTL} = 2.7\text{ V}$		+32 +50  +30 +46		dBm dBm  dBm dBm
Error Vector Magnitude	EVM	802.11a, 54 Mbps, $P_{IN} = <+24\text{ dBm}$ , $V_{CTL} = 3.0\text{ V}$  802.11g, 54 Mbps, $P_{IN} = <+25.5\text{ dBm}$ , $V_{CTL} = 3.0\text{ V}$		2.5   2.5		%   %
Control voltage: Low High	$V_{CTL\_L}$ $V_{CTL\_H}$		0 1.6	 2.7	0.2 5.0	V V
Supply current	$I_{CC}$	$V_{CTL\_L} = 0\text{ V}$ $V_{CTL\_H} = 5\text{ V}$		5 5		$\mu\text{A}$ $\mu\text{A}$

**Note 1:** Performance is guaranteed only under the conditions listed in this Table.

## Typical Performance Characteristics

( $V_{CTL} = 0\text{ V}$  and  $+2.7\text{ V}$ ,  $T_{OP} = +25\text{ }^{\circ}\text{C}$ ,  $P_{IN} = 0\text{ dBm}$ , Characteristic Impedance  $[Z_0] = 50\text{ }\Omega$ , Blocking Capacitors =  $100\text{ pF}$ , Bypass Capacitors =  $33\text{ pF}$ , Unless Otherwise Noted)

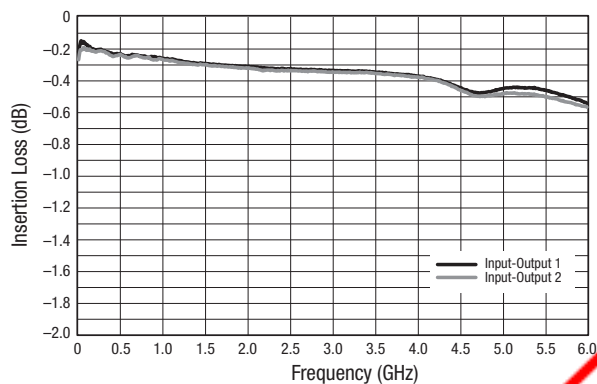


Figure 3. Typical Insertion Loss

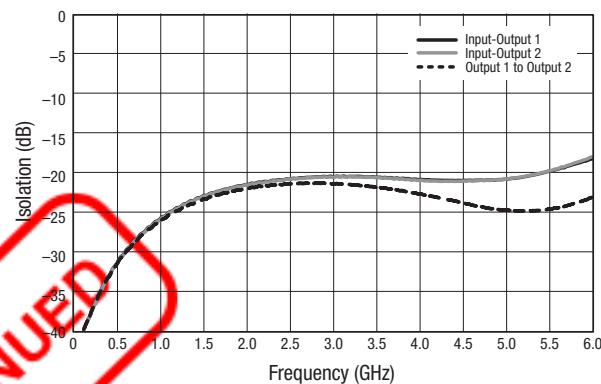


Figure 4. Typical Isolation

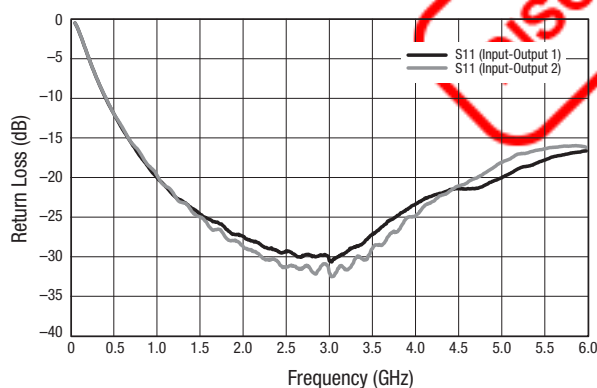


Figure 5. Typical Return Loss

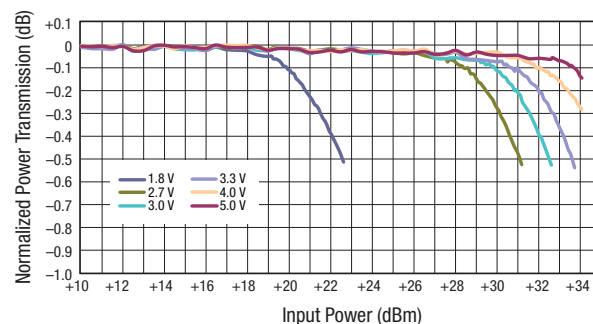


Figure 6. Compression vs Control Voltage (2450 MHz)

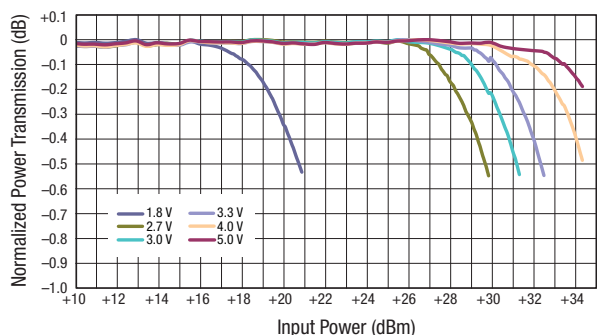


Figure 7. Compression vs Control Voltage (5.2 to 5.8 GHz)

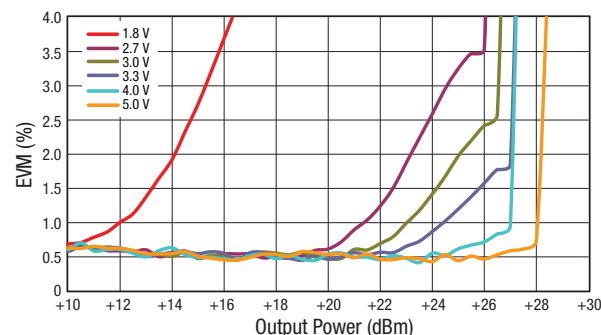


Figure 8. 802.11a EVM vs Output Power, 5.2-5.8 GHz, 54 Mbps

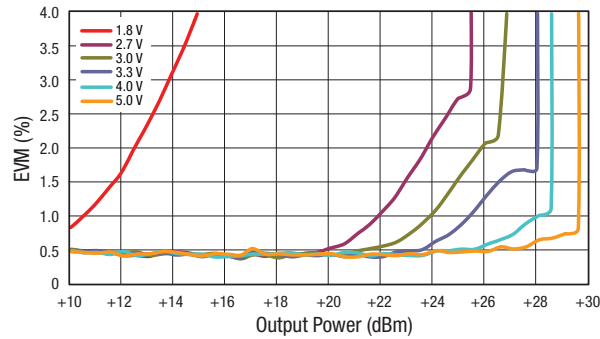


Figure 9. 802.11g EVM vs Output Power @ 2.45 GHz, 54 Mbps

Table 5. SKY13350-385LF Truth Table

VCTL1 (Pin 1)	VCTL2 (Pin 5)	INPUT to OUTPUT1 Path	INPUT to OUTPUT2 Path
1	0	Insertion loss	Isolation
0	1	Isolation	Insertion loss

**Note:** "1" = +1.6 V to +5 V. "0" = 0 V to +0.2 V. Any state other than described in this Table places the switch into an undefined state. An undefined state will not damage the device.

## Evaluation Board Description

The SKY13350-385LF Evaluation Board is used to test the performance of the SKY13350-385LF SPDT Switch. An Evaluation Board schematic diagram is provided in Figure 10. An assembly drawing for the Evaluation Board is shown in Figure 11.

## Package Dimensions

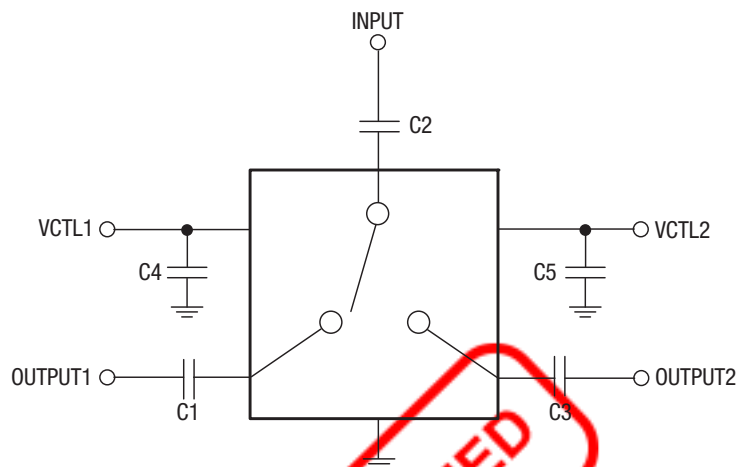
The PCB layout footprint for the SKY13350-385LF is provided in Figure 12. Typical case markings are shown in Figure 13. Package dimensions for the 6-pin MLPD are shown in Figure 14, and tape and reel dimensions are provided in Figure 15.

## Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

THE SKY13350-385LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

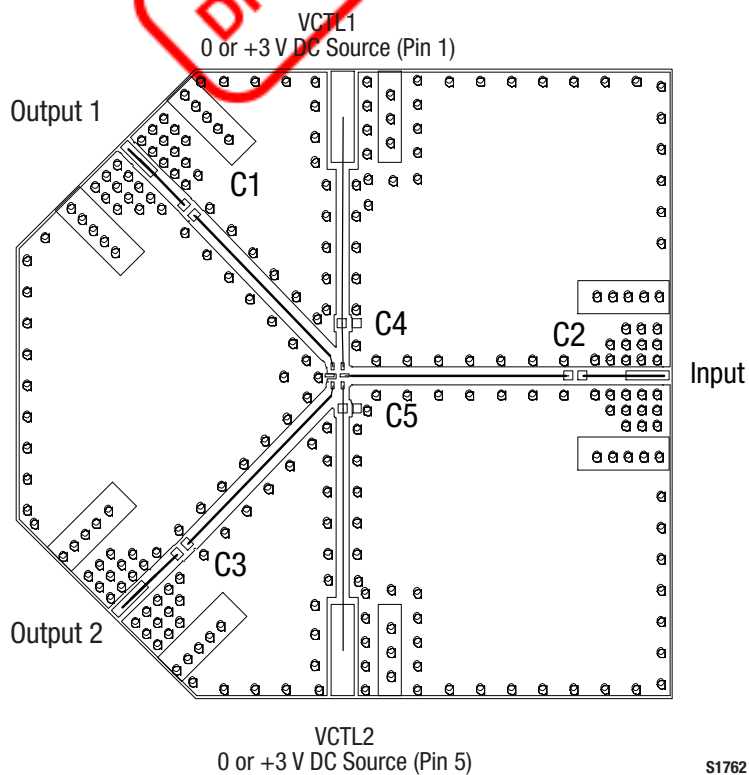
Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



For low frequency performance, larger value blocking capacitors (>1000 pF for operation at 10 MHz) are required.

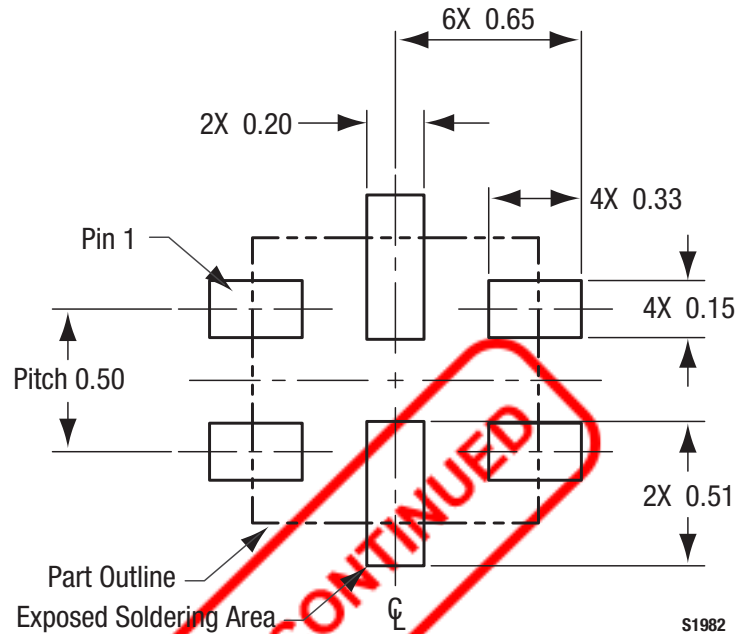
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Figure 10. SKY13350-385LF Evaluation Board Schematic

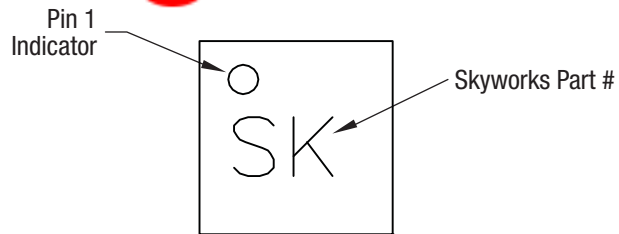


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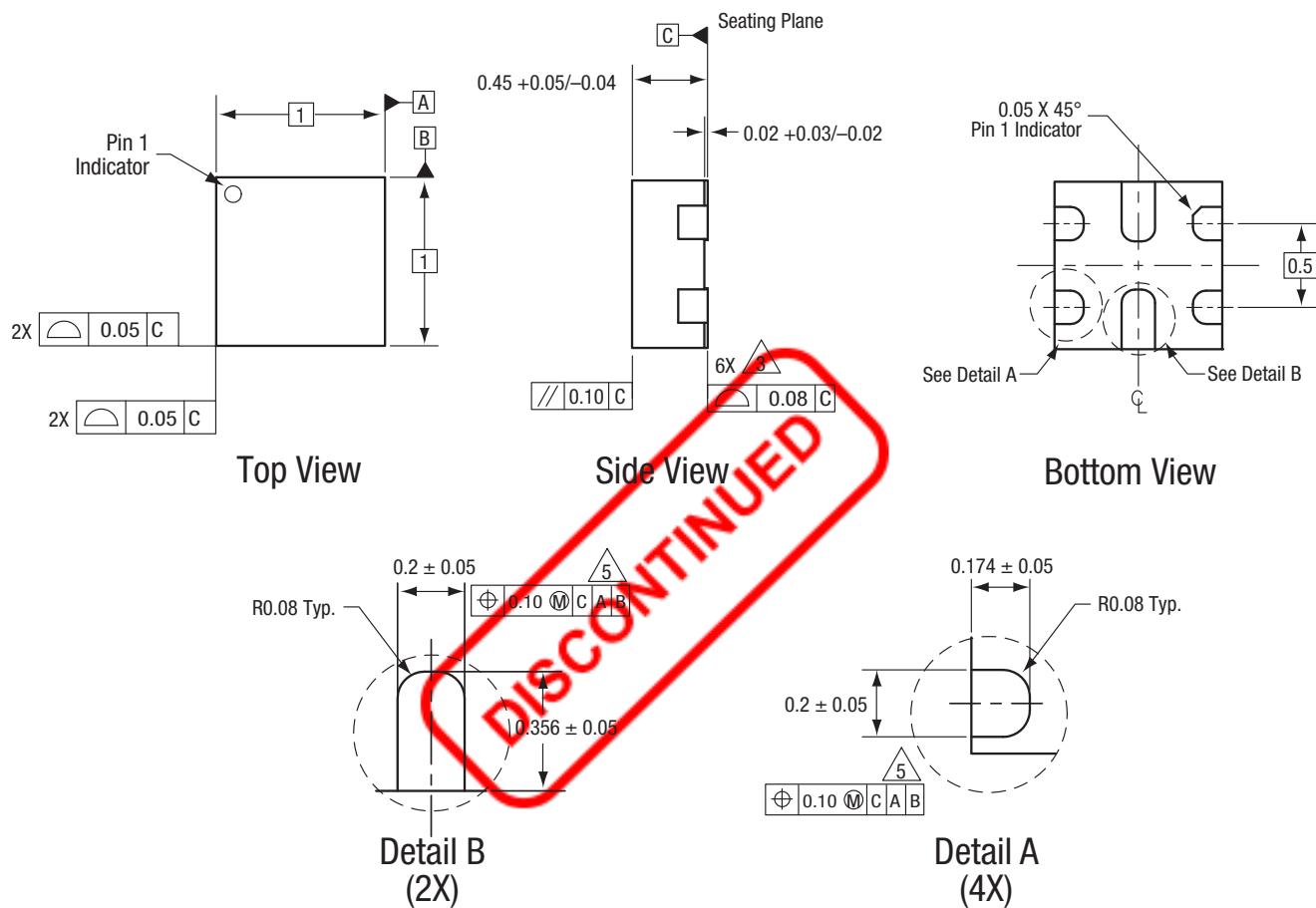
Figure 11. SKY13350-385LF Evaluation Board Assembly Diagram



**Figure 12. SKY13350-385LF PCB Layout Footprint (Top View)**



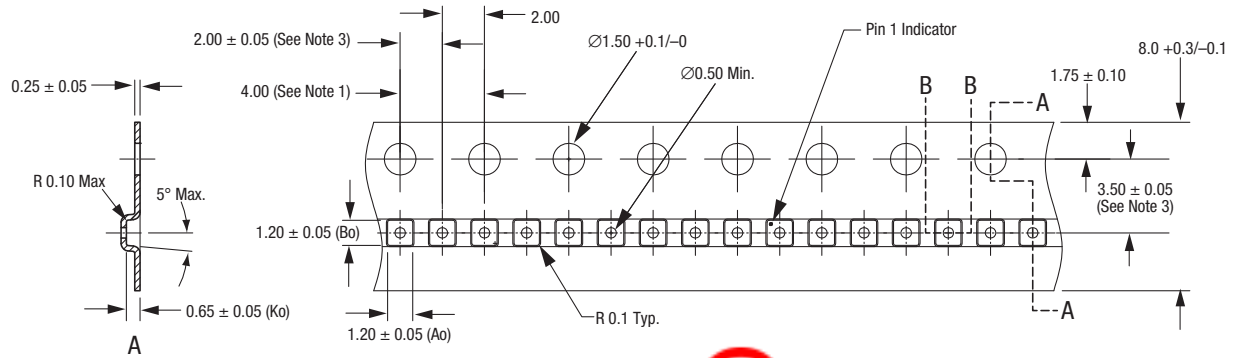
**Figure 13. Typical Case Markings (Top View)**



All measurements are in millimeters.  
Dimensioning and tolerancing according to ASME Y14.5M-1994.

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**Figure 14. SKY13350-385LF 6-Pin MLPD Package Dimensions**



## Notes:

1. 10 sprocket hole pitch cumulative tolerance ±0.2.
2. Chamber in compliance with EIA 481.
3. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.
4. Ao and Bo are measured from a plane at a distance "R" above the bottom of the pocket.
5. Carrier tape: black conductive polycarbonate.
6. Cover tape material: transparent conductive PSA.
7. All measurements are in millimeters.

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Figure 15. SKY13350-385LF Tape and Reel Dimensions



## Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKY13350-385LF SPDT Switch	SKY13350-385LF	SKY13350-385LF-EVB



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