THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET)[b, f]	t ≤ 5 s	R_{thJA}	52	65	°C/W
Maximum Junction-to-Case (Drain) (MOSFET)	Steady State	R_{thJC}	12.5	16	
Maximum Junction-to-Ambient (Schottky)[b, g]	t ≤ 5 s	R_{thJA}	62	76	
Maximum Junction-to-Case (Drain) (Schottky)	Steady State	R_{thJC}	15	18.5	

Notes:
a. Package limited.
b. Surface Mounted on 1" x 1" FR4 board.
c. t = 5 s.
d. See Solder Profile (*http://www.vishay.com/ppg?73257*). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
f. Maximum under Steady State conditions is 110 °C/W.
g. Maximum under Steady State conditions is 110 °C/W.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		27		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 3.7		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	0.6		1.5	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 30 V, V_{GS} = 0 V			1	µA
		V_{DS} = 30 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	15			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 3.3 A		0.050	0.061	Ω
		V_{GS} = 4.5 V, I_D = 3.1 A		0.059	0.072	
		V_{GS} = 2.5 V, I_D = 0.9 A		0.090	0.110	
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 3.3 A		9		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz		340		pF
Output Capacitance	C_{oss}			45		
Reverse Transfer Capacitance	C_{rss}			25		
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 4.3 A		7	11	nC
		V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 4.3 A		3.2	5	
Gate-Source Charge	Q_{gs}			0.9		
Gate-Drain Charge	Q_{gd}			0.8		
Gate Resistance	R_g	f = 1 MHz		2		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 4.3 Ω $I_D \cong$ 3.5 A, V_{GEN} = 4.5 V, R_g = 1 Ω		10	15	ns
Rise Time	t_r			10	15	
Turn-Off DelayTime	$t_{d(off)}$			15	25	
Fall Time	t_f			10	15	
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 4.3 Ω $I_D \cong$ 3.5 A, V_{GEN} = 10 V, R_g = 1 Ω		5	10	
Rise Time	t_r			12	20	
Turn-Off DelayTime	$t_{d(off)}$			15	25	
Fall Time	t_f			10	15	

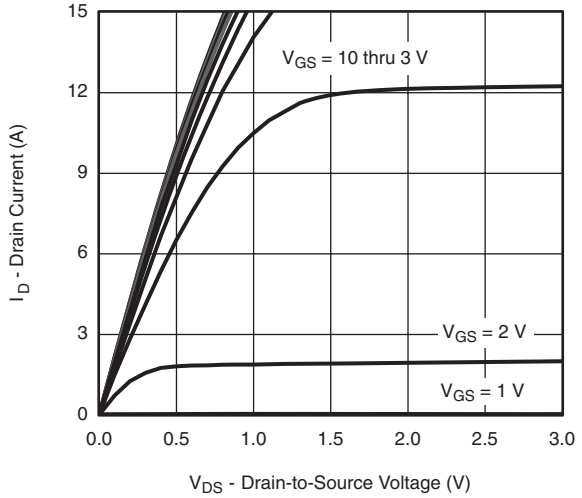
SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			4.5	A
Pulse Diode Forward Current	I_{SM}				15	
Body Diode Voltage	V_{SD}	I_S = 3.5 A, V_{GS} = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 3.5 A, dI/dt = 100 A/µs, T_J = 25 °C		12	20	ns
Body Diode Reverse Recovery Charge	Q_{rr}			6	15	nC
Reverse Recovery Fall Time	t_a			8		ns
Reverse Recovery Rise Time	t_b			4		

Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

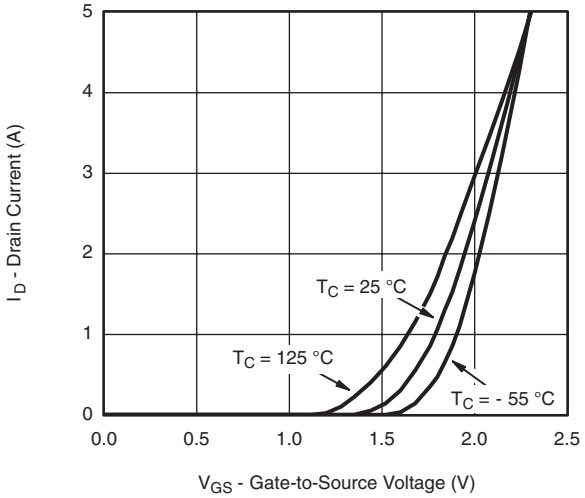
SCHOTTKY SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Forward Voltage Drop	V_F	I_F = 0.5 A		0.37	0.45	V
		I_F = 0.5 A, T_J = 125 °C		0.31	0.37	
		I_F = 1 A		0.46	0.56	
		I_F = 1 A, T_J = 125 °C		0.41	0.50	
Maximum Reverse Leakage Current	I_{rm}	V_r = 30 V		0.025	0.1	mA
		V_r = 30 V, T_J = 85 °C		0.6	6.00	
Junction Capacitance	C_T	V_r = 15 V		35		pF

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

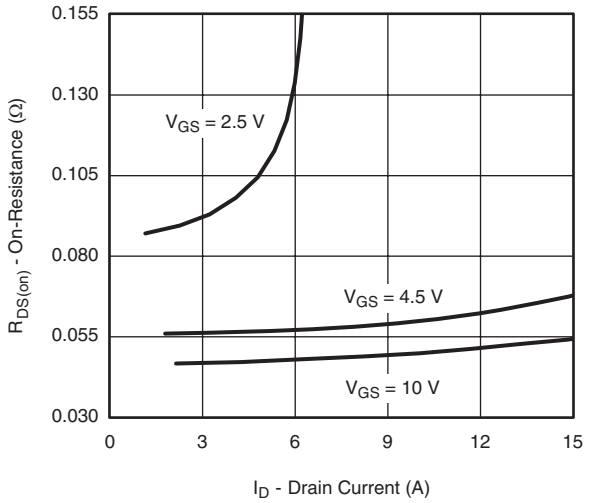
MOSFET TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



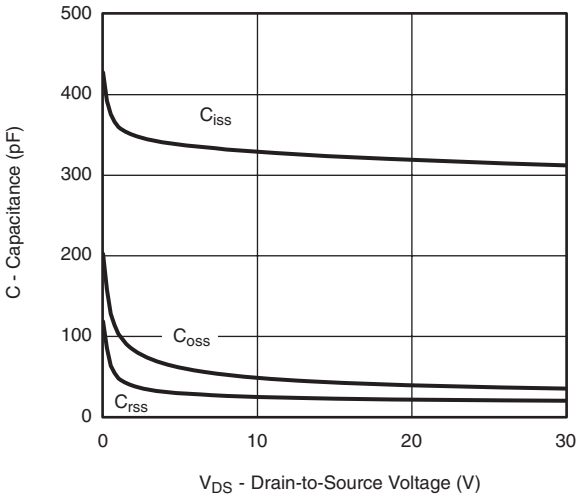
Output Characteristics



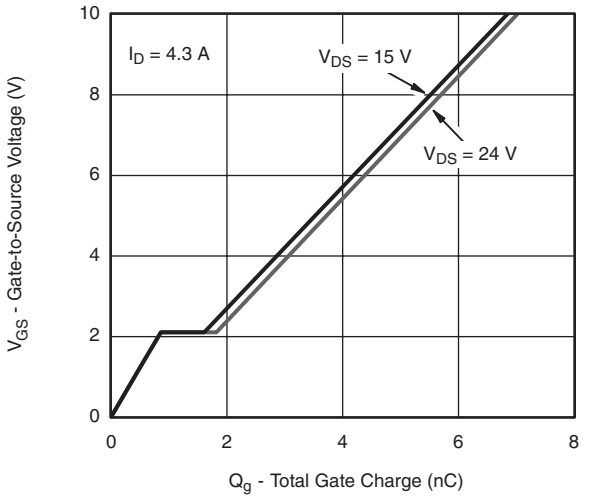
Transfer Characteristics



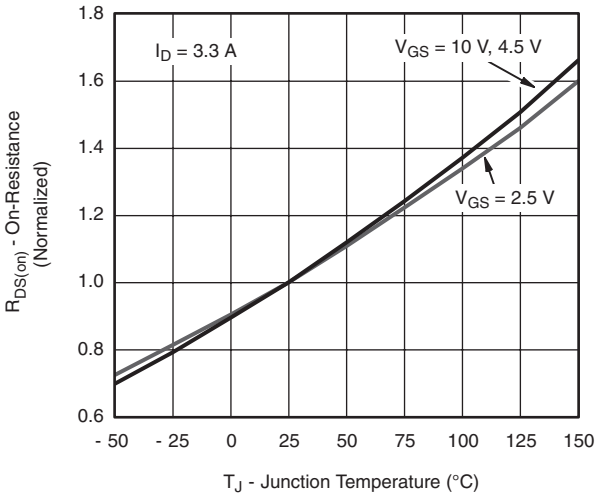
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

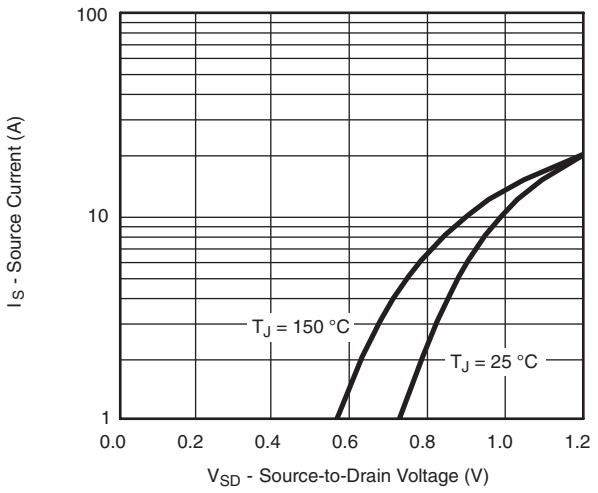


Gate Charge

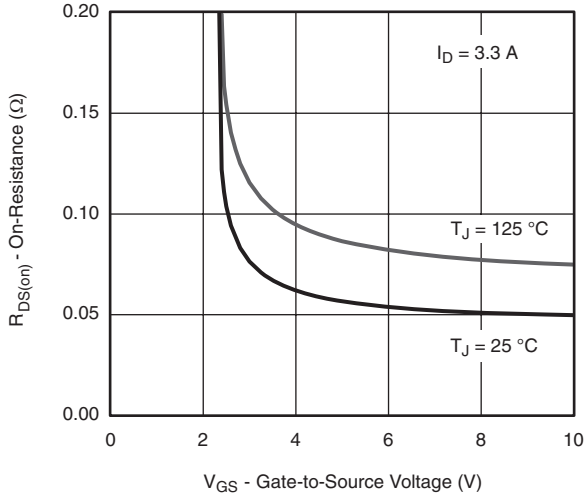


On-Resistance vs. Junction Temperature

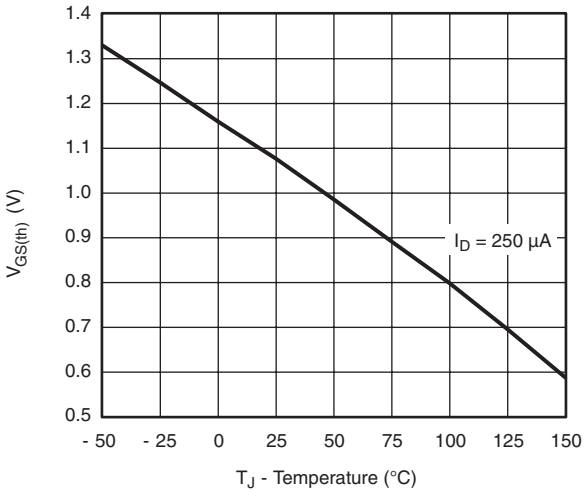
MOSFET TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



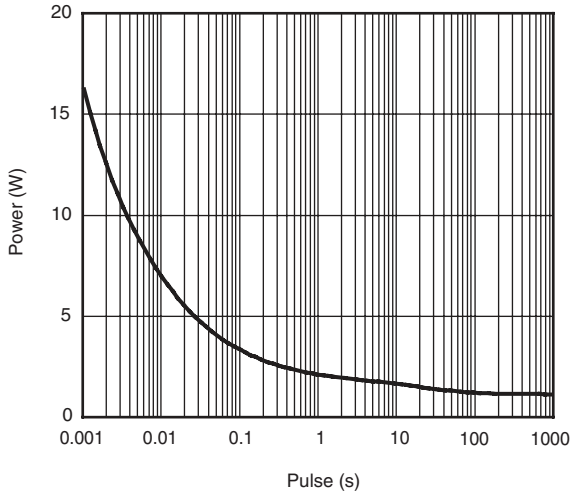
Source-Drain Diode Forward Voltage



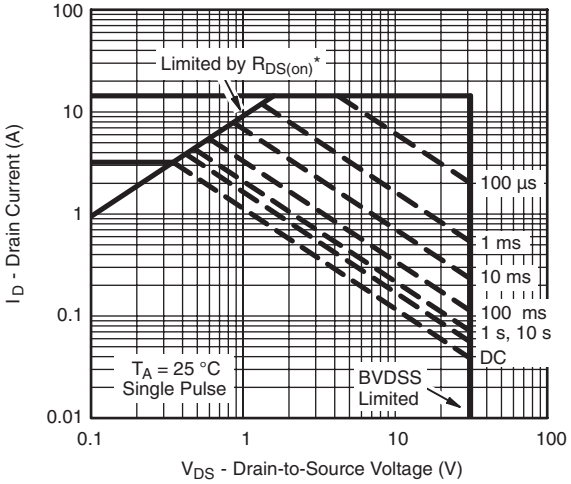
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



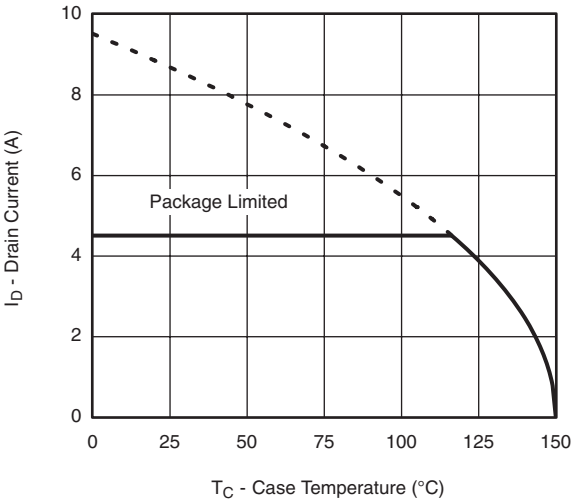
Single Pulse Power (Junction-to-Ambient)



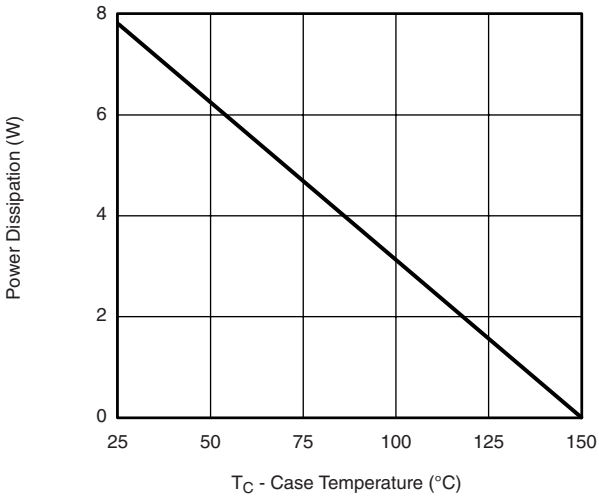
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

MOSFET TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



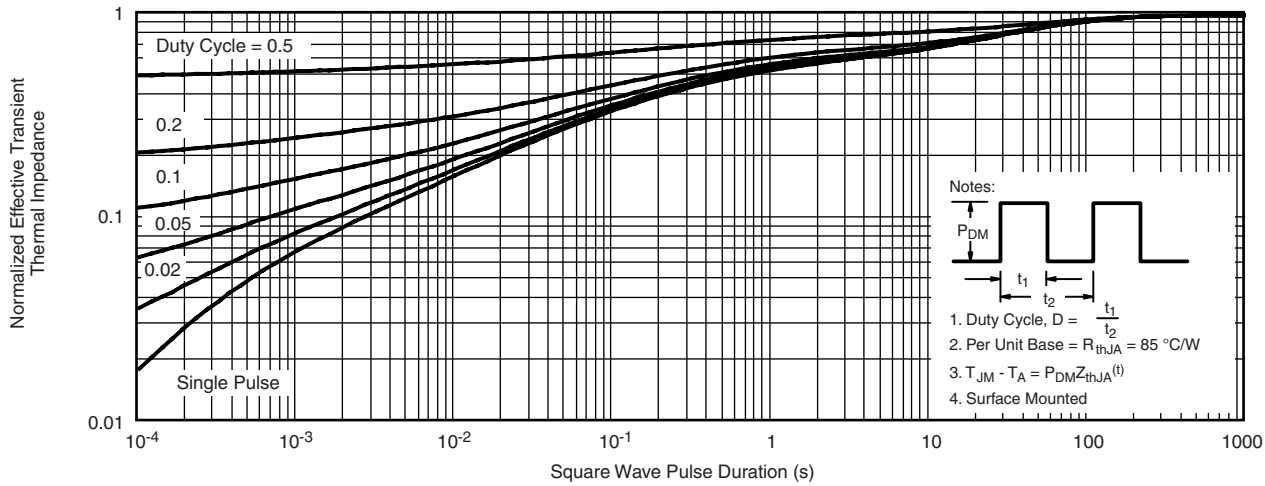
Current Derating*



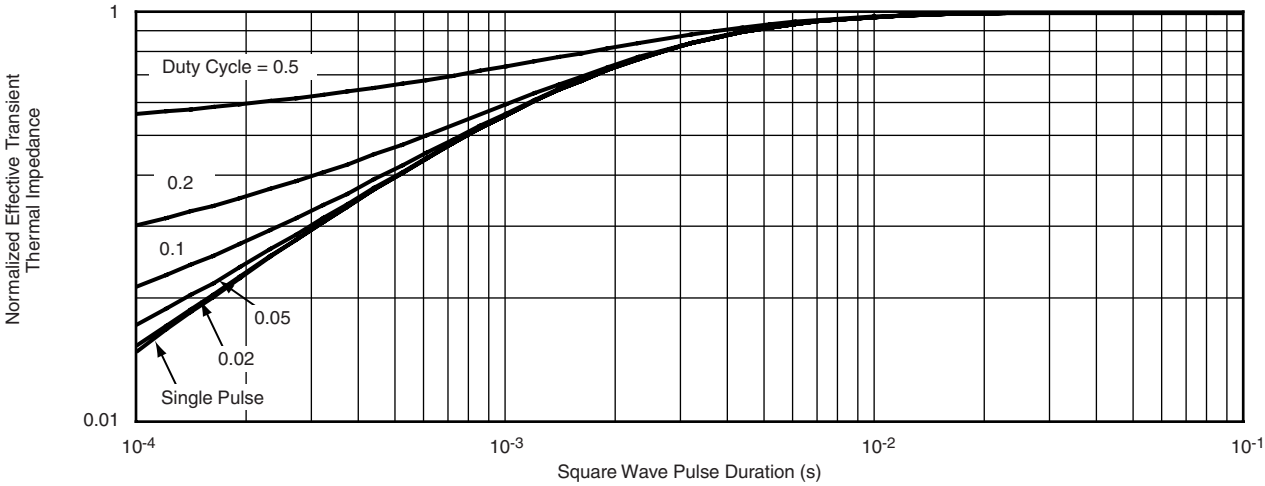
Power Derating

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

MOSFET TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted

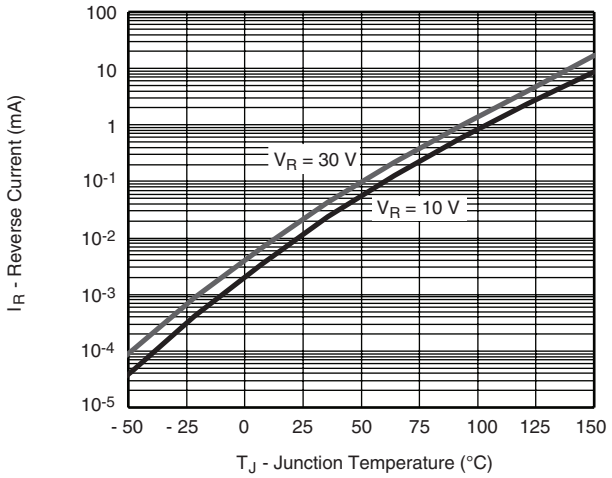


Normalized Thermal Transient Impedance, Junction-to-Ambient

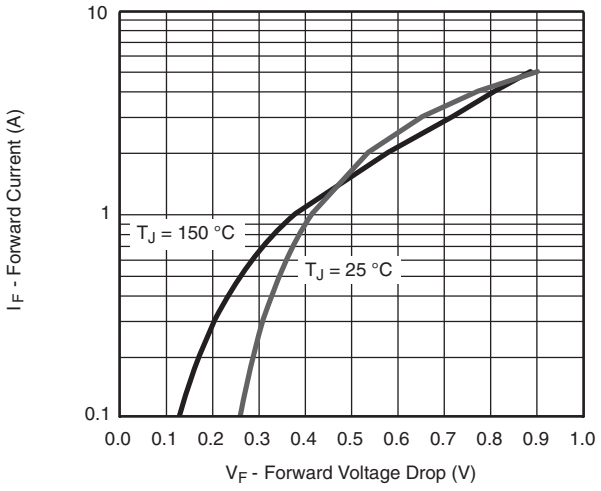


Normalized Thermal Transient Impedance, Junction-to-Case

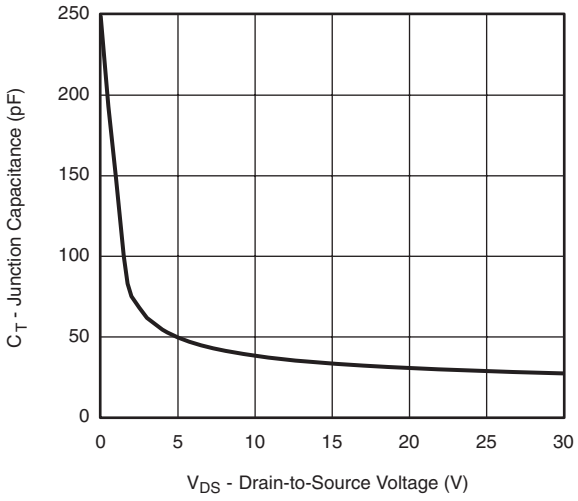
SCHOTTKY TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



Reverse Current vs. Junction Temperature

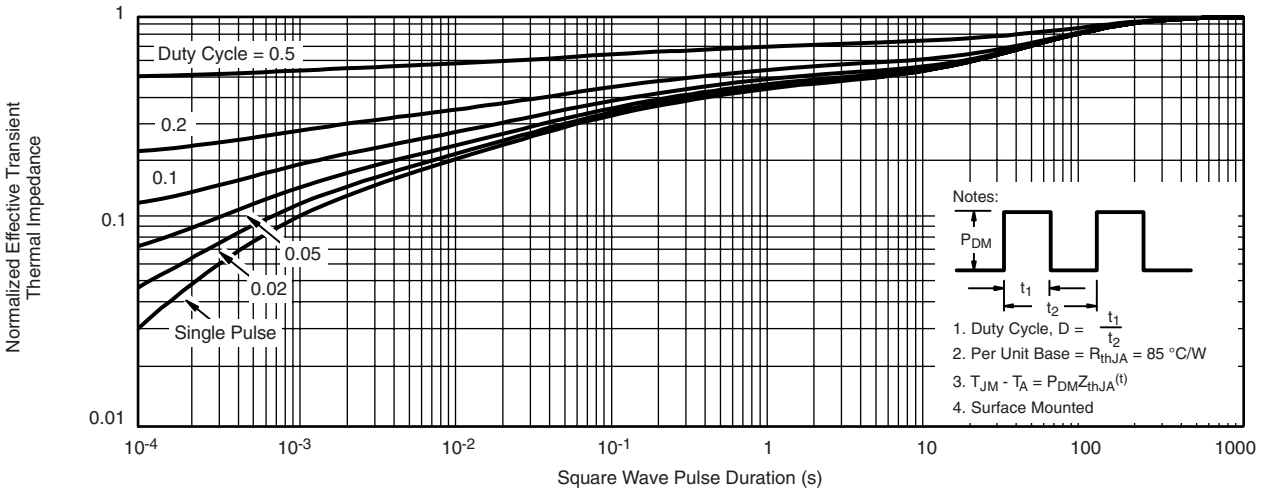


Forward Voltage Drop

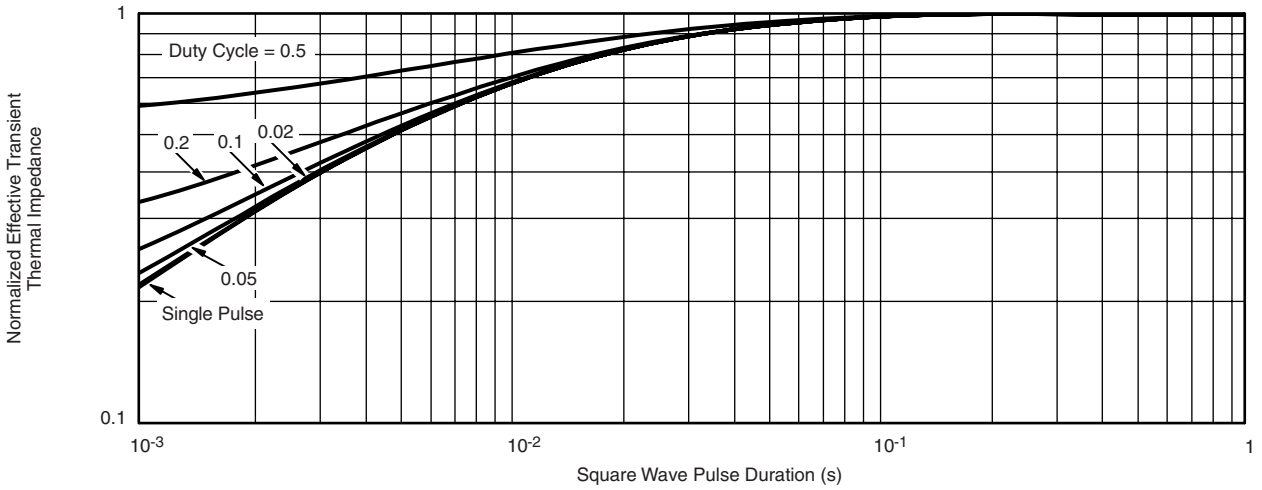


Capacitance

SCHOTTKY TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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